

(1)

Set	Items	Description
S1	53	AU='HULL J' OR AU='HULL J J' OR AU='HULL JONATHAN J' OR AU= ='HULL JONATHAN J RICOH SILICON VALLEY INC'
S2	27	AU='HART P' OR AU='HART PETER E' OR AU='HART PETER E C O R- ICOH CORP' OR AU='HART PETER E RICOH CORP CALIF RES CENT'
S3	79	S1 OR S2
S4	44	S3 AND IC=(G09G? OR G06F?)
S5	44	IDPAT (sorted in duplicate/non-duplicate order)
S6	33	IDPAT (primary/non-duplicate records only)

File 347: JAPIO Oct 1976-2003/Jan(Updated 030506)
(c) 2003 JPO & JAPIO

File 348: EUROPEAN PATENTS 1978-2003/Apr W04
(c) 2003 European Patent Office

File 349: PCT FULLTEXT 1979-2002/UB=20030508, UT=20030501
(c) 2003 WIPO/Univentio

File 350: Derwent WPIX 1963-2003/UD, UM & UP=200329
(c) 2003 Thomson Derwent

6/5/1 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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014374061 **Image available**
WPI Acc No: 2002-194764/200225
Related WPI Acc No: 2001-650622; 2002-507445
XRPX Acc No: N02-147904

Multimedia presentation information recording method used in e.g. commercial environment, involves processing multimedia presentation information and other information to generate representation of both information

Patent Assignee: RICOH KK (RICO)
Inventor: BAXTER M; GAGE P; GRAHAM J; HART P E; HULL J J ; LEE D
Number of Countries: 002 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20010020954	A1	20010913	US 99166081	A	19991117	200225 B
			US 2000521252	A	20000308	
			US 2000244771	A	20001031	
			US 2000714785	A	20001115	
			US 2000728560	A	20001130	
JP 2001256335	A	20010921	JP 2000343471	A	20001110	200225

Priority Applications (No Type Date): US 2000728560 A 20001130; US 99166081 P 19991117; US 2000521252 A 20000308; US 2000244771 P 20001031; US 2000714785 A 20001115

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20010020954	A1		23	G09G-005/00	Provisional application US 99166081

Cont of application US 2000521252
Provisional application US 2000244771
Cont of application US 2000714785

JP 2001256335 A 24 G06F-017/60

Abstract (Basic): US 20010020954 A1

NOVELTY - A multimedia presentation information and an information which are respectively received from multimedia presentation source and a presenter/attendee of a multimedia presentation are processed to generate representation of the multimedia presentation information and information from presenter/attendee. The generated representation is stored.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Computer program for recording information;
- (b) Presentation recording appliance

USE - For recording multimedia presentation information which are used in commercial environments during meetings, marketing presentations, and in academic environments for presenting information during lectures, class project presentations.

ADVANTAGE - The multimedia presentation information and information from external sources are processed and stored in a format which facilitates efficient storage and retrieval.

DESCRIPTION OF DRAWING(S) - The figure shows the simplified flowchart of a process for recording information during multimedia presentation.

pp; 23 DwgNo 3/9

Title Terms: PRESENT; INFORMATION; RECORD; METHOD; COMMERCIAL; ENVIRONMENT; PROCESS; PRESENT; INFORMATION; INFORMATION; GENERATE; REPRESENT; INFORMATION

Derwent Class: P85; T01; W04

International Patent Class (Main): G06F-017/60 ; G09G-005/00

International Patent Class (Additional): G06F-019/00 ; H04N-005/76;

H04N-005/765; H04N-005/91; H04N-007/18

File Segment: EPI; EngPI

6/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014143280 **Image available**
WPI Acc No: 2001-627491/200173
XRPX Acc No: N01-467858

Interfacing method with a communication station using a personal digital assistant (PDA) using semi-structured data from PDA which is parsed to identify the type of data so it may be sent to an indicated destination

Patent Assignee: RICOH KK (RICO)
Inventor: HULL J J ; PHILLIPS D; WOFF G J
Number of Countries: 026 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1089539	A1	20010404	EP 2000123383	A	20001031	200173 B
JP 2001268111	A	20010928	JP 2000379160	A	20001213	200173

Priority Applications (No Type Date): US 2000531240 A 20000321

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
EP 1089539	A1	E 18	H04N-001/00	
Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT				
LI LT LU LV MC MK NL PT RO SE SI				
JP 2001268111	A	15	H04L-012/54	

Abstract (Basic): EP 1089539 A1

NOVELTY - The method involves receiving semi-structured data from a personal digital assistant (PDA) in a format native to the PDA. The semi-structured data is parsed to identify a type of the semi-structured data. A job is sent to a destination indicated by the semi-structured data, if the semi-structured data is destination data.

The PDA wirelessly transmits the semi-structured data, in a standard PDA format to the communication station. The PDA is physically coupled to the communication station when sending the semi-structured data.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for an appts. for sending data from a communication station, for a method of sending data from a communication station and for a system.

USE - For using personal digital assistant to interface with a communication station.

ADVANTAGE - Allows faxes and emails to be sent from personal digital assistant

DESCRIPTION OF DRAWING(S) - The figure shows a network.

pp; 18 DwgNo 1/4

Title Terms: INTERFACE; METHOD; COMMUNICATE; STATION; PERSON; DIGITAL; ASSIST; SEMI; STRUCTURE; DATA; IDENTIFY; TYPE; DATA; SO; SEND; INDICATE; DESTINATION

Derwent Class: T01; W01

International Patent Class (Main): H04L-012/54; H04N-001/00

International Patent Class (Additional): G06F-013/00 ; H04L-012/58; H04M-011/00

File Segment: EPI

6/5/3 (Item 3 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2003 Thomson Derwent. All rts. reserv.

013861900 **Image available**
WPI Acc No: 2001-346112/200137
XRPX Acc No: N01-250888

Portable apparatus e.g. cell phone, has processor which controls transceiver to receive document unconsciously from device and to transfer document, portion of which is downloaded to another device

Patent Assignee: RICOH KK (RICO)

Inventor: HULL J J

Number of Countries: 026 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1098252	A1	20010509	EP 2000116163	A	20000801	200137 B
JP 2001216245	A	20010810	JP 2000322132	A	20001023	200154

Priority Applications (No Type Date): US 99428129 A 19991026

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 1098252 A1 E 13 G06F-015/02

Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT

LI LT LU LV MC MK NL PT RO SE SI

JP 2001216245 A 10 G06F-013/00

Abstract (Basic): EP 1098252 A1

NOVELTY - Transceiver (202B) receives document subjected to unconscious capture by a device and transmits it to another device, so that communication of document takes place between the devices. Memory (201D) coupled to transceiver stores the document. Processor (201C) controls transceiver to receive document unconsciously from a device and to transfer the document, portion of which is downloaded to another device.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Data transfer method;
- (b) Data transcribing system;
- (c) Document transferring method;
- (d) Portable device synchronizing method

USE - E.g. cell phone for unconscious transfer of documents between office equipment such as photocopier, facsimile, printers and multifunction machines and document databases.

ADVANTAGE - Enables capturing documents unconsciously, with a portable electronic device and transferring document using portable device.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of system having unconscious capture device.

Processor (201C)

Memory (201D)

Transceiver (202B)

pp; 13 DwgNo 2/3

Title Terms: PORTABLE; APPARATUS; CELL; TELEPHONE; PROCESSOR; CONTROL; TRANSCEIVER; RECEIVE; DOCUMENT; DEVICE; TRANSFER; DOCUMENT; PORTION; DEVICE

Derwent Class: T01

International Patent Class (Main): G06F-013/00 ; G06F-015/02

International Patent Class (Additional): H04B-007/26; H04L-009/08

File Segment: EPI

6/5/4 (Item 4 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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013855615 **Image available**

WPI Acc No: 2001-339828/200136

XRPX Acc No: N01-245812

Document search system operation procedure involves generating reference document comprising text and image zones corresponding to users input document, based on which target document is searched in database

Patent Assignee: RICOH KK (RICO)

Inventor: CULLEN J F; HULL J J

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2001092852	A	20010406	JP 2000119143	A	20000420	200136 B
US 6397213	B1	20020528	US 99311200	A	19990512	200243

Priority Applications (No Type Date): US 99311200 A 19990512

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2001092852	A		19	G06F-017/30	
US 6397213	B1			G06F-017/30	

Abstract (Basic): JP 2001092852 A

NOVELTY - The procedure involves resolving the document input by the user into text and image zones, A reference document (214) corresponding to the input document is generated. The target document is searched in the database (222), based on text or images in the zones of reference document.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Search key generation procedure;
- (b) Document control system;
- (c) Document reference system;
- (d) Document archive system operation procedure;
- (e) Document database perusal;
- (f) Memory medium

USE - For operating document search system used in internet applications.

ADVANTAGE - As the target document is searched based on texts and images in zones of reference document generated corresponding to users input document, search operation is performed easily and efficiently.

DESCRIPTION OF DRAWING(S) - The figure shows the explanatory drawing of document. (Drawing includes non-English language text).

Reference document (214)

Database (222)

pp; 19 DwgNo 1/16

Title Terms: DOCUMENT; SEARCH; SYSTEM; OPERATE; PROCEDURE; GENERATE;

REFERENCE; DOCUMENT; COMPRISE; TEXT; IMAGE; ZONE; CORRESPOND; USER; INPUT

; DOCUMENT; BASED; TARGET; DOCUMENT; SEARCH; DATABASE

Derwent Class: T01

International Patent Class (Main): G06F-017/30

International Patent Class (Additional): G06F-017/00 ; G06T-001/00

File Segment: EPI

6/5/5 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013632326 **Image available**

WPI Acc No: 2001-116534/200113

XRPX Acc No: N01-085995

Electronic-document management method involves acquiring blank portions of electronic document and assigning it with label for document management

Patent Assignee: RICOH KK (RICO); HULL J J (HULL-I); LEE D (LEED-I);

PIERSOL K (PIER-I); WOLFF G J (WOLF-I)

Inventor: HULL J J ; LEE D; PIERSOL K; WOLFF G J

Number of Countries: 002 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2000339349	A	20001208	JP 2000123640	A	20000425	200113 B
US 20020138541	A1	20020926	US 99322324	A	19990528	200265
US 6526398	B2	20030225	US 99322324	A	19990528	200323

Priority Applications (No Type Date): US 99322324 A 19990528

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2000339349	A		11	G06F-017/30	
US 20020138541	A1			G06F-017/00	
US 6526398	B2			G06F-017/30	

Abstract (Basic): JP 200035349 A

NOVELTY - The blank portion of the electronic document is acquired and assigned with a label (220). The electronic document is managed based on blank portion.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Electronic document management apparatus;
- (b) Memory medium

USE - For conferences.

ADVANTAGE - Enables natural and rational searching of documents, since the documents are managed based on the blank portions of the documents.

DESCRIPTION OF DRAWING(S) - The figure shows the graphical interface displaying blank portion of electronic document.

Label (220)

pp; 11 DwgNo 2/5

Title Terms: ELECTRONIC; DOCUMENT; MANAGEMENT; METHOD; ACQUIRE; BLANK;

PORTION; ELECTRONIC; DOCUMENT; ASSIGN; LABEL; DOCUMENT; MANAGEMENT

Derwent Class: T01

International Patent Class (Main): G06F-017/00 ; G06F-017/30

File Segment: EPI

6/5/6 (Item 6 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013630264 **Image available**

WPI Acc No: 2001-114472/200113

Related WPI Acc No: 1999-257175

XRFX Acc No: N01-084161

Document image retrieval method for computer system, in which control mechanism automatically archives document image in response to document being produced by peripheral device e.g. printer

Patent Assignee: RICOH KK (RICO)

Inventor: CULLEN J; HULL J J ; NISHIMOTO M; PEAIRS M; SUZUKI K

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2352071	A	20010117	GB 9820189	A	19980916	200113 B
			GB 200025551	A	20001018	

Priority Applications (No Type Date): US 97969640 A 19971113

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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GB 2352071	A	32	G06F-017/30	Derived from application	GB 9820189
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Abstract (Basic): GB 2352071 A

NOVELTY - The method for retrieving document images involves selecting a document from an index which contains a list of documents archived in computer memory of a workstation, and a remote storage facility, determining the location of the document and transmitting the document to the workstation.

DETAILED DESCRIPTION - The document image retrieval method involves using a document image storage system (100) which includes a control mechanism that automatically archives a document image in response to a document being produced by a peripheral device e.g. a copier or printer (130,145,150). The system includes a memory (120), which archives document images, and a remote storage facility coupled to memory. The remote storage facility archives document images received from the memory, and may be shared between a number of memory units. INDEPENDENT CLAIMS are included for; a computer system with a workstation coupled to memory, and a remote storage facility; a workstation for use in a computer system.

USE - Providing remote storage and retrieval facilities for archiving electronic documents.

ADVANTAGE - Provides method for extending the storage capacity in a

computer system to facilitate archiving documents.

DESCRIPTION OF DRAWING(S) - The drawing shows an embodiment of an office machine network according to the invention.

Document image storage system (100)

Client system (110)

Digital copier (130)

Print server (140)

Printer (145)

Fax machine (150)

pp; 32 DwgNo 1/9

Title Terms: DOCUMENT; IMAGE; RETRIEVAL; METHOD; COMPUTER; SYSTEM; CONTROL;
MECHANISM; AUTOMATIC; ARCHIVE; DOCUMENT; IMAGE; RESPOND; DOCUMENT;
PRODUCE; PERIPHERAL; DEVICE; PRINT

Derwent Class: T01

International Patent Class (Main): G06F-017/30

File Segment: EPI

6/5/7 (Item 7 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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013510673 **Image available**

WPI Acc No: 2000-682619/200067

XRPX Acc No: N00-505407

Document matching procedure involves generating bit distribution of inquiry document, and comparing bit distribution with that of first few sentences of document in database

Patent Assignee: RICOH KK (RICO)

Inventor: HULL J J ; LEE D; HULL J

Number of Countries: 002 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2000285139	A	20001013	JP 99307938	A	19991029	200067 B
US 6363381	B1	20020326	US 98186041	A	19981103	200226
US 20020116379	A1	20020822	US 98186041	A	19981103	200258
			US 200258169	A	20020125	

Priority Applications (No Type Date): US 98186041 A 19981103; US 200258169 A 20020125

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2000285139	A		20	G06F-017/30	
US 6363381	B1			G06F-017/30	
US 20020116379	A1			G06F-007/00	Cont of application US 98186041

Abstract (Basic): JP 2000285139 A

NOVELTY - The bit distribution of an inquiry document (12) is generated to encode each pixel line, and then compared with the bit distribution about the first few sentences of a document in a database (14) to determine if both documents are in accord.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) a descriptor generation procedure;
- (b) a data processing system;
- (c) and a memory medium.

USE - For data processing system.

ADVANTAGE - Enables highly precise document matching with few amount of calculations and without need to expand or compress document image.

DESCRIPTION OF DRAWING(S) - The figure is the explanatory drawing of a round matching level.

Inquiry document (12)

Database (14)

pp; 20 DwgNo 2/15

Title Terms: DOCUMENT; MATCH; PROCEDURE; GENERATE; BIT; DISTRIBUTE; ENQUIRY
; DOCUMENT; COMPARE; BIT; DISTRIBUTE; FIRST; SENTENCE; DOCUMENT; DATABASE

Derwent Class: T01
International Patent Class (Main): G06F-007/00 ; G06F-017/30
International Patent Class (Additional): G06T-007/00
File Segment: EPI

6/5/8 (Item 8 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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013310626 **Image available**
WPI Acc No: 2000-482563/200042
XRPX Acc No: N00-358807

Virtual environment provision method for simulating virtual assembly of finished product, involves enabling simulation to modified and causing corresponding model to automatically include change of virtual part
Patent Assignee: NAT INST STANDARDS & TECHNOLOGY (NAST-N); UNIV WASHINGTON STATE RES FOUND (UNIW)
Inventor: CHANDRANA H; CONNACHER H I; HART P F; JAYARAM S; JAYARAM U; LYONS K; TIRUMALI H; WANG Y; CONNACHER H; **HART P**
Number of Countries: 090 Number of Patents: 003
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200038117	A1	20000629	WO 99US30753	A	19991223	200042 B
AU 200023823	A	20000712	AU 200023823	A	19991223	200048
US 20020123812	A1	20020905	US 98113629	A	19981223	200260
			WO 99US30753	A	19991223	
			US 2001888055	A	20010621	

Priority Applications (No Type Date): US 98113629 P 19981223; US 2001888055
A 20010621

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 200038117	A1	E	86	G06T-017/40	
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Designated States (National): AE AL AM AT AU AZ BA BB BG BR BY CA CH CN
CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE
SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW NL OA PT SD SE SL SZ TZ UG ZW

AU 200023823	A	G06T-017/40	Based on patent WO 200038117
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US 20020123812	A1	G06F-019/00	Provisional application US 98113629
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Cont of application WO 99US30753

Abstract (Basic): WO 200038117 A1

NOVELTY - The method involves creating a model in a design environment for each part. Each model is translated into a virtual part in virtual environment (102). Each virtual part is positioned in virtual environment by simulating for arranging the parts into assembly. The simulation is modified by carrying out other simulation and causing the corresponding model to contain the change of virtual part on modification.

DETAILED DESCRIPTION - The virtual environment is integrated with a design environment. Each model has a geometry that corresponds to a part.

USE - For simulating virtual assembly of finished product using CAD/CAM system in field of engineering design.

ADVANTAGE - Enhances viability and functionality of the applications of virtual reality technology in design and manufacturing. Increases interaction between different groups in an enterprise and helps to reduce the total time it takes for a new product to move from an initial concept to final manufacture. Helps engineer to understand the functionality, scale, clearances, ergonomics and aesthetics of a new design. Enables users to evaluate analyze and plan the assembly/disassembly of parts for mechanical systems.

DESCRIPTION OF DRAWING(S) - The figure shows the schematic overview

of usage scenario for the virtual assembly design environment.

Virtual environment (102)

pp; 86 DwgNo 1/55

Title Terms: VIRTUAL; ENVIRONMENT; PROVISION; METHOD; SIMULATE; VIRTUAL;
ASSEMBLE; FINISH; PRODUCT; ENABLE; SIMULATE; MODIFIED; CAUSE; CORRESPOND;
MODEL; AUTOMATIC; CHANGE; VIRTUAL; PART

Derwent Class: P82; T01

International Patent Class (Main): G06F-019/00 ; G06T-017/40

International Patent Class (Additional): G03B-021/60; G06K-009/00;

G06T-013/00; G06T-015/00; G06T-015/70; G06T-017/00

File Segment: EPI; EngPI

6/5/9 (Item 9 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013183082 **Image available**

WPI Acc No: 2000-354955/200031

XRPX Acc No: N00-266059

**Automatic electronic document classification involves storing document
according to existing document hierarchy, based on document
classification depending on analyzed character and document image
attribute**

Patent Assignee: RICOH KK (RICO); CULLEN J F (CULL-I); HULL J J (HULL-I);
PEAIRS M (PEAI-I)

Inventor: CULLEN J F; HULL J J ; PEAIRS M

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2000112993	A	20000421	JP 99197628	A	19990712	200031 B
US 20010042085	A1	20011115	US 98163848	A	19980930	200172

Priority Applications (No Type Date): US 98163848 A 19980930

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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JP 2000112993	A		19	G06F-017/30	
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US 20010042085	A1			G06F-015/00	
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Abstract (Basic): JP 2000112993 A

NOVELTY - The character attribute and image attribute of an electronic document are analyzed, based on which the electronic document is classified. The electronic document is then stored according to the existing document hierarchy, based on the classification.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

(a) program of document classification method;

(b) document classification device

USE - For automatic classification of character and image document in electronic documents used in word processor application.

ADVANTAGE - Reduces labor and time for classification of electronic document for storing in memory. Existing document hierarchy can be copied by mirroring and hence the hierarchy can be produced easily.

pp; 19 DwgNo 1/10

Title Terms: AUTOMATIC; ELECTRONIC; DOCUMENT; CLASSIFY; STORAGE; DOCUMENT;
ACCORD; EXIST; DOCUMENT; HIERARCHY; BASED; DOCUMENT; CLASSIFY; DEPEND;
CHARACTER; DOCUMENT; IMAGE; ATTRIBUTE

Derwent Class: T01

International Patent Class (Main): G06F-015/00 ; G06F-017/30

International Patent Class (Additional): G06F-012/00

File Segment: EPI

6/5/10 (Item 10 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013149068 **Image available**
WPI Acc No: 2000-320940/200028
Related WPI Acc No: 1999-193216
XRPX Acc No: N00-240920

Apparatus for document archiving in a computer system involves capturing data transferred to a peripheral device and automatically archiving it

Patent Assignee: RICOH KK (RICO)
Inventor: CULLEN J; **HULL J J** ; PEAIRS M; SUZUKI K
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2343974	A	20000524	GB 9819374	A	19980904	200028 B
			GB 20003510	A	20000215	
GB 2343974	B	20000726	GB 9819374	A	19980904	200037
			GB 20003510	A	20000215	

Priority Applications (No Type Date): US 97938137 A 19970926

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
GB 2343974	A	29	G06F-017/30	Derived from application GB 9819374
GB 2343974	B		G06F-017/30	Derived from application GB 9819374

Abstract (Basic): GB 2343974 A

NOVELTY - On receiving a command from the user a document is transmitted to a peripheral device e.g. hard drive, disk drive etc (102). When the command is executed the document is archived in the computer system memory (103) with the file name being used to provided a link between the archived and current version.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is included for a method of processing documents in a computer system.

USE - Apparatus for document archiving in a computer system.

ADVANTAGE - The document is automatically archived without the user needing to input extra commands.

DESCRIPTION OF DRAWING(S) - The drawing shows a flow chart of the archiving system.

Capture data to be archived (102)

Archive data (103)

pp; 29 DwgNo 1/4

Title Terms: APPARATUS; DOCUMENT; COMPUTER; SYSTEM; CAPTURE; DATA; TRANSFER
; PERIPHERAL; DEVICE; AUTOMATIC

Derwent Class: T01

International Patent Class (Main): **G06F-017/30**

International Patent Class (Additional): **G06F-003/06**

File Segment: EPI

6/5/11 (Item 11 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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012481929 **Image available**
WPI Acc No: 1999-288037/199927
XRPX Acc No: N99-215097

Processor pipeline branch instructions processing

Patent Assignee: IDEA CORP (IDEA-N)

Inventor: CORWIN M P; FIELDEN K; **HULL J** ; MORRIS D; MULDER H; SHARANGPANI
H; YEH T

Number of Countries: 083 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9919792	A1	19990422	WO 98US21374	A	19981009	199927 B
AU 9910761	A	19990503	AU 9910761	A	19981009	199937
EP 1023659	A1	20000802	EP 98953362	A	19981009	200038
			WO 98US21374	A	19981009	
US 6237077	B1	20010522	US 97949277	A	19971013	200130

Priority Applications (No Type Date): US 97949277 A 19971020

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 9919792 A1 E 33 G06F-009/30

Designated States (National): AL AM AT AZ BA BB BG BR BY CA CH CN CU CZ
DE DK EE ES FI GB GD GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM
TR TT UA UG US UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW NL OA PT SD SE SZ UG ZW

AU 9910761 A G06F-009/30 Based on patent WO 9919792

EP 1023659 A1 E G06F-009/30 Based on patent WO 9919792

Designated States (Regional): DE GB

US 6237077 B1 G06F-009/38

Abstract (Basic): WO 9919792 A1

NOVELTY - Method is for a processor pipeline with an instruction buffer transferring instructions from N buffer slots having an execution order 0 to N-1, to execution units. It consists in receiving an N-tuple of instructions, including P branch instructions characterized by an assignment template, loading them into the N buffer slots of the instruction buffer, P branch instructions being loaded into slots N-1 to N-P, and transferring the N-tuple of instructions to the execution units as indicated by the assignment template. The first taken branch is determined among the P branch instructions and retirement of any branch instructions in the bundle following the taken branch instruction is suppressed.

USE - Method is for processing microprocessor branch instructions.

ADVANTAGE - Method processes clustered branch instructions efficiently.

pp; 33 DwgNo 5/5

Title Terms: PROCESSOR; PIPE; BRANCH; INSTRUCTION; PROCESS

Derwent Class: T01

International Patent Class (Main): G06F-009/30 ; G06F-009/38

File Segment: EPI

6/5/12 (Item 12 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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012451094 **Image available**

WPI Acc No: 1999-257202/199922

XRPX Acc No: N99-191678

User interface provision for access to document image

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: HULL J J ; PEAIRS M; SUZUKI K

Number of Countries: 004 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2331437	A	19990519	GB 9822540	A	19981015	199922 B
CN 1217509	A	19990526	CN 98121467	A	19981102	199939
JP 11224148	A	19990817	JP 98318934	A	19981110	199943
GB 2331437	B	20000119	GB 9822540	A	19981015	200006
US 6085205	A	20000704	US 97968694	A	19971112	200036

Priority Applications (No Type Date): US 97968694 A 19971112

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

GB 2331437 A 22 G06F-003/037

CN 1217509 A G06F-017/30

JP 11224148 A 9 G06F-003/00

GB 2331437 B G06F-003/037

US 6085205 A G06F-017/30

Abstract (Basic): GB 2331437 A

NOVELTY - The method involves receiving information indicating a

time associated with a document image, and forming a calendar view showing an icon representing the document image at a location determined by the associated time. The time associated with the document image is a past or future time for either printing, faxing, copying, e-mailing, or retrieving from a network.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for:

(i) a computer product for providing a user interface for access to a document image

(ii) a computer system configured to provide a user interface for access to a document image

USE - Accessing documents that have or will be processed, by time and date.

ADVANTAGE - Assists search for documents, by limiting to documents processed on a particular date.

DESCRIPTION OF DRAWING(S) - The figure shows a monthly calendar view display.

pp; 22 DwgNo 5/7

Title Terms: USER; INTERFACE; PROVISION; ACCESS; DOCUMENT; IMAGE

Derwent Class: T01

International Patent Class (Main): G06F-003/00 ; G06F-003/037 ;

G06F-017/30

File Segment: EPI

6/5/13 (Item 13 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012451067 **Image available**

WPI Acc No: 1999-257175/199922

Related WPI Acc No: 2001-114472

XRPX Acc No: N99-191651

Storage system for document images

Patent Assignee: RICOH KK (RICO)

Inventor: CULLEN J; HULL J J ; NISHIMOTO M; PEAIRS M; SUZUKI K

Number of Countries: 002 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2331383	A	19990519	GB 9820189	A	19980916	199922 B
JP 11224268	A	19990817	JP 98324036	A	19981113	199943
GB 2352071	B	20010725	GB 9820189	A	19980916	200143
			GB 200025551	A	20001018	
GB 2331383	B	20010801	GB 9820189	A	19980916	200144

Priority Applications (No Type Date): US 97969640 A 19971113

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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GB 2331383	A		35	G06F-017/30	
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JP 11224268	A		12	G06F-017/30	
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GB 2352071	B			G06F-017/30	Derived from application GB 9820189
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GB 2331383	B			G06F-017/30	
------------	---	--	--	-------------	--

Abstract (Basic): GB 2331383 A

NOVELTY - The system (100) has a control mechanism that automatically archives a document image in response to a document being produced by a peripheral device (130,145,150). The system includes a memory machine (120) for archiving document images. A remote storage facility is coupled to the memory machine, and this archives images received from the memory machine. The remote facility may be shared between several memory machines.

USE - For document management.

ADVANTAGE - Provides remote storage and retrieval facilities for archiving electronic documents. Enables extending the storage capacity in a computer system to facilitate archiving.

DESCRIPTION OF DRAWING(S) - The figure shows an office machine network.

Memory machine (12)
Fax machine (150)
Printer (145)
Digital copier (130)
pp; 35 DwgNo 1/9

Title Terms: STORAGE; SYSTEM; DOCUMENT; IMAGE

Derwent Class: T01

International Patent Class (Main): G06F-017/30

International Patent Class (Additional): G06F-013/00

File Segment: EPI

6/5/14 (Item 14 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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012426213 **Image available**

WPI Acc No: 1999-232321/199920

XRPX Acc No: N99-172165

Database search method for document images containing textual components

Patent Assignee: RICOH KK (RICO)

Inventor: CULLEN J F; HULL J J ; CULLEN J

Number of Countries: 005 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2330930	A	19990505	GB 9820556	A	19980921	199920 B
DE 19843445	A1	19990520	DE 1043445	A	19980922	199926
JP 11149485	A	19990602	JP 98263041	A	19980917	199932
CN 1216382	A	19990512	CN 98124346	A	19980924	199937
US 5995978	A	19991130	US 97936336	A	19970924	200003
GB 2330930	B	20000405	GB 9820556	A	19980921	200020

Priority Applications (No Type Date): US 97936336 A 19970924

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2330930	A		40	G06F-017/30	
JP 11149485	A		15	G06F-017/30	
US 5995978	A			G06F-017/00	
GB 2330930	B			G06F-017/30	
DE 19843445	A1			G06F-017/30	
CN 1216382	A			G06T-007/20	

Abstract (Basic): GB 2330930 A

NOVELTY - Text is accepted from a user as a keyword, and the textual component of the document images are searched for the keyword. Document images having the keyword contained in their textual component are grouped into a number of clusters based upon the document's compressed or non-compressed representation. A representative document image is displayed for each cluster for selection by the user.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for:

(i) a method for organizing a number of document images in a database

(ii) a computer program product

(iii) a document image database organizing system

USE - Searching a document image database using keyword searching.

ADVANTAGE - The system automatically determines visual characteristics of document images and collects documents together according to the relative similarity of their document images.

DESCRIPTION OF DRAWING(S) - The figure shows a flowchart describing a representative querying operation of the database.

pp; 40 DwgNo 2a/10

Title Terms: DATABASE; SEARCH; METHOD; DOCUMENT; IMAGE; CONTAIN; TEXT; COMPONENT

Derwent Class: T01

International Patent Class (Main): G06F-017/00 ; G06F-017/30 ;

G06T-007/20

File Segment: EPI

6/5/15 (Item 15 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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012387109 **Image available**
WPI Acc No: 1999-193216/199917
Related WPI Acc No: 2000-320940
XRPX Acc No: N99-141556

Automatic document archiving method for computer system

Patent Assignee: RICOH KK (RICO); CULLEN J (CULL-I); HULL J J (HULL-I);
PEAIRS M (PEAI-I); SUZUKI K (SUZU-I); RICOH CORP (RICO)
Inventor: CULLEN J; **HULL J J** ; PEAIRS M; SUZUKI K
Number of Countries: 003 Number of Patents: 005
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2330227	A	19990414	GB 9819374	A	19980904	199917 B
JP 11167567	A	19990622	JP 98254005	A	19980908	199935
GB 2330227	B	20000607	GB 9819374	A	19980904	200031
US 6199073	B1	20010306	US 96754721	A	19961121	200115
			US 97938137	A	19970926	
US 20010016852	A1	20010823	US 96754721	A	19961121	200151
			US 97938137	A	19970926	
			US 2001759002	A	20010111	

Priority Applications (No Type Date): US 97938137 A 19970926; US 96754721 A
19961121; US 2001759002 A 20010111

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2330227	A		35	G06F-017/30	
JP 11167567	A		13	G06F-017/21	
GB 2330227	B			G06F-017/30	
US 6199073	B1			G06F-017/30	CIP of application US 96754721 CIP of patent US 5978477
US 20010016852	A1			G06F-017/30	CIP of application US 96754721 Cont of application US 97938137 CIP of patent US 5978477 Cont of patent US 6199073

Abstract (Basic): GB 2330227 A

NOVELTY - The method involves executing a command, as part of an application program, to transfer a document between a processing device and a peripheral device. The document is transferred in response to the command, and is archived in system memory, and transparently to the application program. The system monitor may detect document transfer, interrupts, or activation of a device driver.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for:

- (i) a method of archiving document images
- (ii) a computer system
- (iii) an apparatus for processing documents

USE - Document archiving.

ADVANTAGE - Data may be captured by tapping into or diverting the document data.

DESCRIPTION OF DRAWING(S) - The figure shows a flow diagram of the archiving.

pp; 35 DwgNo 1/4

Title Terms: AUTOMATIC; DOCUMENT; METHOD; COMPUTER; SYSTEM

Derwent Class: T01; T04

International Patent Class (Main): G06F-017/21 ; G06F-017/30

International Patent Class (Additional): G06F-003/06 ; G06F-013/00 ;

G06F-015/00 ; G06T-011/60

File Segment: EPI

6/5/16 (Item 16 from file: 350)
DIALOG(R)File 350:Derwent WPIX

012377329 **Image available**
WPI Acc No: 1999-183436/199916
Related WPI Acc No: 1994-287380
XRPX Acc No: N99-134756

Remote computational resource invoking system for user operating local system

Patent Assignee: RICOH KK (RICO); GRAHAM J (GRAH-I); HART P (HART-I);
RICOH CORP (RICO)

Inventor: GRAHAM J; HART P

Number of Countries: 003 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2329989	A	19990407	GB 9818626	A	19980826	199916 B
JP 11167452	A	19990622	JP 98251623	A	19980904	199935
US 6049792	A	20000411	US 9334458	A	19930319	200025
			US 96653601	A	19960524	
			US 97923619	A	19970904	
GB 2329989	B	20001129	GB 9818626	A	19980826	200063
US 6295525	B1	20010925	US 9334458	A	19930319	200158
			US 96653601	A	19960524	
			US 97923619	A	19970904	
			US 99444522	A	19991122	
US 20010051938	A1	20011213	US 9334458	A	19930319	200204
			US 96653601	A	19960524	
			US 97923619	A	19970904	
			US 99444522	A	19991122	
			US 2001916158	A	20010725	

Priority Applications (No Type Date): US 97923619 A 19970904; US 9334458 A 19930319; US 96653601 A 19960524; US 99444522 A 19991122; US 2001916158 A 20010725

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2329989	A		48	G06F-017/30	
JP 11167452	A		21	G06F-003/00	
US 6049792	A			G06F-015/00	Cont of application US 9334458 CIP of application US 96653601 Cont of patent US 5546502
GB 2329989	B			G06F-017/30	
US 6295525	B1			G06N-005/00	Cont of application US 9334458 CIP of application US 96653601 Cont of application US 97923619 Cont of patent US 5546502 CIP of patent US 6041182 Cont of patent US 6049792
US 20010051938	A1			G06N-005/04	Cont of application US 9334458 CIP of application US 96653601 Cont of application US 97923619 Cont of application US 99444522 Cont of patent US 5546502 CIP of patent US 6041182 Cont of patent US 6049792 Cont of patent US 6295525

Abstract (Basic): GB 2329989 A

NOVELTY - The user's (302) local computer system has an expert system belief network for operating a base application (304), which receives a series of user interactions, via a data entry device, to establish a context. The context is then used to dynamically instruct the computational resource (308), comprising a database and information retrieval system, via a network, to perform selected computational operations.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for:

(i) a method of dynamically generating a result responsive to a series of user interactions

- (ii) a method of processing information
- (iii) a computer network
- (iv) a computer system for assisting a user in utilizing a base application
- (v) a computer program product for assisting a user in utilizing a base application
- (vi) a computer readable storage medium storing software for dynamically invoking a computational resource for a user

USE - Dynamic invocation of remote computational resources, such as database searching and retrieval.

ADVANTAGE - Takes full advantage of remote computational resources, and assists user in utilizing resources.

DESCRIPTION OF DRAWING(S) - The figure shows a diagram representing the relationship of a computational resource, a base application, and an agent that performs computations responsive to a context defined by user interactions with the base application.

user (302)

base application (304)

computational resource (308)

pp; 48 DwgNo 3/14

Title Terms: REMOTE; COMPUTATION; RESOURCE; INVOKE; SYSTEM; USER; OPERATE; LOCAL; SYSTEM

Derwent Class: T01

International Patent Class (Main): G06F-003/00 ; G06F-015/00 ;

G06F-017/30 ; G06N-005/00; G06N-005/04

International Patent Class (Additional): G06F-009/44 ; G06F-013/00

File Segment: EPI

6/5/17 (Item 17 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011965868 **Image available**

WPI Acc No: 1998-382778/199833

XRPX Acc No: N98-299587

Computer aided document storage method for printing in digital copier, facsimile - involves reproducing document image, based on image data obtained by scanning by printing or faxing

Patent Assignee: RICOH KK (RICO); BAXTER M (BAXT-I); CULLEN J (CULL-I);

HULL J J (HULL-I); PEAIRS M (PEAI-I)

Inventor: BAXTER M; CULLEN J; HULL J J ; PEAIRS M

Number of Countries: 003 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 10154228	A	19980609	JP 97221692	A	19970818	199833 B
DE 19751571	A1	19980806	DE 1051571	A	19971120	199837
US 5978477	A	19991102	US 96754721	A	19961121	199953
US 20010043362	A1	20011122	US 96754721	A	19961121	200176
			US 99347953	A	19990706	

Priority Applications (No Type Date): US 96754721 A 19961121; US 99347953 A 19990706

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 10154228	A		14	G06T-001/00	
DE 19751571	A1			H04N-001/00	
US 5978477	A			H04L-009/00	
US 20010043362	A1			B41B-001/00	Cont of application US 96754721 Cont of patent US 5978477

Abstract (Basic): JP 10154228 A

The method involves receiving a single user input command by receiving process. Based on the input command, image data from a document image is obtained by scanning process.

Based on this image data, the document image is reproduced by printing or faxing. The image data is also forwarded for storage, to a

document control work station (108).

ADVANTAGE - Enables document storage with high confirmity.

Dwg.1/9

Title Terms: COMPUTER; AID; DOCUMENT; STORAGE; METHOD; PRINT; DIGITAL; COPY
; FACSIMILE; REPRODUCE; DOCUMENT; IMAGE; BASED; IMAGE; DATA; OBTAIN; SCAN
; PRINT

Derwent Class: P74; T01; W02

International Patent Class (Main): B41B-001/00; G06T-001/00; H04L-009/00;
H04N-001/00

International Patent Class (Additional): G06F-017/00

File Segment: EPI; EngPI

6/5/18 (Item 18 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011890834 **Image available**

WPI Acc No: 1998-307744/199827

XRPX Acc No: N98-241912

**Document classification method using word length distribution analysis
for computer - involves extracting word length distribution information
of image extracted from electronic display based on which document is
classified**

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: HULL J J

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 10111867	A	19980428	JP 97158910	A	19970616	199827 B
US 5909680	A	19990601	US 96709707	A	19960909	199929

Priority Applications (No Type Date): US 96709707 A 19960909

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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JP 10111867	A		8	G06F-017/27	
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US 5909680	A			G06F-009/00	
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Abstract (Basic): JP 10111867 A

The method involves scanning the image of the document to be
classified on an electronic display. Each sentence in the scanned image
is positioned on a document by using a standard page division
technique.

The length of each word in the text is then calculated. A
probability distribution is set up for the calculated word length.
Then, the document is classified based on the word length distribution
information.

ADVANTAGE - Minimizes calculation. Uses storage resources
effectively.

Dwg.1/4

Title Terms: DOCUMENT; CLASSIFY; METHOD; WORD; LENGTH; DISTRIBUTE; ANALYSE;
COMPUTER; EXTRACT; WORD; LENGTH; DISTRIBUTE; INFORMATION; IMAGE; EXTRACT;
ELECTRONIC; DISPLAY; BASED; DOCUMENT; CLASSIFY

Derwent Class: T01

International Patent Class (Main): G06F-009/00 ; G06F-017/27

International Patent Class (Additional): G06F-017/30

File Segment: EPI

6/5/19 (Item 19 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011458893 **Image available**

WPI Acc No: 1997-436800/199741

XRPX Acc No: N97-363093

**Search process for document-image database - using sample document image
and image characteristics data for searching through database, displaying**

search results and uses search result as key for further search

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: CULLEN J; **HART P** ; **HULL J**

Number of Countries: 004 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 19708265	A1	19970904	DE 1008265	A	19970228	199741 B
JP 9237282	A	19970909	JP 9723429	A	19970206	199746
US 5933823	A	19990803	US 96609641	A	19960301	199937
CN 1170168	A	19980114	CN 97102957	A	19970228	200323

Priority Applications (No Type Date): US 96609641 A 19960301

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
DE 19708265	A1		21	G06F-017/30	
JP 9237282	A		13	G06F-017/30	
US 5933823	A			G06F-017/30	
CN 1170168	A			G06F-017/30	

Abstract (Basic): DE 19708265 A

The system has a bus that supports a CPU [14], system memory [16], I-O controller [18], printer [20], floppy disc drive [33], display [24], mouse [38], keyboard [30], hard disk [32], and scanner [34]. The system memory contains documents that have a range of different data formats and layouts.

The search procedure is made on the basis of defined example document structures. The example document structures can be entered by optical scanning.

USE/ADVANTAGE - Database searching. Allows document structure to be used as basis of search.

Dwg.1/8

Title Terms: SEARCH; PROCESS; DOCUMENT; IMAGE; DATABASE; SAMPLE; DOCUMENT; IMAGE; IMAGE; CHARACTERISTIC; DATA; SEARCH; THROUGH; DATABASE; DISPLAY; SEARCH; RESULT; SEARCH; RESULT; KEY; SEARCH

Derwent Class: T01

International Patent Class (Main): **G06F-017/30**

International Patent Class (Additional): G06K-009/46; G06T-007/00

File Segment: EPI

6/5/20 (Item 20 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011352119 **Image available**

WPI Acc No: 1997-330025/199730

XRPX Acc No: N97-273796

Document search method using electronic document management system with copier - by using minimum of one document in document database that contains descriptor similar to that of purpose document, to check coincidence document of purpose document

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: **HULL J** ; PEAIRS M

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 9134372	A	19970520	JP 96219939	A	19960821	199730 B
US 5867597	A	19990202	US 95523731	A	19950905	199912

Priority Applications (No Type Date): US 95523731 A 19950905

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 9134372	A		22	G06F-017/30	
US 5867597	A			G06K-009/62	

Abstract (Basic): JP 9134372 A

The method involves using a document search system (10) which includes a document database (16) that stores data of a purpose

document (24). The image of the purpose document is input in the document search system. The boundary of the characters in the image of the purpose document is detected. The space between the characters is measured based on one sample of the character boundary. The threshold distribution value of a character space corresponding to the minimum approximation division of the space value between characters related to the space between words, is determined.

The descriptor of the purpose document is produced based on the space between words and on the pattern of a label in a word. The index of several descriptors is searched in the document database using the descriptor of the purpose document. A minimum of one document in the document database that contains a descriptor similar to that of the purpose document, is used to check the coincidence document of the purpose document.

ADVANTAGE - Improves document management system by performing high-speed illustration search.

Dwg.1/15

Title Terms: DOCUMENT; SEARCH; METHOD; ELECTRONIC; DOCUMENT; MANAGEMENT; SYSTEM; COPY; MINIMUM; ONE; DOCUMENT; DOCUMENT; DATABASE; CONTAIN; DESCRIBE; SIMILAR; PURPOSE; DOCUMENT; CHECK; COINCIDE; DOCUMENT; PURPOSE; DOCUMENT

Derwent Class: T01

International Patent Class (Main): G06F-017/30 ; G06K-009/62

International Patent Class (Additional): G06T-001/00

File Segment: EPI

6/5/21 (Item 21 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011234526 **Image available**

WPI Acc No: 1997-212429/199719

XRPX Acc No: N97-175297

Word recognition method for optical character recognition system - involves generating ranking of candidates for each word based upon likelihood of correctness of each candidate, each ranking has top choice indicating initial most likely correct candidate for word

Patent Assignee: UNIV NEW YORK STATE RES FOUND (UYN Y)

Inventor: HONG T; HULL J J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5617488	A	19970401	US 95381767	A	19950201	199719 B

Priority Applications (No Type Date): US 95381767 A 19950201

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5617488	A	12	G06F-015/02	

Abstract (Basic): US 5617488 A

The method involves dividing a document into a two dimensional matrix of pixel areas. The pixel areas are scanned with a light source and detects the intensity of light from each pixel area to generate a number of pixel data signals. The pixel data signals are compared to data signals stored in a memory that represent alphanumeric characters and words in order to initially recognise word candidates from the pixel data signals.

A ranking of candidates are generated for each word based upon a likelihood of correctness of each candidate, each ranking has a top choice indicating the initial most likely correct candidate for the word. Ranked word candidates are grouped into neighbourhoods, each neighbourhood comprising two or more word candidates and each neighbourhood is adjacent to at least one other neighbourhood. a collocation score is computed for each word candidate and the top choice of at least two adjacent neighbourhoods.

ADVANTAGE - Effectively improves on strictly local analysis by

allowing for strong collocation to reinforce weak collocation.

Dwg.3/5

Title Terms: WORD; RECOGNISE; METHOD; OPTICAL; CHARACTER; RECOGNISE; SYSTEM
; GENERATE; RANK; CANDIDATE; WORD; BASED; CORRECT; CANDIDATE; RANK; TOP;
CHOICE; INDICATE; INITIAL; CORRECT; CANDIDATE; WORD

Derwent Class: T01; T04

International Patent Class (Main): G06F-015/02

International Patent Class (Additional): G06K-009/62; G06K-009/72

File Segment: EPI

6/5/22 (Item 22 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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010806416 **Image available**

WPI Acc No: 1996-303369/199631

Related WPI Acc No: 1996-130451

XRPX Acc No: N96-255183

Image processor for defective photographic image - has controller that drives continuous image-processing of mechanism for strange pixel of image domain

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: BILLAWALA N; HART P ; PEAIRS M

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 6318251	A	19941115	JP 9490645	A	19940404	199631 B
US 5623558	A	19970422	US 9345954	A	19930412	199722
			US 94335515	A	19941107	

Priority Applications (No Type Date): US 9345954 A 19930412; US 94335515 A 19941107

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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JP 6318251	A		13	G06F-015/68	
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US 5623558	A		14	G06K-009/40	Cont of application US 9345954
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Abstract (Basic): JP 6318251 A

The processor has a mechanism (2) that performs an image-processing to an abnormal pixel value of a domain specific part (1). Each known pixel is use to determine the value of a strange pixel w.r.t. to the reference pixel value. The image-processing of the mechanism is continuously performed for the remaining strange pixel by using a controller (3).

ADVANTAGE - Attains effective restoration and estimation of pixel in image domain due to continuous image processing.

Dwg.1/18

Title Terms: IMAGE; PROCESSOR; DEFECT; PHOTOGRAPH; IMAGE; CONTROL; DRIVE; CONTINUOUS; IMAGE; PROCESS; MECHANISM; PIXEL; IMAGE; DOMAIN

Derwent Class: T01; W02

International Patent Class (Main): G06F-015/68 ; G06K-009/40

International Patent Class (Additional): H04N-001/40

File Segment: EPI

6/5/23 (Item 23 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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010551560 **Image available**

WPI Acc No: 1996-048513/199605

XRPX Acc No: N96-040715

Input document to database reference document matching appts - has descriptors database contg lists of descriptors where latter are derived from feature document

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: HART P E; HULL J J

Number of Countries: 002 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5465353	A	19951107	US 94222281	A	19940401	199605 B
JP 7282088	A	19951027	JP 9575168	A	19950331	199605

Priority Applications (No Type Date): US 94222281 A 19940401

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5465353	A	20	G06F-015/40	
JP 7282088	A	24	G06F-017/30	

Abstract (Basic): US 5465353 A

The appts includes a document database, wherein reference descriptors are derived from content of reference documents in the document database. A descriptor database identifies for each reference descriptor a list of reference documents which include content from which the each reference descriptor is derived.

The descriptor database includes for each reference document, a number of redundant reference descriptors for the each reference document. A reference descriptor is redundant in that the each reference document is identifiable from less than all of the number of redundant reference descriptors for the each reference document. An input device is used for inputting content of an input document to be matched against the reference documents of the document database.

USE/ADVANTAGE - In image processing and storage, e.g. for comparing input document to database of stored documents and finding documents matching that input. Improved matching text-graphic documents and in case of noise preclusion of exact character-by-character or pixel-by-pixel match.

Dwg.2/7

Title Terms: INPUT; DOCUMENT; DATABASE; REFERENCE; DOCUMENT; MATCH;

APPARATUS; DESCRIBE; DATABASE; CONTAIN; LIST; DESCRIBE; LATTER;

DERIVATIVE; FEATURE; DOCUMENT

Index Terms/Additional Words: COMPUTERISED; TOMOGRAPHY; SCAN; NUCLEAR;

MAGNETIC; RESONANCE; HOLOGRAM

Derwent Class: T04

International Patent Class (Main): G06F-015/40 ; G06F-017/30

International Patent Class (Additional): G06F-015/62

File Segment: EPI

6/5/24 (Item 24 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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010019668 **Image available**

WPI Acc No: 1994-287380/199436

Related WPI Acc No: 1999-183436

XRPX Acc No: N94-226296

Automatic invocation of computational resources e.g. comprising database
- establishes base application comprising expert system for user,
receives user instructions which establish context and automatically
invokes computational resource

Patent Assignee: RICOH KK (RICO); RICOH CORP (RICO)

Inventor: GRAHAM J; HART P

Number of Countries: 005 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 616288	A2	19940921	EP 93119006	A	19931125	199436 B
EP 616288	A3	19950920	EP 93119006	A	19931125	199615
US 5546502	A	19960813	US 9334458	A	19930319	199638
US 6041182	A	20000321	US 9334458	A	19930319	200021
			US 96653601	A	19960524	

Priority Applications (No Type Date): US 9334458 A 19930319; US 96653601 A 19960524

Cited Patents: No-SR.Pub; 2.Jnl.Ref; EP 436459; US 5103498; WO 9202880

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 616288	A2	E	19	G06F-015/40	
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Designated States (Regional): DE FR GB NL

US 6041182	A			G06F-009/445	Cont of application US 9334458 Cont of patent US 5546502
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US 5546502	A		16	G06F-017/20	
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EP 616288	A3			G06F-015/40	
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Abstract (Basic): EP 616288 A

The information retrieval system includes a computer system in which is stored documentation relating to the appts. to be investigated as well as probabilistic information (from expert system) relating to faults in the appts. which may cause such symptoms. The user of the system employs enters data, e.g. using a keyboard to select from a menu on screen, to allow the user to enter symptoms concerning the appts. being investigated.

The system calculates probabilities of the individual faults as indicated by the symptoms they cause. The possible faults are displayed and the user is given an opportunity to select documentation related to the possible faults.

USE/ADVANTAGE - For displaying availability of additional information to user. Enables probabilistic determination to be made of likely importance of available information.

Dwg.1/11

Title Terms: AUTOMATIC; COMPUTATION; RESOURCE; COMPRISE; DATABASE;

ESTABLISH; BASE; APPLY; COMPRISE; EXPERT; SYSTEM; USER; RECEIVE; USER;

INSTRUCTION; ESTABLISH; CONTEXT; AUTOMATIC; COMPUTATION; RESOURCE

Derwent Class: T01

International Patent Class (Main): G06F-009/445 ; G06F-015/40 ;

G06F-017/20

International Patent Class (Additional): G06F-009/44

File Segment: EPI

6/5/25 (Item 25 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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010019126 **Image available**

WPI Acc No: 1994-286838/199436

XRPX Acc No: N94-225875

Page orientation determination for document images - dividing scanned document into series of word blocks, and comparing number of upper and lower segments of characters to statistical analysis

Patent Assignee: RICOH KK (RICO)

Inventor: CULLEN J F; EJIRI K; HART P ; STORK D G

Number of Countries: 001 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 4405105	A1	19940915	DE 4405105	A	19940217	199436 B
DE 4405105	C2	19991007	DE 4405105	A	19940217	199945

Priority Applications (No Type Date): US 9328504 A 19930309

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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DE 4405105	A1		19	G06F-015/70	
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DE 4405105	C2			G06T-007/60	
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Abstract (Basic): DE 4405105 A

An optical scanner scans documents under the control of a CPU that is coupled to memories that store word data and picture data. The CPU also receives input from a user interface. A specific block of text is scanned to determine a total data value.

The values are data compressed to provide values for a rectangular block of information. The block is compared with a reference to determine the orientation and a classification can be made. This may be used to provide corrections prior to reproduction.

Dwg.7/11

Title Terms: PAGE; ORIENT; DETERMINE; DOCUMENT; IMAGE; DIVIDE; SCAN;
DOCUMENT; SERIES; WORD; BLOCK; COMPARE; NUMBER; UPPER; LOWER; SEGMENT;
CHARACTER; STATISTICAL; ANALYSE

Index Terms/Additional Words: serif; tail

Derwent Class: T01; T04; U21

International Patent Class (Main): G06F-015/70 ; G06T-007/60

International Patent Class (Additional): G06K-009/32; G06K-009/52;

H04N-001/047

File Segment: EPI

6/5/26 (Item 26 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00812710

Object-oriented communications framework system with support for multiple remote machine types

Objektorientiertes Kommunikationsumgebungssystem mit Unterstutzung fur mehrere entfernte Maschinentypen

Systeme d'environnement de communication oriente objet avec support pour plusieurs types de machines eloignees

PATENT ASSIGNEE:

Ricoh Company, Ltd., (209036), 3-6, Nakamagome 1-chome, Ohta-ku, Tokyo 143, (JP), (applicant designated states: DE;ES;FR;GB;NL)

INVENTOR:

Hart, Peter E., c/o Ricoh Corp. , 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Jeng, Tina L., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Roth, Rithy K., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Savitzky, Stephen R., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Golding, Richard, c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

LEGAL REPRESENTATIVE:

Schwabe - Sandmair - Marx (100951), Stuntzstrasse 16, 81677 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 755008 A2 970122 (Basic)

EP 755008 A3 971210

APPLICATION (CC, No, Date): EP 96111547 960717;

PRIORITY (CC, No, Date): US 504039 950719

DESIGNATED STATES: DE; ES; FR; GB; NL

INTERNATIONAL PATENT CLASS: G06F-009/46 ; G06F-009/44

ABSTRACT EP 755008 A2

A system enabling an application programmer to construct a plurality of application programs for communicating with a plurality of remote machines of a plurality of machine types, each of the plurality of remote machines having remotely accessible data and remotely performable operations includes a computer system with a memory, a processor, and a mass storage device, the computer system for storing programs, class declarations, and class libraries in an object-oriented programming language, means for compiling files containing source-code representations of application programs in the object-oriented programming language, implementations of a hierarchy of data description classes, each implementation for describing a set of data and for describing methods for manipulating the set of data, implementations of a hierarchy of remote data description classes, each implementation for describing data items contained in one of the plurality of remote machines and for describing methods for accessing the data items, and source code files for a plurality of sample application programs, each

sample application program for communicating with at least one of the plurality of remote machines.

ABSTRACT WORD COUNT: 174

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 970122 A2 Published application (A1with Search Report ;A2without Search Report)

Examination: 970122 A2 Date of filing of request for examination: 960717

Change: 971126 A2 Obligatory supplementary classification (change)

Search Report: 971210 A3 Separate publication of the European or International search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	EPAB97	597
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SPEC A	(English)	EPAB97	7307
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Total word count - document A			7904
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Total word count - document B			0
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Total word count - documents A + B			7904
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6/5/27 (Item 27 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00812708

Method of using an object-oriented communication system with support for multiple remote machine types

Anwendungsverfahren fur ein objektorientiertes Kommunikationssystem mit Unterstutzung fur mehrere entfernte Maschinentypen

Procede d'utilisation d'un systeme de communication oriente objet avec support pour plusieurs types de machines eloignees

PATENT ASSIGNEE:

Ricoh Company, Ltd., (209036), 3-6, Nakamagome 1-chome, Ohta-ku, Tokyo

143, (JP), (applicant designated states: DE;ES;FR;GB;NL)

INVENTOR:

Savitzky, Stephen R., Ricoh Corp., Calif.Res.Cent., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Roth, Rithy K., Ricoh Corp., Calif.Res.Cent., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Jeng, Tina L., Ricoh Corp., Calif.Res.Cent., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Hart, Peter E., Ricoh Corp., Calif.Res.Cent., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

Golding, Richard, Ricoh Corp., Calif.Res.Cent., 2882 Sand Hill Road, Suite 115, Menlo Park, CA 94025, (US)

LEGAL REPRESENTATIVE:

Schwabe - Sandmair - Marx (100951), Stuntzstrasse 16, 81677 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 755007 A2 970122 (Basic)

EP 755007 A3 971203

APPLICATION (CC, No, Date): EP 96111544 960717;

PRIORITY (CC, No, Date): US 504120 950719

DESIGNATED STATES: DE; ES; FR; GB; NL

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT EP 755007 A2

A method for communicating with a plurality of remote machines, of a plurality of machine types, using a computer system having a memory, includes the steps of constructing within the memory a first plurality of software objects, the first plurality of software objects describing services for one of the plurality of remote machines, establishing communications with the one remote machine, and invoking operations on the one remote machine in response to requests described by services of the first plurality of software objects.

ABSTRACT WORD COUNT: 83

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 970122 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 970122 A2 Date of filing of request for examination:
960717

Search Report: 971203 A3 Separate publication of the European or
International search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB97	577
SPEC A	(English)	EPAB97	7132
Total word count - document A			7709
Total word count - document B			0
Total word count - documents A + B			7709

6/5/28 (Item 28 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00812707

Object-oriented communication system with support for multiple remote
machine types

Objektorientiertes Kommunikationssystem mit Unterstutzung fur mehrere
entfernte Maschinentypen

Systeme de communication oriente objet avec support pour plusieurs types de
machines eloignees

PATENT ASSIGNEE:

Ricoh Company, Ltd., (209036), 3-6, Nakamagome 1-chome, Ohta-ku, Tokyo
143, (JP), (applicant designated states: DE;ES;FR;GB;NL)

INVENTOR:

Savitzky, Stephen R., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115,
Menlo Park, CA 94025, (US)

Roth, Rithy K., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo
Park, CA 94025, (US)

Jeng, Tina L., c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo
Park, CA 94025, (US)

Hart, Peter E. , c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo
Park, CA 94025, (US)

Golding, Richard, c/o Ricoh Corp., 2882 Sand Hill Road, Suite 115, Menlo
Park, CA 94025, (US)

LEGAL REPRESENTATIVE:

Schwabe - Sandmair - Marx (100951), Stuntzstrasse 16, 81677 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 755006 A2 970122 (Basic)

EP 755006 A3 971203

APPLICATION (CC, No, Date): EP 96111543 960717;

PRIORITY (CC, No, Date): US 504192 950719

DESIGNATED STATES: DE; ES; FR; GB; NL

INTERNATIONAL PATENT CLASS: G06F-009/46

ABSTRACT EP 755006 A2

An apparatus for communicating with a plurality of remote machines, of
a plurality of machine types includes a computer system including a
processor and memory a data communication means, coupled to the computer
system and to the plurality of remote machines, for communicating with
each of the plurality of remote machines, a first plurality of software
objects within the memory for describing services for the plurality of
remote machines, and a plurality of operations within the memory
associated with the first plurality of software objects, the plurality of
operations for satisfying requests described by the services of the first
plurality of software objects.

ABSTRACT WORD COUNT: 104

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 970122 A2 Published application (Alwith Search Report
;A2without Search Report)

Examination: 970122 A2 Date of filing of request for examination:
960717
Search Report: 971203 A3 Separate publication of the European or
International search report
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language Update Word Count
CLAIMS A (English) EPAB97 1078
SPEC A (English) EPAB97 7247
Total word count - document A 8325
Total word count - document B 0
Total word count - documents A + B 8325

6/5/29 (Item 29 from file: 347)
DIALOG(R)File 347:JAPIO
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07474286 **Image available**
DEVICE AND METHOD FOR RECEPTION OF VISITOR, AND RECORDING MEDIUM RECORDED
WITH VISITOR RECEPTION PROGRAM

PUB. NO.: 2002-342804 [JP 2002342804 A]
PUBLISHED: November 29, 2002 (20021129)
INVENTOR(s): JONATHAN J HAL
MARKO BARABANOVIKKU
MICHAEL BAXTER
GRAHAM JAMEY
HART PETER E
DAA SHAN RII
GREG WOLF
APPLICANT(s): RICOH CO LTD
APPL. NO.: 2001-145498 [JP 20011145498]
FILED: May 15, 2001 (20010515)
INTL CLASS: G07C-009/00; G06F-003/00 ; G06F-017/60

ABSTRACT

PROBLEM TO BE SOLVED: To provide a visitor KIOSK terminal for acquiring and
storing personal information as to a visitor.

SOLUTION: The KIOSK terminal is installed in an entrance of a monitored
facility, the each visitor signs own name in the KIOSK terminal. A name
card, or the name of the visitor and an employment place of the visitor,
and a face image of the visitor are acquired therein. The visitor writes
down a visiting company person and the purpose of visit. The company person
is informed of an arrival of the visitor by an electronic mail or a voice
phone. The visitor's information is stored locally or remotely,
automatically retrieved, and sent to the company person. A user is able to
input information about a visit- predicted person via a network interface.
A phone interface is provided to input a greeting speech and to inspect an
arrival situation of the visitor.

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6/5/30 (Item 30 from file: 347)
DIALOG(R)File 347:JAPIO
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07037929 **Image available**
COMPLEX USER INTERFACE METHOD, COMPUTER SYSTEM AND COMPUTER READABLE
RECORDING MEDIUM

PUB. NO.: 2001-265563 [JP 2001265563 A]
PUBLISHED: September 28, 2001 (20010928)
INVENTOR(s): JONATHAN J HAL

HART PETER E

APPLICANT(s): RICOH CO LTD
APPL. NO.: 2000-380411 [JP 2000380411]
FILED: December 14, 2000 (20001214)
PRIORITY: 00 532412 [US 2000532412], US (United States of America),
March 22, 2000 (20000322)
INTL CLASS: **G06F-003/14 ; G06F-003/00**

ABSTRACT

PROBLEM TO BE SOLVED: To provide a method to couple user interfaces of some applications.

SOLUTION: Data generated by a first application is extracted from a display buffer. The data is related to the user interface from the first application. A layout pattern is recognized from the extracted data. Complex display is created by using layout. The complex display is used to display a second piece of data generated by a second application. No direct link exists between the first and second applications.

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6/5/31 (Item 31 from file: 347)

DIALOG(R) File 347:JAPIO
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06907087 **Image available**
SYSTEM AND METHOD FOR OPENING ELECTRONIC DOCUMENT, INFORMATION PROCESSOR
AND RECORDING MEDIUM

PUB. NO.: 2001-134612 [JP 2001134612 A]
PUBLISHED: May 18, 2001 (20010518)
INVENTOR(s): **HULL JONATHAN J**
MARCO BARABANOVIKU
PETER HART
GREGG WOLF

APPLICANT(s): RICOH CO LTD
APPL. NO.: 2000-276232 [JP 2000276232]
FILED: September 12, 2000 (20000912)
PRIORITY: 99 398229 [US 99398229], US (United States of America),
September 17, 1999 (19990917)
INTL CLASS: **G06F-017/30 ; G06F-013/00**

ABSTRACT

PROBLEM TO BE SOLVED: To reduce the danger of illegal access by preventing an unauthorized person from accessing an electronic document opened on a network.

SOLUTION: A document source 5 provides an electronic document and is, for instance, a multifunctional device. A filter computer 10 converts the electronic document into one for the public and transmits it to server 15. The server 15 stores the electronic document, makes electronic document access using a document identifier available and also transmits information on the electronic document to a place designated by an opening person. The server 15 eliminates the stored electronic document when a condition related to it is established.

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6/5/32 (Item 32 from file: 347)

DIALOG(R) File 347:JAPIO
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06504403 **Image available**

DOCUMENT BROWSING SUPPORT METHOD, STORAGE MEDIUM, AND DOCUMENT BROWSING
SUPPORT SYSTEM

PUB. NO.: 2000-090119 [JP 2000090119 A]
PUBLISHED: March 31, 2000 (20000331)
INVENTOR(s): GRAHAM JAMEY
HART PETER E
APPLICANT(s): RICOH CO LTD
APPL. NO.: 11-195547 [JP 99195547]
FILED: July 09, 1999 (19990709)
PRIORITY: 149921 [US 98149921], US (United States of America),
September 09, 1998 (19980909)
INTL CLASS: G06F-017/30 ; G06F-003/12 ; G06F-017/21

ABSTRACT

PROBLEM TO BE SOLVED: To support that the information that a user is interested in is more speedily printed.

SOLUTION: User input for instructing an item that a user is interested in as an interest concept is accepted (402), an electronic document is analyzed, and the position of the interest concept in the electronic document is discriminated (404); and the electronic document is printed as a simple image which is enlarged to form print pages (408) so that rough page positions in the electronic document and the position of the interest concept are shown to the reader. The document including the item that the user is interested in is automatically retrieved speedily printed to provide its hard copy for the user by executing those steps (402, 404, and 408). Information that the user is interested in can be understood at a glance through the print contents printed in the step 408.

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6/5/33 (Item 33 from file: 347)
DIALOG(R)File 347:JAPIO
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06504402 **Image available**
DOCUMENT BROWSING SUPPORT METHOD, STORAGE MEDIUM, AND DOCUMENT BROWSING
SUPPORT SYSTEM

PUB. NO.: 2000-090118 [JP 2000090118 A]
PUBLISHED: March 31, 2000 (20000331)
INVENTOR(s): GRAHAM JAMEY
HART PETER E
APPLICANT(s): RICOH CO LTD
APPL. NO.: 11-195546 [JP 99195546]
FILED: July 09, 1999 (19990709)
PRIORITY: 149920 [US 98149920], US (United States of America),
September 09, 1998 (19980909)
INTL CLASS: G06F-017/30

ABSTRACT

PROBLEM TO BE SOLVED: To support that the information that a user is interest in is more speedily printed.

SOLUTION: User input for instructing an item that a user is interested in as an interest concept is accepted (302), an electronic document is analyzed to discriminate the position of an interest concept in the electronic document (304), and a visual instruction of the discriminated position is displayed (306), and user input for instructing printing condition setting for the printing of the position is accepted (308) and part of the electronic document corresponding to the printing condition setting and the position of the interest concept is printed (310). The document including the item that the user is interested in is automatically

retrieved and speedily printed to provide its hard copy to the user by
executing those steps (302, 304, 306, 308, and 310).

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Set	Items	Description
S1	1091025	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	86728	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	5196	VIDEO (2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	63968	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	1736	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATION OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N) S2
S6	76628	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT() TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	59511	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBLEM? OR ICON? OR GUI OR GUIs OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) () DOWN() MENU? OR SELECTOR?
S8	131875	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERENT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	50	S1 (S) S2 (S) S3
S10	3	S9 (S) S5
S11	50	S9 OR S10
S12	33	S11 AND IC=(G06F? OR G09G?)

File 348:EUROPEAN PATENTS 1978-2003/Apr W04

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File 349:PCT FULLTEXT 1979-2002/UB=20030508,UT=20030501

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12/5,K/1 (Item from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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01313485

Communication system with multicarrier telephony transport
Kommunikationssystem mit Mehrtragertelefonubertagbarkeit
Systeme de communication de transmission telephonique a porteuses multiples
PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 1122650 A2 010808 (Basic)
EP 1122650 A3 020116

APPLICATION (CC, No, Date): EP 2001201516 970124;

PRIORITY (CC, No, Date): US 10497 960124; US 10506 960124; US 673002 960628
; US 650408 960520

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;
MC; NL; PT; SE

RELATED PARENT NUMBER(S) - PN (AN):

EP 882268 (EP 97903135)

INTERNATIONAL PATENT CLASS: G06F-017/14 ; H04L-001/00; H04L-001/24;
H04L-005/02; H04L-005/14; H04L-012/10; H04L-012/12; H04L-012/26;
H04L-012/28; H04L-012/44; H04L-027/26; H04M-007/00; H04L-027/34;
H04L-025/03

ABSTRACT EP 1122650 A2

A communication system includes a hybrid fibre/coax distribution
network. A head end generates a master clock signal and derives a head

end RF clock signal locked in frequency to the master clock signal. A head end symbol clock signal is derived, locked in frequency to the master clock signal, and multiple strings of downstream digital data are converted to sequences of symbols at times determined by the symbol clock signal. The symbols are modulated onto multiple orthogonal carriers having frequencies determined by the RF clock signal. The modulated orthogonal downstream carriers are transmitted over the distribution network to remote locations.

ABSTRACT WORD COUNT: 101

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 010808 A2 Published application without search report
Change: 010919 A2 Inventor information changed: 20010801
Change: 020116 A2 International Patent Classification changed: 20011128

Search Report: 020116 A3 Separate publication of the search report
Change: 020717 A2 Inventor information changed: 20020524
Examination: 020918 A2 Date of request for examination: 20020716

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200132	713
SPEC A	(English)	200132	79700
Total word count - document A			80413
Total word count - document B			0
Total word count - documents A + B			80413

INTERNATIONAL PATENT CLASS: G06F-017/14 ...

...SPECIFICATION signals are created, any number of copies may be made by an appropriate splitter and that the **present** invention is not limited to any specific number.

The splitter is a passive means for splitting broad...there is no sample data bandwidth reduction and little loss of resolution.

An optical distribution node 18, **shown** in further detail in Figure 5, receives the **split** downstream optical video signal from the **splitter** 38 on optical fiber feeder line 42. The downstream optical video signal is applied to a downstream...

...remote unit 46. Interactive video data from set top box 45 would be transmitted by an additional **separate** RF modem provided by the video service provider at a relatively low frequency in the bandwidth of...

...for the transport of upstream and downstream telephony data and downstream video.

For an MISU 66, a **separate** coaxial line from coaxial tap 44 is utilized to provide transmission of video signals from the coaxial...the HDT 12 to provide a more cost effective transport solution. By placing this function in a **separate** component, the expense of this function does not need to be replicated in each CXMU 56 of...

...and combines them at combiner 25 into a single RF signal. Each 6 MHz frequency band is **separated** by a guard band as is known to one skilled in the art. Downstream telephony information is...

...800 MHz frequency band. The telephony transmitter 14 passes the combined signal through a 1-to-2 **splitter** (not shown), thereby producing redundant downstream electrical signals. The two redundant signals are each delivered to redundant...

12/5,K/2 (Item 2 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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00853531

Plurality of disk array units, data recording/reproducing method and data format

Mehrere Speicherplattenanordnungen, Verfahren zur Datenaufzeichnung/-wiedergabe und Datenformat

Pluralite de reseaux d'unites de disques, methode d'enregistrement/reproduction de donnees et format de donnees

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 786719 A2 970730 (Basic)

APPLICATION (CC, No, Date): EP 97300337 970120;

PRIORITY (CC, No, Date): JP 9628469 960123

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-003/06

ABSTRACT EP 786719 A2

Video data controllers and disk array units are connected with an independent input serial transmission line and an independent output serial transmission line. Data is serially transmitted between the video data controllers and the disk array units. Data of the video data controllers is sent with time slots assigned thereto. Data has a synchronous signal at the beginning thereof. The synchronous signal synchronizes with time slots. The synchronous signal is followed by commands/status and video data.

ABSTRACT WORD COUNT: 77

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 970730 A2 Published application (A1with Search Report ;A2without Search Report)

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9707W5	2758
SPEC A	(English)	9707W5	4410
Total word count - document A			7168
Total word count - document B			0
Total word count - documents A + B			7168

INTERNATIONAL PATENT CLASS: G06F-003/06

...SPECIFICATION the serial transmission line S-OUT.

Fig. 6 is a schematic diagram showing timings of which the video data controllers 1 to 6 of the data recording/reproducing apparatus according to the embodiment of the present invention send data to the disk array units 30 to 35. As shown in Fig. 6, a time period for 1.01 seconds that is equivalent to 30 frames of video data is divided into time slots corresponding to the number of video data controllers being connected. The video data controllers are assigned to individual time slots. Thus, the video data controllers 1 to 6 are prevented from issuing data record/reproduction requests at the same time.

For example...

00785658

Data processing method and device for adapting display data to changes in the conditions of the display device

Datenverarbeitungsverfahren und Vorrichtung zur Verwendung in einem Anzeigegerat

Methode de traitement de donnees et dispositif pour utilisation dans un appareil d'affichage

PATENT ASSIGNEE:

CANON KABUSHIKI KAISHA, (542361), 30-2, 3-chome, Shimomaruko, Ohta-ku, Tokyo, (JP), (applicant designated states: DE;FR;GB;IT;NL)

INVENTOR:

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LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 732681 A2 960918 (Basic)

EP 732681 A3 970402

APPLICATION (CC, No, Date): EP 96301644 960311;

PRIORITY (CC, No, Date): JP 9554269 950314

DESIGNATED STATES: DE; FR; GB; IT; NL

INTERNATIONAL PATENT CLASS: G09G-003/36 ; H04N-007/01

ABSTRACT EP 732681 A2

To perform data processing to allow satisfactory display even when the driving condition changes, a display apparatus of the invention having display means, video data memory for storing video data in which one frame consists of a plurality of fields, and display data memory for storing display data to be displayed on display includes setting unit for setting the driving condition for the display, and unit for thinning the video data in units of fields in response to a predetermined driving condition set by the setting unit. (see image in original document)

ABSTRACT WORD COUNT: 110

LEGAL STATUS (Type, Pub Date, Kind, Text):

Examination: 000524 A2 Date of dispatch of the first examination report: 20000410

Application: 960918 A2 Published application (Alwith Search Report ;A2without Search Report)

Change: 970319 A2 Obligatory supplementary classification (change)

Search Report: 970402 A3 Separate publication of the European or International search report

Change: 970416 A2 Title of invention (German) (change)

Change: 970416 A2 Title of invention (French) (change)

Examination: 971008 A2 Date of filing of request for examination: 970813

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1384
SPEC A	(English)	EPAB96	7354
Total word count - document A			8738
Total word count - document B			0
Total word count - documents A + B			8738

INTERNATIONAL PATENT CLASS: G09G-003/36 ...

...SPECIFICATION corresponding to a data processing device of the present invention, and a display panel controller 90.

The video data controller 80 includes a color decoder 3 for converting TV video data into analog R, G, and B signals, a synchronizing separation circuit 4 for extracting horizontal and

vertical synchronizing signals from the TV video data, a clock generation circuit 5 for generating...

...buffer structure for storing video data after pseudo intermediate processing. A control circuit 11 controls the entire video data controller 80, i.e., timing management of read/write access to each memory, setting of parameters for pseudo...

...the display unit, and serves as a convert means for changing the relationship between video data and display data. A border register 12 stores display data outside of the effective display area (frame portion) of the FLCD. A scanning line address register 13...

12/5,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00750402

Power management for a display device

Leistungssteuerung für eine Anzeigeeinrichtung

Gestion de l'énergie dans un dispositif d'affichage

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 707301 A1 960417 (Basic)

APPLICATION (CC, No, Date): EP 95114252 950911;

PRIORITY (CC, No, Date): US 306282 940914

DESIGNATED STATES: DE; FR; GB; IT; NL

INTERNATIONAL PATENT CLASS: G09G-003/22

ABSTRACT EP 707301 A1

A field emission display electronics system includes a power reduction apparatus 40 in accordance with the present invention. The system includes a matrix-addressable emitter plate 14 and a voltage-switched trichromatic anode plate 10. In a reduced power consumption mode, the display is switched from a color mode to a monochrome mode, and power reduction apparatus 40 performs three functions, each of which contributes to power reduction of the display device. The first function disables the switched application of high voltage sequentially to the three combs of anode stripes 12(sub(R)), 12(sub(G)) and 12(sub(B)), substituting the constant application of high voltage to all of the anode stripes 12, thus reducing the anode switching power to zero. The second function supplies a clock signal to column drivers 18 and row address counter/decoder 20 which is one-third the frequency of the clock signal used during color operation, thus reducing by two-thirds the capacitive power drop in row driver circuits 22, column driver circuits 18 and the emitter panel 14 over a color display. The third function performed by power reduction apparatus 40 is to provide display inversion. Data analyzer 60 senses the data being passed from the host to video controller 16, and determines whether the display data provided by video controller 16 to column drivers 18 should be altered so as to provide an inverted display. Three alternative schemata for controlling entry into the reduced power consumption mode are disclosed. (see image in original document)

ABSTRACT WORD COUNT: 268

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 960417 A1 Published application (A1with Search Report
;A2without Search Report)

Examination: 961211 A1 Date of filing of request for examination:

Withdrawal: 980923 A1 Date on which the European patent application
was deemed to be withdrawn: 980401

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	570
SPEC A	(English)	EPAB96	4479
Total word count - document A			5049
Total word count - document B			0
Total word count - documents A + B			5049

INTERNATIONAL PATENT CLASS: G09G-003/22

...SPECIFICATION column drivers 18. Drivers 18 receive video data from the host device, which has been formatted by **video controller 16** into **separate** red, green and blue display frames from an original mixed signal. In this example, **video controller 16** may process the video data according to the VGA standard, and may typically output 640 parallel...

...to 640 column drivers 18, to thereby provide one color component of a single row of the **display**. The **data** from **video controller 16** are latched into column drivers 18 upon each occurrence of a clock signal received at the...

12/5,K/5 (Item 5 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00475746

Video system

Videosystem

Systeme video

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 482678 A2 920429 (Basic)
EP 482678 A3 920916
EP 482678 B1 980114

APPLICATION (CC, No, Date): EP 91121918 850723;

PRIORITY (CC, No, Date): US 633367 840723; US 633383 840723; US 633384
840723; US 633385 840723; US 633386 840723; US 633387 840723; US 633388
840723; US 633389 840723

DESIGNATED STATES: DE; FR; GB

RELATED PARENT NUMBER(S) - PN (AN):

EP 182454 (EP 853052256)

INTERNATIONAL PATENT CLASS: G09G-001/16

CITED PATENTS (EP A): US 4298931 A; EP 80043 A

ABSTRACT EP 482678 A2

The present invention is a video system which includes a data processor (1), such as a microprocessor, for processing data, a video memory (5)

9/9/12 (Item 1 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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012228193 **Image available**
WPI Acc No: 1999-034300/199903
Related WPI Acc No: 2000-593670
XRPX Acc No: N99-025644

Dual display video controller IC for GUI - has LCD and CRT display data clocks with frequency characteristics corresponding to respective refresh rate and pixel resolution of LCD and CRT display, mutually different from each other

Patent Assignee: CIRRUS LOGIC INC (CIRR-N)
Inventor: BINDLISH R; BRIL V; EGLIT A J; FUIKS K; HAN R S; KOTHA S
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5841418	A	19981124	US 95483584	A	19950607	199903 B

Priority Applications (No Type Date): US 95483584 A 19950607

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5841418	A	15	G09G-005/00	

Abstract (Basic): US 5841418 A

The IC is connected with a single display memory (220) containing CRT and LCD **display data**, through a **display memory data bus** (230). A CRT **display data** clock has a frequency characteristic corresponding to refresh rate and pixel resolution of a CRT display. An LCD **display data** clock has a frequency characteristics corresponding to refresh rate and pixel resolution of LCD display, which is independent of CRT display. A CRT **display data** pipeline is coupled to the **display memory data bus** and CRT **display data** clock, for processing the CRT **display data** synchronising with CRT display clock. The CRT **display data** pipeline has a CRT FIFO (221) which receives and temporarily stores CRT **display data** from display memory, during CRT data read cycle.

The stored CRT **display data** is output to a CRT video data path (222), where the compressed portions of **display data** are decompressed and a CRT pixel data is output. A look up table receives the CRT pixel data and outputs it along with its corresponding address. A D/A converter converts the CRT pixel data and outputs an analog CRT display signal. An LCD **display data** pipeline is coupled to **display memory data bus** and LCD **display data** clock, for processing LCD **display data**. The LCD **display data** pipeline has an LCD FIFO (231) which temporarily stores LCD **display data** from display memory during LCD data read cycle. The LCD **display data** is decompressed along an LCD video data path (232) thereby obtaining an LCD pixel data. The LCD pixel data is output from a look-up table, along with its corresponding address.

ADVANTAGE - Controls more than one video display with single **video controller**. Produces different images on different displays using single **video controller**. Provides an easy to use software model for writing video information for different images into video memory. Utilizes available band width from wide DRAMS by providing two **separate** data paths in a single **video controller** for two video displays.

Dwg.2/6

Title Terms: DUAL; DISPLAY; VIDEO; CONTROL; IC; LCD; CRT; DISPLAY; DATA; CLOCK; FREQUENCY; CHARACTERISTIC; CORRESPOND; RESPECTIVE; REFRESH; RATE; PIXEL; RESOLUTION; LCD; CRT; DISPLAY; MUTUAL

Derwent Class: P85; T01; T04; W03

International Patent Class (Main): G09G-005/00

File Segment: EPI; EngPI

Manual Codes (EPI/S-X): T01-C04A; T01-C04D; T04-H01; T04-H03C2; W03-A08A9; W03-A08B3

()

for storing data from the data processor corresponding to an image to be displayed, a display (11), such as a raster scan cathode ray tube, for displaying the image data stored in the video memory means, and a video system controller (3) connected to the video memory (5) For controlling the transfer of data from the video memory (5) to the display (11) and between the data processor (1) and the video memory (5). The video memory (5) is preferably a multiport dynamic random access memory including an addressable memory array. The video system controller (3) performs a number of functions including refresh of the dynamic random access memory, multiplexing of the various access requests of the video memory and control of the blanking interval of the display. This is accomplished by having a first portion which operates synchronously with the video memory (5) and a second portion which operates synchronously with the data processor (1). The transfer operations in the video system controller are preferably controlled through the use of a programmable state machine which manipulate inputs in a logic array. (see image in original document)

ABSTRACT WORD COUNT: 213

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 920429 A2 Published application (A1with Search Report
;A2without Search Report)

Examination: 920429 A2 Date of filing of request for examination:
920114

Search Report: 920916 A3 Separate publication of the European or
International search report

Grant: 980114 B1 Granted patent

Oppn None: 990107 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9803	1073
CLAIMS B	(German)	9803	915
CLAIMS B	(French)	9803	1435
SPEC B	(English)	9803	14953
Total word count - document A			0
Total word count - document B			18376
Total word count - documents A + B			18376

INTERNATIONAL PATENT CLASS: G09G-001/16

...SPECIFICATION during the time the column address enable low byte, CEL, which is a control line that is **present** on **data** bus 23 goes low, which determines whether the register access is a read or a write. The... timing requirements of the CRT monitor 11. Both interlaced and non-interlaced scan modes are available. The **video system controller** can be programmed to lock up to externally generated sync signal, an application in which the graphic...

12/5,K/6 (Item 6 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00475743

Video system

Videosystem

Systeme video

PATENT ASSIGNEE:

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Albachten, Rudy J., III, 87 Lyons Drive, Centerville, OH 45459, (US)

LEGAL REPRESENTATIVE:

Blanco White, Henry Nicholas et al (50111), ABEL & IMRAY Northumberland
House 303-306 High Holborn, London WC1V 7LH, (GB)

PATENT (CC, No, Kind, Date): EP 481534 A2 920422 (Basic)
EP 481534 A3 920826
EP 481534 B1 980114

APPLICATION (CC, No, Date): EP 91121915 850723;

PRIORITY (CC, No, Date): US 633367 840723; US 633383 840723; US 633384
840723; US 633385 840723; US 633386 840723; US 633387 840723; US 633388
840723; US 633389 840723

DESIGNATED STATES: DE; FR; GB

RELATED PARENT NUMBER(S) - PN (AN):

EP 182454 (EP 853052256)

INTERNATIONAL PATENT CLASS: G09G-001/16

CITED PATENTS (EP A): US 4286320 A

CITED REFERENCES (EP A):

IBM TECHNICAL DISCLOSURE BULLETIN. vol. 25, no. 3B, August 1982, NEW YORK
US pages 1610 - 1611; J.P. HURST: 'Simultaneous storage of two
asynchronous memories for CRT refresh';

ABSTRACT EP 481534 A2

The present invention is a video system which includes a data processor
(1), such as a microprocessor, for processing data, a video memory (5)
for storing data from the data processor corresponding to an image to be
displayed, a display (11), such as a raster scan cathode ray tube, for
displaying the image data stored in the video memory means, and a video
system controller (3) connected to the video memory (5) for controlling
the transfer of data from the video memory (5) to the display (11) and
between the data processor (1) and the video memory (5). The video memory
(5) is preferably a multiport dynamic random access memory including an
addressable memory array. The video system controller (3) performs a
number of functions including refresh of the dynamic random access
memory, multiplexing of the various access requests of the video memory
and control of the blanking interval of the display. This is accomplished
by having a first portion which operates synchronously with the video
memory (5) and a second portion which operates synchronously with the
data processor (1). The transfer operations in the video system
controller are preferably controlled through the use of a programmable
state machine which manipulates inputs in a logic array. (see image in
original document)

ABSTRACT WORD COUNT: 213

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 920422 A2 Published application (Alwith Search Report
;A2without Search Report)
Examination: 920422 A2 Date of filing of request for examination:
920114
Search Report: 920826 A3 Separate publication of the European or
International search report
Grant: 980114 B1 Granted patent
Oppn None: 990107 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9803	1359
CLAIMS B	(German)	9803	1089
CLAIMS B	(French)	9803	1631
SPEC B	(English)	9803	14721
Total word count - document A			0
Total word count - document B			18800
Total word count - documents A + B			18800

INTERNATIONAL PATENT CLASS: G09G-001/16

...SPECIFICATION during the time the column address enable low byte, CEL, which is a control line that is **present** on **data** bus 23 goes low, which determines whether the register access is a read or a write. The...

...timing requirements of the CRT monitor 11. Both interlaced and non-interlaced scan modes are available. The **video** system **controller** can be programmed to lock up to externally generated sync signal, an application in which the graphic...

12/5,K/7 (Item 7 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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00460209

Pixel-depth converter for a computer video display.
Bildelementetiefenwandler fur ein Computervideoanzeigegerat.
Convertisseur de profondeur de pixel pour un affichage video d'ordinateur.
PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road, Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

Larky, Steven Philip, 8911 Scotsman Drive, Austin, Texas 78750-3483, (US)
Peevers, Alan Wesley, 1238 Park Street, Peekskill, New York 10566, (US)
St. Clair, Joe Christopher, 2603 Valley View Cove, Round Rock, Texas 78681, (US)

LEGAL REPRESENTATIVE:

Schafer, Wolfgang, Dipl.-Ing. (62021), IBM Deutschland
Informationssysteme GmbH Patentwesen und Urheberrecht, D-70548
Stuttgart, (DE)

PATENT (CC, No, Kind, Date): EP 457039 A2 911121 (Basic)
EP 457039 A3 920506
EP 457039 B1 950329

APPLICATION (CC, No, Date): EP 91105970 910415;

PRIORITY (CC, No, Date): US 524201 900516

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G09G-001/16 ; G09G-005/14

CITED PATENTS (EP A):: US 4236228 A; EP 358353 A; WO 8707973 A; EP 239840 A;
US 4689807 A; EP 314922 A

ABSTRACT EP 457039 A2

A pixel-depth converter for converting source-pixel data having a source-pixel depth to destination-pixel data having a destination-pixel depth which differs from the source-pixel depth by a user-selectable pixel-depth-conversion scale factor includes a packed-pixel-data depacker circuit, a pixel-data-conversion-table storage circuit and a plurality of conversion-table address-selector multiplexers. The packed-pixel-data depacker circuit receives source-pixel data words from a source-pixel-data memory and transmits the data words depacked-pixel-data-word-component-by-depacked-pixel-data-word-component in accordance with the selected pixel-depth-conversion scale factor. The pixel-data-conversion-table storage circuit stores user-selectable depth-altering pixel-data-conversion data in locations having conversion-table read addresses which are associated with values of depacked-source-pixel-data portions corresponding to the selected pixel-depth-conversion scale factor. The pixel-data-conversion-table storage circuit includes a plurality of independently-operable converted-data-read parallel output ports and a like plurality of associated conversion-table read-address input ports. (see image in original document)

ABSTRACT WORD COUNT: 132

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 911121 A2 Published application (A1with Search Report
;A2without Search Report)

Examination: 920226 A2 Date of filing of request for examination:

911219

Search Report: 920506 A3 Separate publication of the European or
International search report
Examination: 940105 A2 Date of despatch of first examination report:
931123
Grant: 950329 B1 Granted patent
Oppn None: 960320 B1 No opposition filed
Lapse: 970423 B1 Date of lapse of the European patent in a
Contracting State: GB 960415

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	3072
CLAIMS B	(English)	EPAB95	2251
CLAIMS B	(German)	EPAB95	2066
CLAIMS B	(French)	EPAB95	3163
SPEC A	(English)	EPABF1	12712
SPEC B	(English)	EPAB95	12860
Total word count - document A			15787
Total word count - document B			20340
Total word count - documents A + B			36127

INTERNATIONAL PATENT CLASS: G09G-001/16 ...

... G09G-005/14

...SPECIFICATION pictorial images, and other visually-perceived information. Conventional video-display systems include a display screen and a **video** -display **controller** for controlling the display screen. A video display is ordinarily **divided** into atomic display units termed "picture elements" or "pixels." In general, pixels can be controlled with regard to their intensity or color by the **video** -display **controller** of the video-display system, which in turn typically is controlled by the central processor of the computer system. When every pixel of a display can be independently controlled by the **video** -display **controller**, the controller is referred to as an "all-points-addressable," or "APA," controller. Conventional **video** -display **controllers** include digital video memory for storing digital representations of the information to be displayed. In the video memory of an APA **video** **controller**, the digital representation of the information to be displayed on a full display screen is referred to as a "bitmap." A bitmap includes **display** pixel **data** in which each pixel is represented by one or more bits. The display pixel data is organized...

12/5,K/8 (Item 8 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
(c) 2003 European Patent Office. All rts. reserv.

00406366

Commodity information retrieval system.

Wiederauffindungssystem fur geschäftliche Informationen.

Systeme de recherche d'informations commerciales.

PATENT ASSIGNEE:

YAMATO DENKI ENGINEERING KABUSHIKI KAISHA, (1129680), 3-go, 1-ban,
Nishitenma 5-chome Kita-ku, Osaka-shi, (JP), (applicant designated
states: AT;BE;CH;DE;ES;FR;GB;GR;IT;LI;LU;NL;SE)

INVENTOR:

Yamamoto, Eizo, 13-banchi, Skuragaoka 3-chome, Mino-shi Osaka-fu, (JP)
Maiki, Hayato, 4-21, Kamo 2-chome, Takaishi-shi Osaka-fu, (JP)

LEGAL REPRESENTATIVE:

Dipl.-Ing. H. Hauck, Dipl.-Ing. E. Graalfs, Dipl.-Ing. W. Wehnert,
Dr.-Ing. W. Doring (100551), Neuer Wall 41, W-2000 Hamburg 36, (DE)

PATENT (CC, No, Kind, Date): EP 410036 A1 910130 (Basic)

APPLICATION (CC, No, Date): EP 89113860 890727;

PRIORITY (CC, No, Date): EP 89113860 890727

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06F-015/40

CITED PATENTS (EP A): EP 207468 A; US 4266284 A; US 4064490 A

CITED REFERENCES (EP A):

S.M.P.T.E., vol. 94, no. 1, January 1985, pages 11-15; E. LEONARD:
"Broadening video/graphics applications"

IDEM

SYSTEMS-COMPUTER-CONTROLS, vol. 11, no. 1, January-February 1980, pages
10-17; H. NAKAGAWA et al.: "Picture data base on-line remote access
system"

IEEE TRANS. ON CONSUMER ELECTRONICS, vol. CE-26, no. 3, August 1980,
pages 487-505; W.D. PIERCE et al.: "A low cost terminal for the 1980's:
project green thumb";

ABSTRACT EP 410036 A1

A commodity information retrieval system comprising at least one host station and a plurality of user stations interconnected through telecommunication lines by way of a repeater mounted on a communications satellite. The host station stores various commodity data including video data of commodities. Each of the user stations includes a terminal commodity data storage, and a retrieval terminal for searching desired commodities by utilizing commodity data stored in the terminal commodity data storage., When the user wants to look at a selected number of commodity images, corresponding video data are requested from the host station. Desired commodities are made referring to commodity classifications, users' tastes and/or purposes of purchase. Various data are obtained from the selections made by the user, which data are used as history data, demand data and so on for grasping market trends and system operations.

ABSTRACT WORD COUNT: 143

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 910130 A1 Published application (A1with Search Report
;A2without Search Report)

Examination: 910130 A1 Date of filing of request for examination:
890807

Change: 910313 A1 Representative (change)

Examination: 940309 A1 Date of despatch of first examination report:
940121

Withdrawal: 941123 A1 Date on which the European patent application
was deemed to be withdrawn: 940601

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1649
SPEC A	(English)	EPABF1	21712
Total word count - document A			23361
Total word count - document B			0
Total word count - documents A + B			23361

INTERNATIONAL PATENT CLASS: G06F-015/40

...SPECIFICATION the front end computer 218. Its touch screen 213 gives displays based on the icon data, icon **display** attribute **data**, and icon highlight data read out of the files 215, 219 and 217 by the front end computer 218. In addition, the retrieval terminal 212 has various functions: a function to actuate the **video controller** 224 for reading the video data and description data from the video buffer memory 226 and of retrieved commodities for display in the four **split** screen mode on the touch screen 213, and to switch one image after another for scroll display...

12/5,K/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2003 European Patent Office. All rts. reserv.

00340837

Virtual display adapter.
Virtueller Anzeigeadapter.
Adaptateur d'affichage virtuel.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road,
Armonk, N.Y. 10504, (US), (applicant designated states:
AT;BE;CH;DE;ES;FR;GB;IT;LI;NL;SE)

INVENTOR:

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Peevers, Alan Wesley, 122 Wells Street, Peekskill New York 10566, (US)
St. Clair, Joe Christopher, 2603 Valley View Cove, Round Rock Texas 78681
, (US)

LEGAL REPRESENTATIVE:

Teufel, Fritz, Dipl.-Phys. (11855), IBM Deutschland Informationssysteme
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PATENT (CC, No, Kind, Date): EP 338416 A2 891025 (Basic)
EP 338416 A3 911211
EP 338416 B1 940831

APPLICATION (CC, No, Date): EP 89106573 890413;

PRIORITY (CC, No, Date): US 183795 880420

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE

INTERNATIONAL PATENT CLASS: G09G-001/16 ; G09G-001/00

CITED PATENTS (EP A): EP 244112 A; US 4484302 A; EP 99989 A; EP 172055 A

CITED REFERENCES (EP A):

IEEE MICRO. vol. 8, no. 2, April 1988, NEW YORK US pages 22 - 36;
S.KIMURA ET AL.: 'Implementation of the V60/V70 and Its FRM Function'
PATENT ABSTRACTS OF JAPAN vol. 9, no. 214 (P-384) August 31, 1985
& JP-A-60 074 085 (FUJITSU KABUSHIKI KAISHA) April 26, 1985;

ABSTRACT EP 338416 A2

A display control means such as a virtual display adapter (110) allows
the advanced functions of a display controller to be utilized in a large
area of memory in addition to the normal use in display memory (120).
This large area of memory includes system memory (130), and efficient
access to this large area of memory is permitted for normal system use.
The display controller also functions with non-contiguous and
non-resident bitmaps. The flexibility of demand-paged virtual memory is
utilized for display tasks, as display bitmaps may be written to the
large area of memory as well as the display memory.

ABSTRACT WORD COUNT: 105

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 891025 A2 Published application (Alwith Search Report
;A2without Search Report)
Examination: 900425 A2 Date of filing of request for examination:
900224
Search Report: 911211 A3 Separate publication of the European or
International search report
Change: 930331 A2 Representative (change)
Change: 930512 A2 Representative (change)
Examination: 930908 A2 Date of despatch of first examination report:
930728
Grant: 940831 B1 Granted patent
Lapse: 950628 B1 Date of lapse of the European patent in a
Contracting State: BE 940831
Lapse: 950719 B1 Date of lapse of the European patent in a
Contracting State: BE 940831, SE 941130
Oppn None: 950823 B1 No opposition filed
Lapse: 970326 B1 Date of lapse of the European patent in a
Contracting State: BE 940831, CH 950430, LI
950430, SE 941130
Lapse: 970326 B1 Date of lapse of the European patent in a
Contracting State: BE 940831, CH 950430, LI
950430, SE 941130
Lapse: 970416 B1 Date of lapse of the European patent in a

Contracting State: AT 960413, BE 940831, CH
950430, LI 950430, SE 941130

Lapse: 970423 B1 Date of lapse of the European patent in a
Contracting State: AT 960413, BE 940831, CH
950430, LI 950430, GB 960413, SE 941130

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPBBF1	1981
CLAIMS B	(English)	EPBBF1	603
CLAIMS B	(German)	EPBBF1	512
CLAIMS B	(French)	EPBBF1	732
SPEC A	(English)	EPBBF1	6263
SPEC B	(English)	EPBBF1	6323
Total word count - document A			8244
Total word count - document B			8170
Total word count - documents A + B			16414

INTERNATIONAL PATENT CLASS: G09G-001/16 ...

... G09G-001/00

...SPECIFICATION final period of each horizontal line.

U.S. patent 4,656,597 to Bond et al., entitled " **Video System Controller** With a Row Address Override Circuit" is directed to a video system which is able to change...

...video monitor with a minimal number of memory transfer cycles. The video system includes a monitor for **displaying** of processed **data** , a processor means for processing the data to be displayed, a display memory means **divided** into a plurality of planes addressable by a row address, the display memory stores the data that...

...SPECIFICATION final period of each horizontal line.

U.S. patent 4,656,597 to Bond et al., entitled " **Video System Controller** With a Row Address Override Circuit" is directed to a video system which is able to change...

...video monitor with a minimal number of memory transfer cycles. The video system includes a monitor for **displaying** of processed **data** , a processor means for processing the data to be displayed, a display memory means **divided** into a plurality of planes addressable by a row address, the display memory stores the data that...

12/5,K/10 (Item 10 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00339460

Multi-screen setting condition display system

Anzeigesystem der Betriebszustände mit einem Bildschirm mit mehreren Feldern

Systeme d'affichage de paramètres sélectionnés utilisant un écran subdivisé en zones multiples

PATENT ASSIGNEE:

FUJI XEROX CO., LTD., (450440), No. 3-5, Akasaka 3-chome, Minato-ku Tokyo 107, (JP), (applicant designated states: DE;GB)

INVENTOR:

Shibayama, Yoshinaru, c/o Fuji Xerox Co., Ltd. Ebina Works, 2274, Hongo Ebina-shi Kanagawa, (JP)

Ohtake, Takao, c/o Fuji Xerox Co., Ltd. Ebina Works, 2274, Hongo Ebina-shi Kanagawa, (JP)

LEGAL REPRESENTATIVE:

Grunecker, Kinkeldey, Stockmair & Schwanhauser Anwaltssozietat (100721) , Maximilianstrasse 58, 80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 334327 A2 890927 (Basic)
EP 334327 A3 901107

EP 334327 B1 931229
APPLICATION (CC, No, Date): EP 89105150 890322;
PRIORITY (CC, No, Date): JP 6838088 880323
DESIGNATED STATES: DE; GB
INTERNATIONAL PATENT CLASS: G06F-003/037
CITED PATENTS (EP A): DE 3043081 A; US 4728985 A; JP 62255965 A
CITED REFERENCES (EP A):
PATENT ABSTRACTS OF JAPAN, vol. 7, no. 109 (P-196) 1254 , 12th May 1983;
& JP-A-58 031 347 (RICOH K.K.) 24-02-1983;

ABSTRACT EP 334327 A2

A display control system for controlling a display unit (301) used as a user interface in a recording apparatus for the purposes of selecting recording functions and setting execution conditions where each of the functions includes a corresponding plurality of function groups comprising, display control means for controlling the display unit (301) to display selection-mode screens for each function group and to instruct the operator of the recording apparatus to enter values to set the execution conditions, and screen control means for controlling the display unit (301) to display an area that indicates the set state of each of the function groups for each of the functions whereby the operator can see at one time all of the function group settings.

ABSTRACT WORD COUNT: 125

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 890927 A2 Published application (Alwith Search Report ;A2without Search Report)
Change: 891206 A2 Representative (change)
Search Report: 901107 A3 Separate publication of the European or International search report
Examination: 910213 A2 Date of filing of request for examination: 901218
Examination: 920819 A2 Date of despatch of first examination report: 920702
Grant: 931229 B1 Granted patent
Oppn: 941109 B1 Opposition 01/940912 Oce-Nederland B.V.; St. Urbanusweg 43; NL-5914 CC Venlo; (NL)
(Representative:)Hanneman, Henri W.A.M.;
Oce-Nederland B.V. Patents and Information St.
Urbanusweg 43 P.O. Box 101; NL-5900 MA Venlo;
(NL)
Oppn: 941123 B1 Opposition 01/940912 Oce-Nederland B.V.; St. Urbanusweg 43; NL-5914 CC Venlo; (NL)
(Representative:)Hanneman, Henri W.A.M.;
Oce-Nederland B.V. Patents and Information St.
Urbanusweg 43 P.O. Box 101; NL-5900 MA Venlo;
(NL)
02/940929 Siemens Nixdorf Informationssysteme AG; Heinz-Nixdorf-Ring 1; D-33106 Paderborn; (DE)
(Representative:)Fuchs, Franz-Josef, Dr.-Ing. et al; Postfach 22 13 17; D-80503 Munchen; (DE)
*Oppn: 961113 B1 Opposition (change) 02/940929 Siemens Nixdorf Informationssysteme AG; Heinz-Nixdorf-Ring 1; D-33106 Paderborn; (DE)
*Oppn: 961127 B1 Opposition (change) 02/940929 Siemens Nixdorf Informationssysteme AG; Heinz-Nixdorf-Ring 1; D-33106 Paderborn; (DE)
(Representative:)Wiebusch, Manfred;
Patentanwalte TER MEER-MULLER-STEINMEISTER & PARTNER Artur-Ladebeck-Strasse 51; 33617 Bielefeld; (DE)

Amended: 990630 B2 Maintenance of the European patent as amended

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9926	290
CLAIMS B	(German)	9926	261

CLAIMS B (French) 9926 357
SPEC B (English) 9926 25993
Total word count - document A 0
Total word count - document B 26901
Total word count - documents A + B 26901

INTERNATIONAL PATENT CLASS: G06F-003/037

...SPECIFICATION be delivered to an attribute adding circuit 356. The attribute adding circuit 356 receives a blanking signal from the CRT controller 335 and controls the video signal corresponding to attribute data in a display period. A one-shot circuit 348 generates an interrupt signal for the U/I CPU 46 from a vertical synchronizing blanking signal, which is included in the blanking signal sent out from the CRT controller 335.

Fig. 36 is a view showing an example of the address correspondence in V-RAMs; Fig...

12/5,K/11 (Item 11 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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00309779

Improvements in computer graphics systems.

Graphische Computersysteme.

Systemes graphiques a ordinateur.

PATENT ASSIGNEE:

AXIOM INNOVATION LIMITED, (1235491), 1 Logan Mews, London W8 6QP, (GB),
(applicant designated states: AT;BE;CH;DE;ES;FR;GR;IT;LI;LU;NL;SE)

INVENTOR:

Phillips, David John, 284 Old Farm Avenue, Sidcup Kent DA15 8AR, (GB)

LEGAL REPRESENTATIVE:

Milhench, Howard Leslie et al (33863), R.G.C. Jenkins & Co. 26 Caxton
Street, London SW1H 0RJ, (GB)

PATENT (CC, No, Kind, Date): EP 280582 A2 880831 (Basic)
EP 280582 A3 900704
EP 280582 B1 950719

APPLICATION (CC, No, Date): EP 88301742 880229;

PRIORITY (CC, No, Date): GB 8704653 870227; GB 8727119 871119; GB 8801012
880118

DESIGNATED STATES (Pub A): AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL;
SE; (Pub B): AT; BE; CH; DE; ES; FR; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G09G-001/00

CITED PATENTS (EP A): WO 8502049 A; WO 8502049 A; WO 8502049 A; EP 147542 A
; EP 153197 A; EP 121015 A

ABSTRACT EP 280582 A2

A multi-tasking, multiple windowing computer graphics facility which does not place undue burdens upon the processing power of its host computer comprises a multi-planar memory to which data defining windows to be displayed in viewports on a display screen can be written, and a plurality of viewport controllers each individually associated with a respective one of the memory planes and capable of extracting data from each of a plurality of windows defined in its respective memory plane for effecting a corresponding display in any desired viewport location on the display screen. For the management of overlapping viewports, the window-viewport assignments which determine the display locations of viewports on the display screen include viewport priority indications which are taken into account in the reading out of data from the several planes of the multi-planar memory so that overlapping viewports are handled automatically and can appear transparent or opaque relative to one another as required. Windows can be created of any size and any number of planes deep (up to the available depth of the display memory), and opaque or transparent images can be moved freely around the display screen without requiring modification of the data in the memory by flexibly mapping windows in the display memory to viewports on the

screen. A VRAM (video RAM) multi-planar memory is used and the viewport controllers are configured as modular VLSI components which can be cascaded for addressing any number of memory planes.

ABSTRACT WORD COUNT: 246

LEGAL STATUS (Type, Pub Date, Kind, Text):

Lapse: 020612 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19950719, BE 19950719, CH 19950719, LI 19950719, DE 19951020, ES 19950719, FR 19951215, GR 19950719, IT 19950719, LU 19960229, SE 19951019,

Lapse: 20000126 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19950719, BE 19950719, CH 19950719, LI 19950719, DE 19951020, FR 19951215, GR 19950719, IT 19950719, SE 19951019,

Lapse: 030212 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19950719, BE 19950719, CH 19950719, LI 19950719, DE 19951020, ES 19950719, FR 19951215, GR 19950719, IT 19950719, LU 19960229, NL 19950719, SE 19951019,

Application: 880831 A2 Published application (Alwith Search Report ;A2without Search Report)

Lapse: 20000209 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19950719, BE 19950719, CH 19950719, LI 19950719, DE 19951020, FR 19951215, GR 19950719, IT 19950719, LU 19960229, SE 19951019,

Search Report: 900704 A3 Separate publication of the European or International search report

Examination: 910508 A2 Date of filing of request for examination: 910307

Examination: 930407 A2 Date of despatch of first examination report: 930225

Change: 941026 A2 Representative (change)

*Assignee: 941026 A2 Applicant (transfer of rights) (change): AXIOM INNOVATION LIMITED (1235491) 1 Logan Mews London W8 6QP (GB) (applicant designated states: AT;BE;CH;DE;ES;FR;GB;GR;IT;LI;LU;NL;SE)

Grant: 950719 B1 Granted patent

Lapse: 960313 B1 Date of lapse of the European patent in a Contracting State: SE 951019

Lapse: 960403 B1 Date of lapse of the European patent in a Contracting State: AT 950719, SE 951019

Lapse: 960403 B1 Date of lapse of the European patent in a Contracting State: AT 950719, DE 951020, SE 951019

Lapse: 960501 B1 Date of lapse of the European patent in a Contracting State: AT 950719, CH 950719, LI 950719, DE 951020, SE 951019

Lapse: 960501 B1 Date of lapse of the European patent in a Contracting State: AT 950719, CH 950719, LI 950719, DE 951020, SE 951019

Lapse: 960508 B1 Date of lapse of the European patent in a Contracting State: AT 950719, CH 950719, LI 950719, DE 951020, FR 951215, SE 951019

Lapse: 960529 B1 Date of lapse of the European patent in a Contracting State: AT 950719, BE 950719, CH 950719, LI 950719, DE 951020, FR 951215, SE 951019

Oppn None: 960710 B1 No opposition filed

Lapse: 991020 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19950719, BE 19950719, CH 19950719, LI

19950719, DE 19951020, FR 19951215, IT
19950719, SE 19951019,

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	1565
CLAIMS B	(English)	EPAB95	1600
CLAIMS B	(German)	EPAB95	1348
CLAIMS B	(French)	EPAB95	2076
SPEC A	(English)	EPABF1	8555
SPEC B	(English)	EPAB95	8581
Total word count - document A			10121
Total word count - document B			13605
Total word count - documents A + B			23726

INTERNATIONAL PATENT CLASS: G09G-001/00

...SPECIFICATION actions to be performed by the VRAM controller 17 in extracting image data from the VRAM for **display**. The **data** output from the VRAM serial port is input a byte at a time to merge logic and...

...plurality of display pixels to achieve the required single pixel display accuracy before passing them to the **video controller** 22. The **video controller** 22 accepts the merged data and outputs it under control of the priority controller 23 for **display**. Video **data** is loaded from the VRAM into each of the four viewport controllers eight pixels at a time and is output to the display four pixels at a time, and the **video controller** takes care of this 8:4 multiplex operation; by virtue of this arrangement the maximum on-chip...

...SPECIFICATION a VRAM setup list which describes the actions to be performed by the VRAM controller 17 in **extracting** image data from the VRAM for **display**. The **data** output from the VRAM serial port is input a byte at a time to merge logic and...

...plurality of display pixels to achieve the required single pixel display accuracy before passing them to the **video controller** 22. The **video controller** 22 accepts the merged data and outputs it under control of the priority controller 23 for **display**. Video **data** is loaded from the VRAM into each of the four viewport controllers eight pixels at a time and is output to the display four pixels at a time, and the **video controller** takes care of this 8:4 multiplex operation; by virtue of this arrangement the maximum on-chip...

12/5,K/12 (Item 12 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00237693

Method for inspection of printed circuit board assembly by arithmetic comparison of several pictures in different colours.

Verfahren zur Untersuchung von Leiterplatten mittels arithmetischen Vergleichs von mehreren Bildern in verschiedenen Farben.

Procede pour l'inspection d'assemblages de plaques de circuits imprimes par comparaison arithmetique de plusieurs images dans des couleurs differentes.

PATENT ASSIGNEE:

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Ukyo-ku, Kyoto-shi Kyoto-fu, (JP), (applicant designated states:
AT;BE;CH;DE;ES;FR;GB;GR;IT;LI;LU;NL;SE)

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PATENT (CC, No, Kind, Date): EP 231941 A2 870812 (Basic)
EP 231941 A3 880302
EP 231941 B1 910918

APPLICATION (CC, No, Date): EP 87101522 870205;

PRIORITY (CC, No, Date): JP 8623292 860205

DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06F-007/02 ; G01N-021/88; G01N-021/91;

G01N-021/84; G01R-031/28

CITED PATENTS (EP A): GB 2139754 A; US 3976383 A; US 4330712 A; DE 3422395
A

CITED REFERENCES (EP A):

JEE JOURNAL OF ELECTRONIC ENGINEERING, vol. 16, no. 15, July 1979, pages
60-63, Tokyo, JP; M. MINAMI: "Toshiba's MDI equipment enhances LSI mask
inspection"

PATENT ABSTRACTS OF JAPAN, vol. 6, no. 125 (P-127) 1003 , 10th July 1982,
& JP - A - 57 050 645 (HITACHI SEISAKUSHO K.K.) 25-03-1982

PATENT ABSTRACTS OF JAPAN, vol. 8, no. 23 (P-251) 1460 , 31th January
1984; & JP - A - 58 179 343 (NIPPON DENKI K.K.) 20th October 1983;

ABSTRACT EP 231941 A2

An apparatus for automatic inspection of printed circuit board
assemblies inspects for the correct presence, positioning, and
orientation of component parts mounted on a base printed circuit board.
In this method for inspection of printed circuit board assemblies, an
arithmetic comparison operation is conducted on several color pictures of
different colors taken of the base printed circuit board mounted with the
component parts; and a distinction is made between parts of the base
printed circuit board not occupied by the component parts, and the
component parts. Optionally, one of the base printed circuit board and
the component parts is tinted in a color of a category selected from the
group consisting of the green category, the red category, and the yellow
category. The arithmetic comparison operation may be division, or may be
subtraction. The base printed circuit board may be coated with a preflux
mixed with a fluorescent agent, which may be green; or may be coated with
a bonding agent mixed with a fluorescent agent, which again may be green.
Illumination may be provided by a light source whose wave length varies
from visible blue to ultraviolet. The color pictures may be taken by a
color TV camera.

ABSTRACT WORD COUNT: 203

LEGAL STATUS (Type, Pub Date, Kind, Text):

Lapse: 20000126 B1 Date of lapse of European Patent in a
contracting state (Country, date): AT
19910918, BE 19910918, CH 19910918, LI
19910918, GR 19910918, IT 19910918, LU
19920229, SE 19910918,
Application: 870812 A2 Published application (A1with Search Report
;A2without Search Report)
Examination: 870812 A2 Date of filing of request for examination:
870205
Search Report: 880302 A3 Separate publication of the European or
International search report
Change: 880302 A2 Obligatory supplementary classification
(change)
Examination: 900207 A2 Date of despatch of first examination report:
891221
Grant: 910918 B1 Granted patent
Lapse: 920318 B1 Date of lapse of the European patent in a
Contracting State: CH 910918, LI 910918
Lapse: 920513 B1 Date of lapse of the European patent in a

()

Set	Items	Description
S1	1211289	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	64160	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	5060	VIDEO (2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	53291	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	820	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATION OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N) S2
S6	51494	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT() TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	131345	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBLEM? OR ICON? OR GUI OR GUIS OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) () DOWN() MENU? OR SELECTOR?
S8	118952	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERENT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	0	S1 AND S2 AND S3
S10	23	S2 AND S3
S11	21	S10 NOT PY>2000
S12	21	S11 NOT PD>20000322
S13	19	RD (unique items)
File	8: Ei Compendex(R)	1970-2003/May W1 (c) 2003 Elsevier Eng. Info. Inc.
File	35: Dissertation Abs Online	1861-2003/Apr (c) 2003 ProQuest Info&Learning
File	202: Info. Sci. & Tech. Abs.	1966-2003/Apr 04 (c) Information Today, Inc
File	65: Inside Conferences	1993-2003/May W1 (c) 2003 BLDSC all rts. reserv.
File	2: INSPEC	1969-2003/May W1 (c) 2003 Institution of Electrical Engineers
File	233: Internet & Personal Comp. Abs.	1981-2003/Apr (c) 2003 Info. Today Inc.
File	94: JICST-EPlus	1985-2003/May W1 (c) 2003 Japan Science and Tech Corp(JST)
File	99: Wilson Appl. Sci & Tech Abs	1983-2003/Mar (c) 2003 The HW Wilson Co.
File	95: TEME-Technology & Management	1989-2003/Apr W4 (c) 2003 FIZ TECHNIK

13/5/1 (Item 1 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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05477470 E.I. No: EIP00025025913

Title: Large capacity airborne data recorders for reconnaissance applications

Author: Queau, Alain

Corporate Source: Enertec SA, Clamart, Fr

Conference Title: Proceedings of the 1999 Airborne Reconnaissance XXIII

Conference Location: Denver, CO, USA Conference Date: 19990720-19990721

Sponsor: SPIE

E.I. Conference No.: 56278

Source: Proceedings of SPIE - The International Society for Optical Engineering v 3751 1999. p 192-196

Publication Year: 1999

CODEN: PSISDG ISSN: 0277-786X

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 0003W4

Abstract: Magnetic Disk Recorders for data and **video** recording on-board airborne vehicles are described in this presentation. The DS4000 Hard Disk System (HDS) is designed for high capacity and high bandwidth data storage and can be configured to accommodate various sensors inputs. It is based upon high-end 3.5 inches magnetic disks packaged in a ruggedized housing to withstand severe environmental conditions. The disk cartridge is removable and directly compatible with standard computer interfaces (SCSI/EIDE). The VS2000 Video Hard Disk System (VHDS) is a customized version dedicated to acquisition of 1 to 4 video channels and auxiliary data. It consists of a 2.5 inches disk drive cartridge coupled with digitization and video compression modules. It efficiently replaces up to 4 analog video recorders by a single compact box, which can be easily fitted in a very small volume. Retrieval and simultaneous **display** of video **data** is made on a standard PC system. A data recording version DS 2000 of the video recorder is to be used in the small payload application. The DS 2000 Ultra Compact Airborne recorder can acquire and record any type of digital data up to 60 Mb/s. First tests in actual flight conditions have recently demonstrated the robustness of the disk recorder. (Author abstract)

Descriptors: *Optical sensors; Hard disk storage; Reconnaissance aircraft ; Interfaces (computer); Digital image storage

Identifiers: Airborne data recorders; Video hard disk systems (VHDS)

Classification Codes:

652.1.2 (Military Aircraft)

741.3 (Optical Devices & Systems); 722.1 (Data Storage, Equipment & Techniques); 404.1 (Military Engineering); 652.1 (Aircraft, General); 722.2 (Computer Peripheral Equipment); 722.4 (Digital Computers & Systems)

741 (Optics & Optical Devices); 722 (Computer Hardware); 404 (Military Engineering); 652 (Aircraft)

74 (OPTICAL TECHNOLOGY); 72 (COMPUTERS & DATA PROCESSING); 65 (AEROSPACE ENGINEERING)

13/5/2 (Item 2 from file: 8)
DIALOG(R)File 8: Ei Compendex(R)
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05108731 E.I. No: EIP98084356022

Title: DIVA: Exploratory data analysis with multimedia streams

Author: Mackay, Wendy E.; Beaudouin-Lafon, Michel

Corporate Source: Cent d'Etudes de la Navigation Aerienne, Fr

Conference Title: Proceedings of the 1998 Conference on Human Factors in Computing Systems, CHI

Conference Location: Los Angeles, CA, USA Conference Date: 19980418-19980423

E.I. Conference No.: 48874

Source: Conference on Human Factors in Computing Systems - Proceedings
1998. ACM, New York, NY, USA. p 416-423

Publication Year: 1998

CODEN: 002163

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications); G
; (General Review)

Journal Announcement: 9810W4

Abstract: DIVA supports exploratory data analysis of multimedia streams, enabling users to visualize, explore and evaluate patterns in data that change over time. The underlying stream algebra provides the mathematical basis for operating on diverse kinds of streams. The streamer visualization technique provides a smooth transition between spatial and temporal **views** of the **data**. Mapping source and presentation streams into a two-dimensional space provides users with a direct manipulation, non-temporal interface for viewing and editing streams. DIVA was developed to help us analyze both qualitative and quantitative data collected in our research with French air traffic controllers, including **video** of **controllers** at work, audio records of telephone, radio and other conversations, output from tools such as RADAR, and coded logs based on our observations. Although our emphasis is on exploratory data analysis, DIVA's stream architecture should prove useful for a wide variety of multimedia applications. (Author abstract) 32 Refs.

Descriptors: *Hypermedia systems; Interactive computer systems; Data reduction; Graphical user interfaces; Interactive computer graphics; Network protocols

Identifiers: Exploratory data analysis

Classification Codes:

722.4 (Digital Computers & Systems); 723.2 (Data Processing); 722.2 (Computer Peripheral Equipment); 723.5 (Computer Applications)

723 (Computer Software); 722 (Computer Hardware)

72 (COMPUTERS & DATA PROCESSING)

13/5/3 (Item 3 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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04452609 E.I. No: EIP96043156782

Title: Measurements of the perceived dynamic range of a medical imaging workstation

Author: Kenney, Robert S.; Channin, David S.; Prior, Fred W.

Corporate Source: The Pennsylvania State Univ. Coll. of Medicine, Hershey, PA, USA

Conference Title: Medical Imaging 1996: Image Perception

Conference Location: Newport Beach, CA, USA Conference Date: 19960214

Sponsor: SPIE - Int Soc for Opt Engineering, Bellingham, WA USA

E.I. Conference No.: 22520

Source: Proceedings of SPIE - The International Society for Optical Engineering v 2712 1996. Society of Photo-Optical Instrumentation Engineers, Bellingham, WA, USA. p 146-153

Publication Year: 1996

CODEN: PSISDG ISSN: 0277-786X ISBN: 0-8194-2087-5

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications); T
; (Theoretical); X; (Experimental)

Journal Announcement: 9609W3

Abstract: Murch and Weiman have demonstrated that greater than 11 bits of contrast information are perceivable by a human observer. Digital display controllers with 10 or 12 bit digital to analog converters are becoming available. Before attempting to determine if these technologies improve the clinical effectiveness of medical imaging workstations it is first necessary to determine if measurable differences can be produced in the perceived dynamic range (PDR) of the displays. A set of experiments have been performed to determine a baseline PDR for an 8-bit per pixel **display**. This **data** will be used as the control for future measurements at 10 bits per pixel. The experimental design includes all psychovisual factors

that affect an observer's perception of contrast. Stimulus display duration, physical size of the stimulus and training factors were all studied and controlled in the experiments. Simple images are used to avoid complicating the observer's task and display time is kept short to prevent adaptation and boredom effects. Data was collected using four non-radiologists and four radiologists. Each subject had at least normal corrected vision and wore his corrective lenses during each session. All experiments were conducted on a SUN SPARC workstation using an Image Systems (M21P-47S01-2KHB) portrait monitor driven by a modified DOME Imaging Systems (Md2/SUN) 10-bit, grayscale, **video board** initially configured to run in 8-bit mode. Specially developed software was used to control the experiments and to gather and analyze the data. Pizer and Chan's methodology for computing PDR was adapted for the above hardware and software environment. A rating experiment was used to determine the just noticeable difference in contrast for a given reference intensity. Integration over the range of the monitor provides the PDR for that display for one observer. This data is then averaged with all other observations to determine a baseline PDR. These experiments allow for the determination of a baseline PDR for comparison with future hardware configurations. All calibration, control and analysis software is in place such that new hardware can be easily evaluated. 12 Refs.

Descriptors: *Medical imaging; Image quality; Display devices; Computer control systems; Human engineering

Identifiers: Image perception; Workstations; Human observer; Perceived dynamic range; Human factors

Classification Codes:

461.1 (Biomedical Engineering); 741.1 (Light/Optics); 723.5 (Computer Applications); 722.2 (Computer Peripheral Equipment); 461.4 (Human Engineering)

461 (Biotechnology); 741 (Optics & Optical Devices); 723 (Computer Software); 722 (Computer Hardware)

46 (BIOENGINEERING); 74 (OPTICAL TECHNOLOGY); 72 (COMPUTERS & DATA PROCESSING)

13/5/4 (Item 4 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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03582195 E.I. Monthly No: EI9304045715

Title: **High speed fiber-optic link for color video applications.**

Author: Lynn, M.; Miller, G.

Corporate Source: General Motors Corp

Source: SAE (Society of Automotive Engineers) Transactions v 100 n Sect 6 1991, 910873 p 1280-1286

CODEN: SAETA5 ISSN: 0096-736X ISBN: 1-56091-277-4

Language: English

Document Type: JA; (Journal Article) Treatment: X; (Experimental); A; (Applications)

Journal Announcement: 9304

Abstract: A low cost fiber optic data link has been developed for use in automotive video **display** systems. The **data** link multiplexes the CRT controller's red, green, blue, horizontal sync, and vertical sync signals onto a single plastic optical fiber. The multiplexing and LED driver circuitry are integrated into the CRT controller module, and the receiver and demultiplexing circuitry are integrated into the CRT package. The data are transmitted at 57.6MBd. This paper first describes the wiring options available to the system designer in this application and explains why a multiplexed, single fiber system is the best solution. The key features of the multiplexing and demultiplexing schemes used are then discussed, and finally the system performance is examined. (Author abstract)

Descriptors: *FIBER OPTIC COMPONENTS; AUTOMOBILE ELECTRONIC EQUIPMENT; AUTOMOBILE INSTRUMENTS; TELEVISION EQUIPMENT; DATA COMMUNICATION SYSTEMS; MULTIPLEXING; AUTOMOBILE ELECTRIC EQUIPMENT

Identifiers: HIGH SPEED FIBER OPTIC LINK; COLOR **VIDEO** ; CRT **CONTROLLER** ; DEMULTIPLEXING

Classification Codes:

741 (Optics & Optical Devices); 662 (Automotive Design & Manufacture);
716 (Radar, Radio & TV Electronic Equipment); 722 (Computer Hardware)
74 (OPTICAL TECHNOLOGY); 66 (AUTOMOTIVE ENGINEERING); 71 (ELECTRONICS
& COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING)

13/5/5 (Item 5 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)

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03457553 E.I. Monthly No: EIM9207-036352

Title: Displaying three dimensional data using an autostereoscopic technique on microcomputers.

Author: Podger, Nancy E.; Scarpace, Frank L.; Usery, E. Lynn

Conference Title: Technical Papers - 1991 ACSM-ASPRS Annual Convention

Conference Location: Baltimore, MD, USA

E.I. Conference No.: 15064

Source: GIS Tech Pap 91 ACSM ASPRS Annu Conv Technical Papers -
ACSM-ASPRS Annual Convention v 4. Publ by ACSM, Bethesda, MD, USA. p
156-165

Publication Year: 1991

CODEN: TPACEK

Language: English

Document Type: PA; (Conference Paper) Treatment: A; (Applications); X;
(Experimental)

Journal Announcement: 9207

Abstract: The objective of this paper is to present an implementation of a technology, VISIDEP, on microcomputer systems, which would allow three dimensional viewing. For this research, the VISIDEP display technique was implemented on 8-bit SuperVGA **video boards** and a 24-bit IMAGRAPH **video board**. Two procedures to alternately display the images were implemented, one utilizes the LUT color maps of these display boards in such a way that one image is viewed while another image is displayed in a background color. This implementation limits the number of colors displayed to half the availability of the **video board**. The second method of implementing VISIDEP was to display the two images on alternate scan lines, thus, one image will be displayed on even scan lines of the CRT and the other image on odd scan lines of the CRT. This implementation has the advantage that more colors can be assigned to each image, however, only half the spatial resolution of the **video board** is available to each image. (Edited author abstract) 20 Refs.

Descriptors: *IMAGING TECHNIQUES; MAPS AND MAPPING--Computer Applications
; ELECTRON TUBES, CATHODE RAY; COMPUTERS, MICROCOMPUTER

Identifiers: GEOGRAPHIC INFORMATION SYSTEMS; THREE DIMENSIONAL IMAGES;
VISIDEP SYSTEM; SPATIAL RESOLUTION; BINOCULAR PARALLAX INFORMATION

Classification Codes:

723 (Computer Software); 741 (Optics & Optical Devices); 405
(Construction Equipment & Methods)

72 (COMPUTERS & DATA PROCESSING); 74 (OPTICAL TECHNOLOGY); 40 (CIVIL
ENGINEERING)

13/5/6 (Item 1 from file: 2)

DIALOG(R) File 2: INSPEC

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5394863 INSPEC Abstract Number: B9611-6220F-003, C9611-5430-004

Title: ComBiStation: a computer platform for a distributed multimedia computing environment

Author(s): Young-Hwan Lim

Journal: Journal of KISS(C) (Computing Practices) vol.2, no.2 p.
160-81

Publisher: Korea Inf. Sci. Soc,

Publication Date: June 1996 Country of Publication: South Korea

CODEN: CKNCFY

Material Identity Number: E347-96004

Language: Korean Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Describes the development process of a multimedia workstation called ComBiStation, which was developed to support such applications as multimedia CSCW (computer-supported cooperative work) and a computer secretary on a high-speed network. A new platform architecture was defined in order to maximize the capability of processing continuous media and to provide intelligence with a computer secretary in a distributed multimedia computing environment. Based on an in-depth data-flow analysis of typical applications such as video conferencing, remote camcording, CSCW, etc., a general distributed multimedia processing model was defined which consists of three layers: stream, multimedia presentation and hyper-presentation. The model provides synchronization, integration and **presentation** of multimedia **data** in a distributed environment. Multimedia workstations available now have been designed to support stand-alone multimedia applications; they are not well suited to networked multimedia applications. In order for ComBiStation to be a general platform for a distributed multimedia computing environment, our model was realized in the extended multimedia operating system COSMOS (Collaborative Object Sharing for Multimedia Operating System) as the system software and using an integrated audio/ **video board** as hardware. Finally, two application services (audio networking and video conferencing), which are programmed by means of the COSMOS API (application program interface) and DFC (data-flow configurator) respectively, are described to show how ComBiStation can be used as a platform in a distributed environment. (37 Refs)

Subfile: B C

Descriptors: application program interfaces; data flow analysis; distributed processing; groupware; multimedia computing; network operating systems; synchronisation; teleconferencing; workstations

Identifiers: ComBiStation; distributed multimedia computing environment; multimedia workstation; multimedia CSCW; computer-supported cooperative work; computer secretary; high-speed network; platform architecture; continuous media processing; data-flow analysis; video conferencing; remote camcording; stream layer; multimedia presentation layer; hyper-presentation layer; synchronization; multimedia operating system; COSMOS; collaborative object sharing; integrated audio/ **video board** ; audio networking; application program interface; data-flow configurator

Class Codes: B6220F (ISDN and multimedia terminal equipment); B6210P (Teleconferencing); B6210R (Multimedia communications); B6210L (Computer communications); C5430 (Microcomputers); C6130M (Multimedia); C6130G (Groupware); C6150N (Distributed systems software); C5540 (Terminals and graphic displays); C6150J (Operating systems)

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13/5/7 (Item 2 from file: 2)

DIALOG(R) File 2:INSPEC

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5053482 INSPEC Abstract Number: A9520-8770F-018, B9511-7510D-001, C9511-7330-007

Title: A real-time electrical impedance tomograph

Author(s): Edic, P.M.; Saulnier, G.J.; Newell, J.C.; Isaacson, D.

Author Affiliation: Dept. of Electr. Comput. & Syst. Eng., Rensselaer Polytech. Inst., Troy, NY, USA

Journal: IEEE Transactions on Biomedical Engineering vol.42, no.9 p.849-59

Publication Date: Sept. 1995 Country of Publication: USA

CODEN: IEBEAX ISSN: 0018-9294

U.S. Copyright Clearance Center Code: 0018-9294/95/\$04.00

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Electrical properties of tissues in the human body can be imaged using a technology known as Electrical Impedance Tomography. In this modality, sinusoidal electrical currents are applied to the body using electrodes attached to the skin, and voltages that are developed on the electrodes are measured. Using these data, a reconstruction algorithm

Computes the conductivity and permittivity distributions within the body. This paper describes the reconstruction algorithm, image display algorithm, and hardware of a real-time Electrical Impedance Tomograph known as the Real-Time Imaging System. The reconstruction algorithm, executed by a commercially available coprocessor board that resides in a 386-based personal computer, is a modification of the Newton's One Step Error Reconstructor (NOSER) that minimizes algorithm execution time by precomputing many quantities. The image display algorithm, also executed by the coprocessor board, maps the output of the reconstruction algorithm into an image which is displayed using a **video graphics board**. The architecture of the system and execution times of algorithms implemented by the system are discussed. Using the continuous data acquisition mode of the Real-Time Imaging System, data from the thorax of a normal human subject were collected. Admittivity changes in the chest, as a result of respiration and the cardiac cycle, are **presented**. Data that were collected from the leg of a normal subject are shown which demonstrate capabilities of the triggered data acquisition mode of the system, allowing data acquisition synchronization with an electrocardiogram. (18 Refs)

Subfile: A B C

Descriptors: electric impedance imaging; image reconstruction; medical image processing

Identifiers: medical diagnostic imaging; tissue electrical properties; human body; sinusoidal electrical currents; permittivity distribution; image display algorithm; coprocessor board; 386-based personal computer; Newton's one step error reconstructor; algorithm execution time minimization; thorax; normal human subject; chest admittivity changes; leg; triggered data acquisition mode; data acquisition synchronization; respiration; cardiac cycle

Class Codes: A8770F (Electrodiagnostics); A8728 (Bioelectricity); B7510D (Bioelectric signals); B7310J (Impedance and admittance measurement); B6140C (Optical information, image and video signal processing); C7330 (Biology and medical computing); C5260B (Computer vision and image processing techniques)

Copyright 1995, IEE

13/5/8 (Item 3 from file: 2)

DIALOG(R) File 2:INSPEC

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04279198 INSPEC Abstract Number: C9212-5430-007

Title: Present and future of entry systems

Author(s): Mitsui, N.

Journal: Joho Shori vol.33, no.6 p.593-603

Publication Date: 1992 Country of Publication: Japan

CODEN: JOSHA4 ISSN: 0447-8053

Language: Japanese Document Type: Journal Paper (JP)

Treatment: General, Review (G)

Abstract: Personal computers and workstations have been popularized as entry systems. The hardware of these systems has been improved to a remarkable extent in terms of memory capacity, the amount of **display data** and cost/performance ratios. There are various kinds of application software for personal computers and workstations, such as code information (text, data, graphics), multimedia (including still pictures, video images and speech processing), artificial intelligence and object-oriented user interfaces. The author describes the development history of entry system technology, device technology (**video adapters**, packaging, external storage, operating systems and user interfaces), and the environment of entry system utilization. (0 Refs)

Subfile: C

Descriptors: microcomputers; reviews; technological forecasting; workstations

Identifiers: development history; future; entry systems; workstations; memory capacity; **display data**; cost/performance ratios; application software; personal computers; code information; text; graphics; multimedia; still pictures; video images; speech processing; artificial intelligence; object-oriented user interfaces; device technology; **video adapters**;

packaging; external storage; operating systems; user interfaces
Class Codes: C5430 (Microcomputers)

13/5/9 (Item 1 from file: 233)
DIALOG(R) File 233:Internet & Personal Comp. Abs.
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00396811 95PW09-084

Video pushes Diamond back into the limelight

PC World , September 1, 1995 , v13 n9 p259-262, 3 Page(s)

ISSN: 0737-8939

Company Name: Diamond Multimedia; STB Systems

Product Name: Diamond Stealth 64 Video 3200; STB Velocity 64V

Languages: English

Document Type: Buyer and Vendor Guide

Grade (of Product Reviewed): A; A

Hardware/Software Compatibility: IBM PC Compatible

Geographic Location: United States

Presents the monthly listing of the top 10 Windows accelerators. Includes a buyer's guide to the top 10 which gives an overall value rating, price, performance ratings (overall, for business applications, and for graphics applications), installed and maximum RAM (and type of RAM), chip and driver date, and whether the board is Windows 95-certified Plug and Play. A sidebar by Brad Grimes describes the **Display Data Channel** (DDC) which defines the way graphics cards and monitors communicate with one another. For true Plug and Play both the monitor and graphics card must support DDC. ``Best Buy'' honors were awarded to the Diamond Stealth64 Video 3200 (\$254) from Diamond Multimedia (800) and the STB Velocity 64V (\$299) from STB Systems (800). Both these boards also provide video acceleration. Includes one table, one illustration, three photos. (djd)

Descriptors: **Video Controller** ; Accelerator; Hardware Review; Vendor Guide; Window Software; Benchmark Testing

Identifiers: Diamond Stealth 64 Video 3200; STB Velocity 64V; Diamond Multimedia; STB Systems

13/5/10 (Item 2 from file: 233)
DIALOG(R) File 233:Internet & Personal Comp. Abs.
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00390814 95WN07-041

The better to see you with -- Get your Windows video up to speed with these graphics accelerators

Gabel, David; Chen, Janice J; Albinus, Philip; Etra, Ian

Windows Magazine , July 1, 1995 , v6 n8 p260-282, 17 Page(s)

ISSN: 1060-1066

Company Name: Diamond Multimedia Systems; Matrox Graphics; Number Nine Visual Technology

Product Name: Diamond Stealth 64 Video VRAM; Matrox MGA Impression Plus ; Number Nine 9FX Motion771

Languages: English

Document Type: Buyer and Vendor Guide

Grade (of Product Reviewed): A; A; A

Hardware/Software Compatibility: IBM PC Compatible

Geographic Location: United States

Presents a buyer's guide to 10 graphics accelerators for IBM PC compatibles from 10 manufacturers. Features a table comparing 24 specifications of these cards, including maximum refresh rate at various resolutions, ftp/Web site, graphics chip, memory installed and maximum, drivers, and price. Provides benchmark results and a products report card, and includes brief reviews of each card. Awards the WINDOWS Magazine Recommended seal to three accelerators which feature outstanding GUI acceleration, video playback, and ease of use at good prices: the Diamond Stealth 64 Video VRAM (\$399) from Diamond Multimedia Systems (800, 408); Matrox MGA Impression Plus (\$349) from Matrox Graphics (800, 514); and Number Nine 9FX Motion771 (\$329) from Number Nine Visual Technology (800,

617). Sidebars discuss the **Display Data** Channel communications standard, and the MPEG standard for full-screen, full-motion video. Includes 11 photos, 10 screen displays, 10 product summaries, one table, and one graph. (jo)

Descriptors: **Video Controller** ; Accelerator; High Resolution Graphics; Vendor Guide; Digital Video; Standards

Identifiers: Diamond Stealth 64 Video VRAM; Matrox MGA Impression Plus; Number Nine 9FX Motion771; Diamond Multimedia Systems; Matrox Graphics; Number Nine Visual Technology

13/5/11 (Item 3 from file: 233)

DIALOG(R)File 233:Internet & Personal Comp. Abs.

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00367497 94RP11-002

VESA standard designed to ease display configuration

Presentations , November 1, 1994 , v8 n11 p9, 1 Page(s)

ISSN: 1041-9780

Languages: English

Document Type: Feature Articles and News

Geographic Location: United States

Reports that the Video Electronics Standards Association (VESA) approved the **Display Data** Channel (DDC) standard. Says it defines a channel over which a monitor can automatically communicate its capabilities to a host computer system; provides an interface that allows for the configuration of the display controller via the computer; and is designed to support the plug-and-play capabilities of Windows 95. Adds that the implementation of the standard is designed to simplify the configuration of display monitors. (dpm)

Descriptors: **Video Display**; Monitor; **Controller** ; News; Interface ; Standards

13/5/12 (Item 4 from file: 233)

DIALOG(R)File 233:Internet & Personal Comp. Abs.

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00186025 89IT02-025

NEC introduces IPS-100 interface for PS/2 computers

Information Today , February 1, 1989 , v6 n2 p34

Languages: English

Document Type: Product Announcement

Hardware/Software Compatibility: IBM PS/2; IBM PS/2 Compatible; NEC DataSmart monitor

Geographic Location: United States

Reports that NEC Professional Systems Division of Wood Dale, IL (312) has released the IPS-100 PS/2 Computer Interface (\$NA), which links IBM PS/2s and compatibles to NEC DataSmart P-version monitors, allowing for large screen **viewing of data** . It is compatible with all VGA modes, and features 8514A compatibility with NEC DM ``P'' series monitors. Includes one photo. (bs)

Descriptors: Interface; **Video Controller** ; IBM PS/2; IBM PS/2 Compatible; NEC; Monitor

Identifiers: IPS-100 PS/2 Computer Interface; NEC Professional Systems Division

13/5/13 (Item 5 from file: 233)

DIALOG(R)File 233:Internet & Personal Comp. Abs.

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00154309 87PK10-037

Lotus 1-2-3 utility allows monitor to display larger area of spreadsheet
White, Ron

PC Week , Oct 06 1987 , v4 n40 p116, 1 Pages

ISSN: 0740-1604

Languages: English
Document Type: Software Review
Grade (of Product Reviewed): B
Hardware/Software Compatibility: IBM PC; IBM PC Compatible; Lotus
1-2-3

Geographic Location: United States

Gives a favorable review of SeeMORE for 1-2-3 (\$79.95), a Lotus 1-2-3 utility which displays a larger portion of the spreadsheet on the monitor, from Personics Corp., Concord, MA (617). It requires an IBM PC or compatible with a graphics adapter. The higher the resolution of the **display**, the more **data** can be displayed. Four character sizes can be displayed. Finds it very convenient. Includes a screen display.

Descriptors: UTILITY PROGRAM; **VIDEO CONTROLLER**; SOFTWARE REVIEW

Identifiers: SeeMORE for 1-2-3; Personics

13/5/14 (Item 6 from file: 233)
DIALOG(R)File 233:Internet & Personal Comp. Abs.
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00127876 86CP09-017

Zview v. 1.5

David, Terry

Computer Language, Sep 1986, v3 n9 p132-134, 2 Pages

ISSN: 0749-2839

Languages: English

Document Type: Software Review

Geographic Location: United States

A favorable review of Zview v. 1.5 (\$245), a utility that lets a user modify application program screen **displays**, from **Data Management Consultants** of Las Vegas, NV. Notes that it requires a 320K disk drive, MS- or PC-DOS 2.0 or later, Microsoft or Lattice or Aztec C compiler, and a monitor to run on an IBM PC. Reports that it can be very useful for adapting screen displays and handling screen management. Includes one illustration.

Descriptors: **VIDEO CONTROLLER**; SOFTWARE REVIEW; UTILITY PROGRAM

Identifiers: Zview; Data Management Consultants; IBM PC

13/5/15 (Item 7 from file: 233)
DIALOG(R)File 233:Internet & Personal Comp. Abs.
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00110340 85PI09-003

Tseng chip betters IBM EGA

Gordon, Dawn

PC Magazine, Sep 03 1985, v4 n18 p38, 1 Pages

ISSN: 0745-2500

Languages: English

Document Type: Product Announcement

Geographic Location: United States

Reports that Tseng Laboratories has produced an enhanced graphics chip. Says that the ET2000 Series is compatible with the IBM Enhanced Graphics Adapter and will drive "PC-compatible monochrome monitors (50hz), color monitors (50Hz), or dual monitors (50/60Hz)". Mentions special features including the generation of 12 columns of text and the **viewing of data** off-screen.

Descriptors: Product Announcement; Graphics; **Video Controller**;
IBM Personal Computer; Integrated Circuits

Identifiers: ET200 Series; Tseng Laboratories; IBM Personal Computer

13/5/16 (Item 1 from file: 95)
DIALOG(R)File 95:TEME-Technology & Management
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00879102 E95031739273

Ins rechte Bild gerueckt. Programmiertechniken fuer Video-Overlay-Karten
(The programming of video -overlay cards)

Herth, G

DOS International, v87, n4, pp34-41 (mc extra), 1995

Document type: journal article Language: German

Record type: Abstract

ISSN: 0933-1557

ABSTRACT:

Um Videosequenzen in guter Qualitaet abspielen zu koennen, stellen die Video-Overlay-Karten eine gute Alternative dar. Die Video-Overlay-Karten ermoeglichen, das Signal einer externen Videoquelle auf dem Bildschirm darzustellen unter Kontrolle des Rechners. Grossen Zuspruch finden dabei die Produkte der Video-Blaster-Serie von CPS (MovieBlaster). Der Artikel gibt eine allgemein gehaltene Uebersicht ueber die Funktionsweise von Overlay-Karten sowie die zugrundeliegenden Prinzipien der angesprochenen Karten. Ein Testprogramm 'PCVIDEO.PAS' ist aufgelistet, das die Hardware auf unterster Ebene beeinflusst. Folgende Anforderungen an die Hardware einer Overlay-Karte werden gestellt: Digitalisierung des Video-Eingangs-Signals in Echtzeit, Speicherung der Daten, Auswahl eines interessierenden Bereiches aus dem Gesamtbild oder Skalierung des Gesamtbildes auf einen kleineren Bereich, Lesen der Daten synchron zu den VGA-Signalen, Umwandlung in ein analoges Video-Ausgangs-Signal und gesteuertes definiertes Umschalten zwischen VGA- und Video-Signal. Die Bausteine werden im Detail beschrieben. Der Trend geht hin zur Kombination aus VGA-Subsystemen und Videodigitalisiereinheit auf nur einer Einsteckkarte, zur Festlegung auf einheitliche Uebertragungsnormen und zu hochintegrierten Bausteinen, die die CPU vom aufwendigen Datentransfer zwischen Video-Speicher oder Harddisk und dem VGA-Memory entlasten.

DESCRIPTORS: MICROCOMPUTERS; VIDEO SIGNALS; VIDEO TECHNIQUE; VIDEO TRANSMISSION; GRAPHIC **PRESENTATION** ; COMPUTER PROGRAMMING; **DATA** FORMAT; MEMORY MANAGEMENT; DATA COMPRESSION; STANDARDISATION

IDENTIFIERS: VIDEO OVERLAY KARTE; Video-Overlay-Karte; PC; Programmiertechnik

13/5/17 (Item 2 from file: 95)

DIALOG(R)File 95:TEME-Technology & Management

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00762358 E94021055238

Bei Grafik starker Trend zu Ergonomie und Video . Adapter . Grafikkarten. Markttrends

anonym

PC Magazin, v12, n9, pp38,42, 1994

Document type: journal article Language: German

Record type: Abstract

ABSTRACT:

Die technische Entwicklung der Grafikkarten soll dahin gehen, dass VGA, mit 640 x 480 Bildpunkten, nur noch fuer einfachste Anwendungen verwendet wird. Echtfarbandarstellungen, die eine gleichzeitige Darstellung von 16,7 Millionen Farben verlangen, erfordern einen hohen Aufwand an Grafikspeicher und Rechenleistung. Mindestens 800 x 600 Pixel, mit einer Bildwiederholrate von 72 Hz, werden als Einstieg empfohlen. Heutige Bueroanwendungen verlangen bereits die Darstellung von 1024 x 768 Pixeln, um alle Informationen auf dem Bildschirm auch sehen zu koennen. Das verlangt wiederum eine Monitorgroesse von mindestens 15 bis 17 Zoll. 19 bis 21 Zoll sollen es fuer DTP, Bildverarbeitung und CAD-Anwendungen sein. Monitor und Grafikkarte muessen sehr gut aufeinander abgestimmt sein und der Bildschirm sollte 64 KHz verarbeiten koennen, um den derzeitig angebotenen Anwendungen gerecht werden zu koennen. Leistungsfaeheige Prozessoren (DX2, DX4, Pentium) werden oft bereits als Basis benoetigt. Ein schneller Datentransfer zwischen den einzelnen Komponenten des Rechners soll mit einer Busbreite von 64 Bit erreicht werden. Die digitale Videodarstellung auf dem PC wird hier ebenfalls angesprochen.

DESCRIPTORS: GRAPHIC **PRESENTATION** ; GRAPHIC **DATA** PROCESSING; COMPUTER APPLICATIONS; MONITORS--DISPLAY UNIT; SCREENS--DISPLAY; DEVELOPMENTAL TREND ; IMAGE ELEMENTS; IMAGE QUALITY; BUS SYSTEMS; VIDEO TECHNIQUE; COMPUTING SPEED; ERGONOMICS; GRAPHIC CARD
IDENTIFIERS: Grafikkarte; Adapter; Trend

13/5/18 (Item 3 from file: 95)
DIALOG(R)File 95:TEME-Technology & Management
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00745084 E93110686351

Oberflaechlich betrachtet. Betriebssystemunabhaengiges Hardcopy-System

Haubrich, A

MC - Magazin fuer Computerpraxis, v12, n12, pp50-54, 1993

Document type: journal article Language: German

Record type: Abstract

ISSN: 0943-5409

ABSTRACT:

Die Erstellung von Ausdrucken des Bildschirminhaltes (Hardcopy) ist in der Regel abhaengig vom installierten Betriebssystem, deren Oberflaeche und die benutzten Druckertreiber und Drucker. Fuer den Einsatz in heterogenen Netzwerken bietet sich das betriebssystemunabhaengige Hardcopy-System von Seiko Instruments an. Hierbei wird zwischen dem Videoausgang der Graphikkarte und dem Monitor ein sogenanntes VBA eingeschleift (**Video -Bus- Adapter**). Bis zu 16 Arbeitsplaetze koennen so ueber den Videobus nach dem Daisy-Chain-Prinzip die Hardcopies an ein oder zwei Drucker im Netz ausgeben. Im Drucker befindet sich ein Video-Bus-Interfaceboard (CVIN oder CVIS), das die vom **Video -Bus- Adapter** kommenden digitalisierten Videosignale zwischenspeichert. Je nach eingesetztem Drucker koennen so Hardcopies bis hin zu Fotoqualitaet ausgegeben werden.

DESCRIPTORS: GRAPHIC DATA PROCESSING; COMPUTER NETWORKS; PRINTERS; IMAGE PROCESSING; MONITORS-- **DISPLAY** UNIT; **DATA** OUTPUT EQUIPMENTS; VIDEO SIGNALS; ANALOGUE DIGITAL CONVERSION; HARDCOPIES
IDENTIFIERS: VIDEO BUS INTERFACEBOARD; VBA--(**VIDEO** BUS **ADAPTER**); SEIKO INSTRUMENTS; Hardcopy; **Video -Bus- Adapter**

13/5/19 (Item 4 from file: 95)
DIALOG(R)File 95:TEME-Technology & Management
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00649125 E93010260211

Musikalische Bildverarbeitung. CRT- und LCD-Bildschirmcontroller mit integriertem Soundsystem. Peripheriebausteine

anonym

Elektronik, Muenchen, v41, n26, pp14, 1992

Document type: Short journal article Language: German

Record type: Abstract

ISSN: 0013-5658

ABSTRACT:

Das Unternehmen Advanced Risc Machines engagiert sich neben Software-Tools, Speichern und E/A-Controllern auch bei **Video - Controllern** . Der neueste **Video - Controller** (VIDC 20) treibt sowohl konventionelle Roehrenbildschirme als auch Flachbildschirmrme. Ein On-Chip-Soundsystem ermoeeglicht die Uebertragung von Musik in Stereoton. Einsatzmoeglichkeiten findet der VIDC 20 somit beispielsweise in Notebooks zur Ansteuerung des Displays und externer Bildschirme als auch in Embedded Systemen. Dies wird dem Unternehmen ueber die naechsten fuenf Jahre ein kraeftiges Umsatzplus einbringen. Denn laut Frost a. Sullivan sollen 1995 im europaeischen Wirtschaftsraum Flachbildschirme fuer mehr als 1,1 Milliarden Dollar abgesetzt werden.

DESCRIPTORS: VIDEO TECHNIQUE; MICROCONTROLLERS; SCREENS--DISPLAY; IMAGE
TUBES; IMAGE ELEMENTS; FLAT PANEL DISPLAYS; CHIPS--SEMICONDUCTORS; COMPUTER
SOFTWARE; DIGITAL ANALOGUE CONVERSION; LCD--LIQUID CRYSTAL **DISPLAYS** ;
STEREOPHONICS; **DATA** SIGNALLING RATE; TURNOVER--COMMERCE; MARKET
IDENTIFIERS: Videocontroller

Set	Items	Description
S1	2395826	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	74836	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	41834	VIDEO(2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	283811	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	573	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATION OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N)S2
S6	161082	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT()TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	188624	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBLEM? OR ICON? OR GUI OR GUIS OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) ()DOWN()MENU? OR SELECTOR?
S8	381933	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERENT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	5	S1 (S) S2 (S) S3
S10	5	S9 NOT PY>2000
S11	4	S10 NOT PD>20000322
File	15:ABI/Inform(R)	1971-2003/May 12 (c) 2003 ProQuest Info&Learning
File	810:Business Wire	1986-1999/Feb 28 (c) 1999 Business Wire
File	647:CMP Computer Fulltext	1988-2003/Apr W3 (c) 2003 CMP Media, LLC
File	275:Gale Group Computer DB(TM)	1983-2003/May 12 (c) 2003 The Gale Group
File	674:Computer News Fulltext	1989-2003/May W2 (c) 2003 IDG Communications
File	696:DIALOG Telecom. Newsletters	1995-2003/May 12 (c) 2003 The Dialog Corp.
File	98:General Sci Abs/Full-Text	1984-2003/Mar (c) 2003 The HW Wilson Co.
File	583:Gale Group Globalbase(TM)	1986-2002/Dec 13 (c) 2002 The Gale Group
File	47:Gale Group Magazine DB(TM)	1959-2003/May 09 (c) 2003 The Gale group
File	624:McGraw-Hill Publications	1985-2003/May 12 (c) 2003 McGraw-Hill Co. Inc
File	636:Gale Group Newsletter DB(TM)	1987-2003/May 12 (c) 2003 The Gale Group
File	484:Periodical Abs Plustext	1986-2003/May W1 (c) 2003 ProQuest
File	813:PR Newswire	1987-1999/Apr 30 (c) 1999 PR Newswire Association Inc
File	613:PR Newswire	1999-2003/May 13 (c) 2003 PR Newswire Association Inc
File	141:Readers Guide	1983-2003/Mar (c) 2003 The HW Wilson Co
File	553:Wilson Bus. Abs. FullText	1982-2003/Mar (c) 2003 The HW Wilson Co

11/5,K/1 (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
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00567448 CMP ACCESSION NUMBER: IWK19900219S2922

THE 25-YEAR SCREEN TEST - After many fits and starts, videoconferencing systems may finally be on the brink of stardom

Scott Leibs

INFORMATIONWEEK, 1990, n 258, 52

PUBLICATION DATE: 900219

JOURNAL CODE: IWK LANGUAGE: English

RECORD TYPE: Fulltext

SECTION HEADING: TE

WORD COUNT: 1809

TEXT:

It's been more than 25 years since AT&T proudly unveiled the first Picturephone at the 1964 World's Fair. In the ensuing quarter century, the worlds of both pictures and phones have changed enormously, but Picturephones, as far as most corporations were concerned, might just as well have stayed in the display case.

... to be melded into a single wide-screen image. An Apple Computer Inc. Macintosh computer with a **video card** allows the screen to be **split** so half can be used for graphic images or to **display** other data

VIDEO BROWSING

And Bellcore researchers aren't stopping there. A team of engineers, scientists, and experts in...

11/5,K/2 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01362226 SUPPLIER NUMBER: 08168088 (USE FORMAT 7 OR 9 FOR FULL TEXT)
FoxPro: dBASE without tears. (Fox Software's FoxPro dBASE clone) (Software Review) (evaluation)

Turner, Ian

EXE, v4, n8, p20(4)

Feb, 1990

DOCUMENT TYPE: evaluation ISSN: 0268-6872

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 3731 LINE COUNT: 00274

ABSTRACT: Fox Software's FoxPro dBASE clone adds text-based windows, pull-down menus and dialog boxes to the standard dBASE database language, which enhances the interface considerably. The program offers a range of features and functions but still runs on systems with only 512Kbytes of RAM; the program is claimed to be fully dBASE III/IV and FoxBASE+ compatible while adding about 140 extended or new commands. All setup and processing functions are available through a function call from a program or the System menu bar when working interactively; dBASE requires users work through the Assistant or Command Center for such operations. FoxPro is powerful and full-featured while being easier to use than dBASE; the company is reportedly working on a SQL interpreter as well as the true compiler, both of which are expected soon. The program is well worth considering by professional dBASE developers.

COMPANY NAMES: Fox Software Inc.--Products

DESCRIPTORS: Data Base Languages; Compiler; Compatible Software; Evaluation; DBMS

SIC CODES: 7372 Prepackaged software

TRADE NAMES: Microsoft FoxPro (Database application development software)
--evaluation

FILE SEGMENT: CD File 275

TEXT:

...redirected to other windows if required. At more of that later.
Support is provided for all normal **video cards**, using the commands SET DISPLAY TO EGA43NGA50, which switches the display to 43 lines in EGA or... extensions to the FIELDS clause, such as N. Using these, it is possible to perform Bounds checking, **separate** colour definition for each individual field, and conditional entry into a field dependent on its current contents
...

...layout and a standard EDIT layout, which can use a FORMAT file if required. You can even **split** the browse window to **display** the **data** in both formats within the same window, giving linked or unlinked views of the currently selected record. This **split** can be varied in size by dragging the **divider** icon with the mouse. You can have a BROWSE Window contained within your own windows as defined...

11/5,K/3 (Item 2 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
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01204969 SUPPLIER NUMBER: 06329711 (USE FORMAT 7 OR 9 FOR FULL TEXT)
For cost-performance, partition RISC system on bus parameters.

Cates, Ron
Electronic Design, v35, n27, p121(6)
Nov 12, 1987

ISSN: 0013-4872 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 3158 LINE COUNT: 00255

CAPTIONS: The main parts of the RISC computer fit into just four ICs.
(chart); The memory controller generates the timing and control signals required. (chart); The RISC processor supplies the computational element in the system. (chart); The video controller offers a highly flexible choice of display formats. (chart)

SPECIAL FEATURES: illustration; chart
COMPANY NAMES: VLSI Technology Inc.--Innovations
SIC CODES: 3571 Electronic computers
FILE SEGMENT: TI File 148

... longest instruction and therefore is the limiting factor in interrupt-response time.

Next in importance, the VL86C310 **video controller** (VIDC) gives a choice of display formats in both color and high-resolution monochrome (Fig. 4). The...

...performing video operations, the VIDC also generates high-quality stereo sound with up to eight channels of **separate** stereo positions. The circuit accepts video data in a packed pixel format from the memory, serializes the data into pixel information, and **presents** the **data** to the color-mapping RAM (video palette) that converts it to analog values suitable for driving an...

11/5,K/4 (Item 1 from file: 583)
DIALOG(R)File 583:Gale Group Globalbase(TM)
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04312530
DCA LAUNCHES REMOTE 2 VERSION 2.1
US - DCA LAUNCHES REMOTE 2 VERSION 2.1
Datamation (DTN) 15 May 1991 p102
ISSN: 0011-6963

Digital Communications Associates (Alpharetta, GA) has launched Remote 2 communications software Version 2.1. The software allows a PC to remotely control the operations of a **separate** PC, and it features automatic graphics translation, which enables the PC to **display** systems **data** with

Various **video cards** . A variety of emulations are offered including VT220 and VT100, and XMODEM and CompuServe B+ file transfer protocols are also included.

PRODUCT: Communications Software (7372CS); CAD/CAM Mechanical Software (COSW);

EVENT: PRODUCTS, PROCESSES & SERVICES (30);

COUNTRY: United States (1USA); NATO Countries (420); South East Asia Treaty Organisation (913);

... communications software Version 2.1. The software allows a PC to remotely control the operations of a **separate** PC, and it features automatic graphics translation, which enables the PC to **display** systems **data** with various **video cards** . A variety of emulations are offered including VT220 and VT100, and XMODEM and CompuServe B+ file transfer...

Contracting State: CH 910918, LI 910918, SE 910918

Lapse: 920715 B1 Date of lapse of the European patent in a Contracting State: AT 910918, CH 910918, LI 910918, SE 910918

Lapse: 920902 B1 Date of lapse of the European patent in a Contracting State: AT 910918, BE 910918, CH 910918, LI 910918, SE 910918

Oppn None: 920909 B1 No opposition filed

Lapse: 991020 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19910918, BE 19910918, CH 19910918, LI 19910918, IT 19910918, SE 19910918,

Lapse: 991229 B1 Date of lapse of European Patent in a contracting state (Country, date): AT 19910918, BE 19910918, CH 19910918, LI 19910918, IT 19910918, LU 19920229, SE 19910918,

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	520
CLAIMS B	(German)	EPBBF1	406
CLAIMS B	(French)	EPBBF1	602
SPEC B	(English)	EPBBF1	7522
Total word count - document A			0
Total word count - document B			9050
Total word count - documents A + B			9050

INTERNATIONAL PATENT CLASS: G06F-007/02 ...

...SPECIFICATION which is controlled by a control signal outputted from the picture taking control unit 21 so as to be properly focused and so as to provide a proper and appropriate magnification factor and sensitivity. And...

...reference printed circuit board assembly 25-1) which is supplied from the picture taking unit 16, and **extracts** therefrom the data relating to the positions and the shapes of the component parts 28-1 through...

...the component parts as well as the sequence of the processing, according to the G-B subtraction **video** signal. Subsequently, in the so called inspection mode when the subject printed circuit board assembly 25-2 is mounted on...

...subject printed circuit board assembly 25-2, which is supplied from the picture taking unit 16, and **extracts** therefrom the data (the inspection data) relating to the positions and the shapes of the component parts...

...video signal form the RGB color picture signals which it receives from the control unit 36, and **extracts** various parameters, such as the areas, the shapes, the positions, and the colors, of the component parts ...

...assembly 25-1 or the subject printed circuit board assembly 25-2 by processing the picture image **data** supplied from the control unit 36 according to a predetermined processing sequence or a processing sequence inputted...

12/5,K/13 (Item 13 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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00221619

Digital image rotation.
Digitale Bildumdrehung.

Rotation d'images Amerique.

PATENT ASSIGNEE:

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, (US), (applicant designated states: DE;FR;GB;IT)

INVENTOR:

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LEGAL REPRESENTATIVE:

Hill, Cecilia Ann et al (31832), Rank Xerox Patent Department Albion
House, 55 New Oxford Street, London WC1A 1BS, (GB)

PATENT (CC, No, Kind, Date): EP 216501 A2 870401 (Basic)

EP 216501 A3 881102

EP 216501 B1 930609

APPLICATION (CC, No, Date): EP 86306430 860820;

PRIORITY (CC, No, Date): US 769525 850826

DESIGNATED STATES: DE; FR; GB; IT

INTERNATIONAL PATENT CLASS: G06F-015/62 ; G06K-015/02

CITED PATENTS (EP A): US 4052699 A; US 4168488 A

ABSTRACT EP 216501 A2

A method of rotating a digital image during real time involves conveying bit map data into and out of a band buffer (12) and shift register (18) to an image output terminal to provide either portrait or landscape printing during print time. In particular, for portrait printing; an 8 to 1 multiplexer (16) selects one of 8 bits to send for each raster scan. For landscape printing, the 8 bits are sent to a shift register (18) and then all 8 bits are sent as part of the raster scan.

ABSTRACT WORD COUNT: 94

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 870401 A2 Published application (Alwith Search Report
;A2without Search Report).

Change: 871007 A2 Representative (change)

Change: 881019 A2 Obligatory supplementary classification
(change)

Search Report: 881102 A3 Separate publication of the European or
International search report

Change: 890201 A2 Representative (change)

Change: 890524 A2 Representative (change)

Examination: 890705 A2 Date of filing of request for examination:
890428

Examination: 891123 A2 Date of despatch of first examination report:
891006

Grant: 930609 B1 Granted patent

Oppn None: 940601 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	645
CLAIMS B	(German)	EPBBF1	514
CLAIMS B	(French)	EPBBF1	628
SPEC B	(English)	EPBBF1	5810

Total word count - document A 0

Total word count - document B 7597

Total word count - documents A + B 7597

INTERNATIONAL PATENT CLASS: G06F-015/62 ...

...SPECIFICATION the Band Buffers, an Image/Font Memory holding scanned images and character bitmaps for each font, a **Video Controller** which **retrieves data** from the Image/Font memory and loads the band buffers with video data, a **Print Controller** which interprets character requests and generates commands for the **Video Controller** and an **Output Controller** which **extracts video data from** the band buffers after they are filled by the **Video Controller** and **presents the video data** to the **image** output terminal in scan line form.

Yet another rotation system is described in US-A-4 052...the choice

corresponding to the configuration of the Band Buffers.

In accordance with the present invention, the **Video Controller** is enhanced to implement both The Standard Configuration and the Folded Configuration of the Band Buffers and...

...6) with the configuration of the band buffer memory as an input selected for each page. The **video** data is **extracted** from the bytes read from the band buffer 12 with a demultiplexor 14 which **routes** the **data** for The Standard Configuration to an 8:1 multiplexor 16 which selects the appropriate data bit and...status information on the number of slices which have been transferred for each character. The print controller **extracts** the starting addresses and the width from the font index. For the first character slice, these values...this algorithm is to rotate the data within the Band Buffer 180 degrees. This Output Controller will **present** the **data** at the 8th bit position within the Band Buffer as the first scan line to the Image...

...of each scan line, etc. To accomplish 180 degree rotation of the entire page, the image is **extracted** from memory in reverse order, i.e. the last slice is output as the first band, etc.

ROTATED STANDARD OUTPUT (see image in original document)

In addition, the **Video Controller** must process the slices of the image in reverse order:

INVERSE PORTRAIT PRINTING

STEP 1. Generation of...during the transfer of font data into the band buffers. Additional registers can be used in the **video controller** in pipeline to perform this computation:

```
BandBufferData( sub(i)) <- FontMemoryData( sub(i-1)) OR
FontMemoryData( sub(i...
```

...i+1))

However, since complete output of a character will take place as multiple transfers by the **video controller**, how do the pieces of the character get stitched together on **separate** transfers? Since it has already been transferred, it is possible to use the data already in the

...

12/5,K/14 (Item 14 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00203745

Image scanner interacting with a processor.

Ein mit einem Prozessor wechselwirkender Bildabtaster.

Capteur d'image reagissant reciproquement avec un processeur.

PATENT ASSIGNEE:

International Business Machines Corporation, (200120), Old Orchard Road, Armonk, N.Y. 10504, (US), (applicant designated states: DE;FR;GB)

INVENTOR:

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Yanagisawa, Hiroshi, 6-1-501 5-chome Isogo Isogoku, Yokohama-shi, Kanagawa-ken, (JP)

LEGAL REPRESENTATIVE:

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PATENT (CC, No, Kind, Date): EP 207343 A2 870107 (Basic)
EP 207343 A3 880831
EP 207343 B1 920826

APPLICATION (CC, No, Date): EP 86108086 860613;

PRIORITY (CC, No, Date): JP 85146919 850705

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: **G06F-003/00**

CITED PATENTS (EP A): DE 3342004 A

CITED REFERENCES (EP A):

ABSTRACT EP 207343 A2

The invention is devised to start application of clock pulses to a shift register in a sensor unit of an image scanner in synchronization with a read signal from a processor in a method for controlling the image scanner so as to enable to easily and directly fetch video data outputted from the shift register into the processor as its input data without using a DMA (Direct Memory Access) method or a program loop method that requires special measures in aspects of hardware and software.

In addition, the invention is devised to provide a data disposing cycle in the operation mode of the shift register in addition to the data reading cycle, so that, even when interruption from others causes the operation of the processor fetching the video data from the shift register to exceed a predetermined scanning time before completion of the operation, the data disposing cycle is inserted right after completion of the operation to dispose high level data that has been sensed for a long period of time by a photosensor of the sensor unit, and normal level data or image information properly representing an image being scanned is obtained.

Furthermore, the invention reduces significantly processing load of the processor with a simple hardware constitution by adding the operation of a run length counter etc. so as to enable high speed linear data compression.

ABSTRACT WORD COUNT: 231

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 870107 A2 Published application (Alwith Search Report ;A2without Search Report)
Examination: 870729 A2 Date of filing of request for examination: 870529
Change: 880817 A2 Obligatory supplementary classification (change)
Change: 880824 A2 Obligatory supplementary classification (change)
Search Report: 880831 A3 Separate publication of the European or International search report
Change: 881207 A2 Representative (change)
Examination: 901219 A2 Date of despatch of first examination report: 901102
Change: 920304 A2 Title of invention (German) (change)
Change: 920304 A2 Title of invention (English) (change)
Change: 920304 A2 Title of invention (French) (change)
Grant: 920826 B1 Granted patent
Change: 930331 B1 Representative (change)
Change: 930512 B1 Representative (change)
Oppn None: 930818 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	451
CLAIMS B	(German)	EPBBF1	339
CLAIMS B	(French)	EPBBF1	423
SPEC B	(English)	EPBBF1	2075
Total word count - document A			0
Total word count - document B			3288
Total word count - documents A + B			3288

INTERNATIONAL PATENT CLASS: G06F-003/00

...SPECIFICATION is stopped until the next read signal is emitted by the processor 15.

As previously described, the **video data** is directly read by the processor through an 8-bit serial-parallel register 12. This cycle is...

...of program control as little as possible. If the processor is

interrupted, it suppresses the reading of video data. This significantly delays the video data read operation for a particular line within a predetermined scanning...

...processor only receives run length values and is not used to determine such values.

The hardware configuration shown in Figure 3 consists of a run length counter 30, a gate means 31, a latch means...

...CCD shift register and the run length counter 30 and to interrupt the processor. At that time, the processor receives the value of the run length counter 30 to perform a processing operation, such as...

12/5,K/15 (Item 15 from file: 348)
DIALOG(R) File 348:EUROPEAN PATENTS
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00180571

Video system controller with a row address override circuit.

Steuerlogik für ein Videosystem mit einer Schaltung, welche die Zeilenadresse ausser Kraft setzt.

Contrôle de système vidéo avec un circuit de dépassement de l'adresse de ligne.

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 182454 A2 860528 (Basic)
EP 182454 A3 880323
EP 182454 B1 940202

APPLICATION (CC, No, Date): EP 85305225 850723;

PRIORITY (CC, No, Date): US 633367 840723; US 633383 840723; US 633384
840723; US 633385 840723; US 633386 840723; US 633387 840723; US 633388
840723; US 633389 840723

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-003/14 ; G09G-001/00

CITED PATENTS (EP A): US 4566005 A; US 4125873 A; US 4104624 A; US 4424372
A; US 4390780 A; US 4286320 A

ABSTRACT EP 182454 A2

Video system controller with a row address override circuit.

The present invention is a video system which includes a data processor (1), such as a microprocessor, for processing data, a video memory (5) for storing data from the data processor corresponding to an image to be displayed, a display (11), such as a raster scan cathode ray tube, for displaying the image data stored in the video memory means, and a video system controller (3) connected to the video memory (5) for controlling the transfer of data from the video memory (5) to the display (11) and between the data processor (1) and the video memory (5). The video memory (5) is preferably a multiport dynamic random access memory including an addressable memory array. The video system controller (3) performs a number of functions including refresh of the dynamic random access memory, multiplexing of the various access requests of the video memory and control of the blanking interval of the display. This is accomplished

by having a first portion which operates synchronously with the video memory (5) and a second portion which operates synchronously with the data processor (1). The transfer operations in the video system controller are preferably controlled through the use of a programmable state machine which manipulates inputs in a logic array.
ABSTRACT WORD COUNT: 217

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 860528 A2 Published application (Alwith Search Report
;A2without Search Report)
Search Report: 880323 A3 Separate publication of the European or
International search report
Examination: 881117 A2 Date of filing of request for examination:
880920
Examination: 900228 A2 Date of despatch of first examination report:
900112
Grant: 940202 B1 Granted patent
Oppn None: 950125 B1 No opposition filed

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	983
CLAIMS B	(German)	EPBBF1	777
CLAIMS B	(French)	EPBBF1	1135
SPEC B	(English)	EPBBF1	16509
Total word count - document A			0
Total word count - document B			19404
Total word count - documents A + B			19404

INTERNATIONAL PATENT CLASS: G06F-003/14 ...

... G09G-001/00

...SPECIFICATION during the time the column address enable low byte, CEL, which is a control line that is **present** on **data** bus 23 goes low, for determining whether the register access is a read or a write. The...

...timing requirements of the CRT monitor 11. Both interlaced and non-interlaced scan modes are available. The **video** system **controller** can be programmed to lock to an externally generated sync signal, e.g. for an application in...

12/5,K/16 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00993612 **Image available**

GENERATING AND IMPLEMENTING A COMMUNICATION PROTOCOL AND INTERFACE FOR HIGH DATA RATE SIGNAL TRANSFER

CREATION ET MISE EN OEUVRE D'UN PROTOCOLE DE COMMUNICATION ET D'UNE INTERFACE DE TRANSFERT DE SIGNAUX A HAUT DEBIT

Patent Applicant/Assignee:

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Legal Representative:

OGROD Gregory D (agent), 5775 Morehouse Drive, San Diego, CA 92121, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200323587 A2 20030320 (WO 0323587)
Application: WO 2002US28461 20020906 (PCT/WO US0228461)
Priority Application: US 2001317858 20010906; US 200120520 20011214; US
2002356892 20020213

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU

CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO
RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-003/00**

International Patent Class: A63F-013/06; H04L-012/64; H04L-029/06;

H04J-003/06; H04Q-007/22; H04L-012/28; G06K-007/00; H04N-007/26;

G06F-013/38 ; H04N-005/44

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 39493

English Abstract

A data Interface for transferring digital data between a host and a client over a communication path using packet structures linked together to form a communication protocol for communicating a pre-selected set of digital control and presentation data. The signal protocol is used by link controllers configured to generate, transmit, and receive packets forming the communications protocol, and to form digital data into one or more types of data packets, with at least one residing in the host device and being coupled to the client through the communications path. The interface provides a cost-effective, low power, bi-directional, high-speed data transfer mechanism over a short-range "serial" type data link, which lends itself to implementation with miniature connectors and thin flexible cables which are especially useful in connecting display elements such as wearable micro-displays to portable computers and wireless communication devices.

French Abstract

L'invention porte sur une interface de donnees transferant des donnees numeriques entre un hote et un client via une voie de communication en utilisant des structures de paquets liees ensemble de maniere a constituer un protocole de communication de transfert d'un ensemble preselectionne de donnees de commande numerique et de presentation. Un protocole de signal est utilise par des controleurs de liens pour creer, emettre, et recevoir les paquets formant le protocole de communications et grouper les donnees numeriques en un ou plusieurs types de paquets dont l'un au moins, qui reside dans le dispositif hote, est relie au client via une voie de communications. Ladite interface est un moyen mecanisme economique, peu puissant, bidirectionnel et tres rapide de transfert de donnees sur des liaisons serieles a courte portee, et qui se prete au montage dans des connecteurs miniatures et dans des cables souples fins specialement adaptes au raccordement d'elements d'affichage tels que les micro-affichages a des ordinateurs portables et a des dispositifs de communication sans fil.

Legal Status (Type, Date, Text)

Publication 20030320 A2 Without international search report and to be republished upon receipt of that report.

Main International Patent Class: **G06F-003/00**

...International Patent Class: **G06F-013/38**

Fulltext Availability:

Detailed Description

Detailed Description

... or around 9.21 to 125.75 Megabytes per second (MBps). In addition, one may desire to **present** audio **data** in conjunction with images, such as for a multimedia presentation, or as a **separate** high resolution audio presentation, such as CD quality music. Additional signals dealing with interactive commands, controls, or...cost, or simply consume too much

power. [0011] There are other known interfaces such as the Analog Video Graphics Adapter (VGA), Digital Video Interactive (DVI) or Gigabit Video Interface (GVI) interfaces. The first two of these are...

12/5,K/17 (Item 2 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00986962 **Image available**

VCD-ON-DEMAND SYSTEM AND METHOD
SYSTEME ET PROCEDE DE VCD SUR DEMANDE

Patent Applicant/Assignee:

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, US (Nationality), (For all designated states except: US)

Patent Applicant/Inventor:

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(Residence), US (Nationality), (Designated only for: US)
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, US (Nationality), (Designated only for: US)
SHEN Quincy Kun-Hsu, 19263 Brockton Lane, Saratoga, CA 94070, US, US
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Legal Representative:

CHOU Chien-Wei (et al) (agent), Oppenheimer Wolff & Donnelly LLP, 1400
Page Mill Road, Palo Alto, CA 94304, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200317099 A1 20030227 (WO 0317099)
Application: WO 2001US25558 20010814 (PCT/WO US0125558)
Priority Application: WO 2001US25558 20010814

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU
CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR
KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE
SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/455

International Patent Class: G06F-017/50

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description
Claims

Fulltext Word Count: 92929

English Abstract

The disclosed technology is called VCD on demand. In a typical system, the EDA tool incorporating the VCD on-demand technology has the following high level attributes: (1) RCC-based parallel simulation history compression and recording, (2) RCC-based parallel simulation history decompression and VCD file generation, and (3) On-demand software regeneration for a selected simulation target range and design review without simulation rerun. Each of these attributes will be discussed in greater detail below. When the user selects a simulation range (item 105), the RCC System records a highly compressed version of the primary inputs from the test bench process. The user then selects a narrower region, called the simulation target range (item 135), within the simulation session range for a more focused analysis. The RCC System dumps the hardware state information (i.e., primary outputs) of the hardware model into a VCD file. The RCC System then allows the user to proceed directly to view the VCD file from the beginning of the simulation target range (item 105) without having to rerun the entire simulation from the very beginning of the simulation session range.

French Abstract

L'invention se rapporte a une technique appelee VCD sur demande. Dans un

systeme type, l'util EDA integrant la technique VCD sur demande possede les attributs de haut niveau suivants : (1) compression et enregistrement de l'historique de la simulation parallele de type RCC ; (2) decompression de l'historique de la simulation parallele de type RCC, et generation de fichiers VCD ; et (3) regeneration de logiciels sur demande pour une plage cible de simulation selectionnee, et revision de la conception sans reexecution de la simulation. Chacun de ces attributs seront decrits plus en detail. Lorsque l'utilisateur selectionne une plage de simulation (105), le systeme RCC enregistre une version hautement compressee des entrees primaires issues du processus d'evaluation des performances. L'utilisateur selectionne ensuite une zone plus reduite, appelee la plage cible de simulation (135), dans la plage de session de simulation, en vue d'une analyse plus concentree. Le systeme RCC vide les informations d'etat de materiel (autrement dit, les entrees primaires) du modele de materiel dans un fichier VCD. Le systeme RCC permet ensuite a l'utilisateur de visualiser directement le fichier VCD a partir du debut de la plage cible de simulation (105), sans devoir reexecuter la simulation entiere a partir du tout debut de la plage de session de simulation.

Legal Status (Type, Date, Text)

Publication 20030227 A1 With international search report.

Examination 20030327 Request for preliminary examination prior to end of 19th month from priority date

Main International Patent Class: G06F-009/455

International Patent Class: G06F-017/50

Fulltext Availability:

Detailed Description

Detailed Description

... FIG. 73 shows the timing diagram of the data-out portion of the control logic.

FIG. 74 shows a board layout of the RCC hardware array in accordance with one embodiment of the present invention.

FIG. 75(A...i,j)EECKT k

Here, M is the connectivity matrix. One embodiment of the connectivity matrix is shown in FIG. 7. The distance is calculated for each gate-to-gate connection requiring an interconnection. Thus...353 refines the placement initially selected by the coarsegrain placement operation 352. Here, initial clusters may be split up if such an arrangement will increase the @ optimization. For example, assume logic elements X and Y ...the fine-grain placement operation 353, logic elements X and Y may now be designated as a separate cluster B or made part of another cluster C and designated for placement in FPGA chip 2...

...the user's circuit design to specific FPGAs, is then generated.

The determination of how clusters are split up and placed in certain chips is also based on placement cost, which is calculated through a...

12/5,K/18 (Item 3 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00982583 **Image available**

COMPUTER SOFTWARE PRODUCT FOR DATA SECURITY OF SENSITIVE WORDS CHARACTERS OR ICONS

PRODUIT LOGICIEL INFORMATIQUE DESTINE A LA SECURITE DES DONNEES DE MOTS, DE CARACTERES, OU D'ICONES SENSIBLES

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Legal Representative:

KAIN Robert C Jr (agent), Fleit, Kain, Gibbons, Gutman & Bongini, P.L., Suite 100, 750 Southeast Third Avenue, Fort Lauderdale, FL 33316-1153, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200312666 A1 20030213 (WO 0312666)

Application: WO 2002US21760 20020710 (PCT/WO US0221760)

Priority Application: US 2001916397 20010727; US 20018209 20011206; US 20018218 20011206; US 200215525 20020523

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW

(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-015/16

International Patent Class: G06F-017/00

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Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 25364

English Abstract

The computer software product extracts security sensitive words, data, credits card or account numbers, icons, images or audio or video data from input data (100), thereby creating extract data (104) and reminder data (104). Extract data and remainder data are separately stored (110, 108, respectively) locally on a PC memory (116, 168, 160, 162) or on another computer in a LAN (142, 146) or WAN or on the Internet (154, 157). Encryption (238) and decryption (424, 425, 426, 430) may be utilized to enhance security (including transfers of data and memory map (158) location). Reconstruction of the data (FIG.1B and FIG. 3) is permitted only in the presence of predetermined security clearance levels (226) and full and partial reconstruction is possible with multiple levels of security (226). The data security system may be used to transparently establish and manage a separation of user-based communities of interest based upon crypto-graphically separated, need to know, security levels. The security system, as an adaptive system (FIG.8) protects against electronic attacks (460) and environmental events, generates attacks warnings (462) extracts security sensitive data, stores the data and permits full or partial reconstruction (478). Parsing (556) and dispersion (560) aspects enable users to maintain security (FIG.10). Data security for e-mail (FIG.11A) and browser programs (FIG.12A) is provided. Remainder data is sent to the e-mail addressee (624) or a browser target (712) (a designated web server). The addressee or intended recipient is permitted to retrieve the extracted data from said extract store only in the presence of a security clearance (628, 714) and hence, reconstruct the source e-mail or browser-input data with the extracted data. In other systems, the addressee reconstructs the email by decryption and integration (621, 623, 629). FIG. 1A is generally illustrative.

French Abstract.

Le produit logiciel informatique extrait des mots sensibles, des donnees, des numeros de carte de credit ou de compte, des icones, des images ou des donnees audio ou video de securite a partir de donnees d'entree (100), creant ainsi des donnees d'extraction (104) et des donnees restantes (104). Les donnees d'extraction et les donnees restantes sont stockees separement (110, 108, respectivement) localement sur une memoire de PC (116, 168, 160, 162) ou sur un autre ordinateur dans un reseau

local d'entreprise (LAN) (142, 146) ou un reseau longue portee (WAM) ou sur Internet (154, 157). Le chiffrement (238) et le dechiffrement (424, 425, 426, 430) peuvent etre utilises en vue d'ameliorer la securite (notamment les transferts de donnees et l'emplacement du topogramme de la memoire (158)). La reconstruction des donnees (fig. 1B et fig. 3) est uniquement autorisee en presence de niveaux d'habilitation de securite (226) et la reconstruction totale et partielle est possible avec des niveaux multiples de securite (226). Le systeme de securite des donnees peut etre utilise en vue d'etablir de maniere transparente et de gerer une separation des collectivites d'interet basee utilisateur fondee sur des niveaux de securite separee de maniere cryptographique a diffusion restreinte. Le systeme de securite en tant que systeme adaptatif (fig. 8) protege contre les attaques electroniques (460) et les evenements environnementaux, genere une alerte en cas d'attaque (462), extrait des donnees sensibles de securite, stocke ces donnees et permet leur reconstruction totale ou partielle (478). Les aspects d'analyse (556) et de dispersion (560) permettent aux utilisateurs de maintenir la securite (fig. 10). L'invention concerne egalement une securite des donnees de courrier electronique (fig. 11A) et des programmes de navigation (fig. 12A). Les donnees restantes sont envoyees au destinataire du courrier electronique (624) ou a une cible de navigation (712) (un serveur Web designe). Le destinataire peut recuperer les donnees extraites de la memoire d'extraction uniquement en presence d'une habilitation de securite (628, 714) et reconstruit le courrier electronique source ou les donnees d'entree de navigation avec les donnees extraites. Dans d'autres systemes, le destinataire reconstruit le courrier electronique par dechiffrement et integration (621, 623, 629). Generalement, la figure 1A est donnee a titre indicatif.

Legal Status (Type, Date, Text)

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Main International Patent Class: **G06F-015/16**

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Fulltext Availability:

Detailed Description

Detailed Description

... monitor 163 is fast, the display 163 will show the common or the remainder data and then **show** the **extracted data** such that I/O the user could not humanly perceive a difference in the document. However, the...

...a single document. There is only a visual presentation of the combined document by 3 interleaving the **extracted data** with the common or remainder in the video memory 169. Step 212 notes that the user...

12/5,K/19 (Item 4 from file: 349)

DIALOG(R) File 349: PCT FULLTEXT

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00975264 **Image available**

INTER-CHIP COMMUNICATION SYSTEM

SYSTEME DE COMMUNICATION INTER-PUCES

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Application: WO 2001US26625 20010823 (PCT/WO US0126625)

Priority Application: US 2001900124 20010706

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CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR
KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE
SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
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Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 110858

English Abstract

The inter-chip communication system transfers signals across chip boundaries only when the signals change value. Thus no signals are wasted and every event signal has a fair chance of achieving communication across the boundary. The system includes a series of event detectors (3062, 3063, 3064) that detect changes in signal values and packet schedulers (3036, 3037, 3038) which can then schedule the transfer of these changed signal values to another designated chip. Working with a plurality of signal groups that represents signals at the separated connections, the event detectors (3062, 3063, 3064) detect events or changes in the signal values. When an event or signal change has been detected, the event detector alerts the packet scheduler (3036, 3037, 3038). The packet scheduler employs a token ring scheme. When the scheduler receives a token and detects an event, the packet scheduler grabs the token and schedules the transmission of this packet in the next packet cycle. If, however, the packet scheduler receives the token but does not detect an event, it will pass the token to the next scheduler. At the end of each packet cycle, the packet scheduler that grabbed the token will pass the token to the next logic associated with another packet.

French Abstract

Système de communication inter-puces ne transférant des signaux au-delà des limites de la puce qu'en cas de modification de la valeur des signaux. De cette manière, aucun signal n'est gaspillé et chaque signal d'événement a une chance d'être transmis au-delà de la limite. Le système comprend une série de détecteurs (3062, 3063, 3064) d'événements qui détectent les modifications de valeur des signaux, et des ordonnanceurs (3036, 3037, 3038) de paquets qui peuvent ensuite ordonner le transfert des valeurs modifiées de signaux vers une autre puce désignée. Fonctionnant avec une pluralité de groupes de signaux qui représentent des signaux à des connexions séparées, les détecteurs (3062, 3063, 3064) d'événements détectent des événements ou modifications des valeurs des signaux. Lorsqu'un événement ou qu'une modification de signal a été détecté(e), le détecteur d'événements alerte l'ordonnanceur (3036, 3037, 3038) de paquets. L'ordonnanceur de paquets utilise un système en anneau à jeton. Quand l'ordonnanceur reçoit un jeton et détecte un événement, l'ordonnanceur de paquets saisit le jeton et ordonne la transmission de ce paquet dans le cycle de paquets suivant. Mais si l'ordonnanceur de paquets reçoit le jeton sans avoir détecté d'événement, il passe le jeton à l'ordonnanceur suivant. À la fin de chaque cycle de paquets, l'ordonnanceur de paquets qui a saisi le jeton passe celui-ci à la logique suivante associée à un autre paquet.

Legal Status (Type, Date, Text)

Publication 20030116 A1 With international search report.

Main International Patent Class: **G06F-013/00**

Fulltext Availability:

Detailed Description

Detailed Description

... a system that generates VCD on demand without simulation rerun. In accordance with one embodiment of the **present** invention, the VCD on-demand technology as described herein incorporates the following high level attributes: (1) RCC...times t0 and t1 may be thousands of simulation time periods apart.

Because one embodiment of the **present** invention will be implemented in the RCC System, references to various components of the RCC System shown ...

12/5,K/20 (Item 5 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00876811 **Image available**

SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR DEVICE, OPERATING SYSTEM,
AND NETWORK TRANSPORT NEUTRAL SECURE INTERACTIVE MULTI-MEDIA MESSAGING
SYSTEME, PROCEDE ET PRODUIT PROGRAMME D'ORDINATEUR POUR APPAREIL, SYSTEME
D'EXPLOITATION ET MESSAGERIE MULTIMEDIA INTERACTIVE RESEAU, NEUTRE ET
SECURISEE

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2000627645 20000728; US 2000628205 20000728; US 2000706606 20001104; US
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2000706615 20001104; US 2000706616 20001104; US 2000706617 20001104; US
2000706621 20001104; US 2000706661 20001104; US 2000706664 20001104; US
2001271455 20010225; US 2001912715 20010725; US 2001912936 20010725; US
2001912905 20010725; US 2001912773 20010725; US 2001912885 20010725; US
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CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD
SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
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Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 169299

English Abstract

System, method, signal, operating model, and computer program for
electronic messaging. Systems and method for providing security for

communication of electronic messages, interactive sessions, software downloads, software upgrades, and other content from a source to a receiving device as well as signals used for such communications (304, 309, 308, 324, 342, 338, 334, 330, 326). Systems, methods, signals, device architectures, data formats, and computer program structures for providing authentication, integrity, confidentiality, non-repudiation, replay protection, and other security properties while minimizing the network (306) bandwidth, computational resources and manual user interactions (314) required to install, enable, deploy and utilize these security properties. System, device, method, computer program, and computer program product for searching and selecting data and control elements in message procedural/data sets for automatic and complete portrayal of message to maintain message intent.

French Abstract

Système, procédé, signal, modèle opératoire et programme d'ordinateur pour messagerie électronique. Systèmes et procédé permettant de sécuriser la communication de données de messages électroniques, sessions interactives, téléchargements de logiciels, mises à jour de logiciels et autres contenus d'une source à un appareil récepteur ; signaux utilisés pour ce type de communication (304, 309, 308, 324, 342, 338, 334, 330, 326). Systèmes, procédés, signaux, architectures d'appareils, formats de données et structures de programmes d'ordinateur assurant l'authentification, l'intégrité, la confidentialité, la non-repudiation, la protection contre la réinsertion ainsi que d'autres propriétés de sécurité tout en réduisant la bande passante du réseau (306), ressources informatiques et interactions manuelles de l'utilisateur (314) requises pour l'installation, l'activation, le déploiement et l'utilisation de ces propriétés de sécurité. Système, appareil, procédé, programme d'ordinateur et produit programme d'ordinateur permettant de rechercher et de sélectionner des éléments de donnée et de commande dans des procédures relatives aux messages et des ensembles de données pour obtenir une représentation automatique et complète du message et préserver l'intention du message.

Legal Status (Type, Date, Text)

Publication 20020207 A1 With international search report.

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Main International Patent Class: **G06F-017/00**

Fulltext Availability:

Detailed Description

Detailed Description

... Data-To-Protect, Protected-Data, OutputCBC-Chain) performs the following steps.

- 1 . Let Crypto-Checksum = HMAC (Key, **Data** -To-Protect).
2. Let Plaintext = **Data** -To-Protect || Crypto-Checksum.
3. Let Ciphertext = CBC-Pad-Encrypt (Key, CBC-Chain, Plaintext).
4. Set...

12/5,K/21 (Item 6 from file: 349)
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00811803 **Image available**

VIDEO, AUDIO AND GRAPHICS DECODE, COMPOSITE AND DISPLAY SYSTEM
SYSTEME COMPOSITE DE PRESENTATION A DECODAGE VIDEO AUDIO ET GRAPHIQUE

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2000641936 20000818; US 2000643223 20000818; US 2000640870 20000818; US
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DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ
LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG
SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
((OAPI utility model)) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
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Main International Patent Class: H04N-009/64

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Fulltext Availability:

Detailed Description

Claims

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English Abstract

A video, audio and graphics system uses multiple transport processors to receive in-band and out-of-band MPEG transport streams, to perform PID and section filtering as well as DVB and DES decryption and to de-multiplex them. The system processes the PES into digital audio, MPEG video and message data. The system is capable of decoding multiple MPEG SLICES concurrently. Graphics windows are blended in parallel, and blended with video using alpha blending. During graphics processing, a single-port SRAM is used equivalently as a dual-port SRAM. The video may include both analog video, e.g., NTSC/PAL/SECAM/S-video, and digital video, e.g., MPEG-2 video in SDTV or HDTV format. The system has a reduced memory mode in which video images are reduced in half in horizontal direction only during decoding. The system is capable of receiving and processing digital audio signals such as MPEG Layer 1 and Layer 2 audio and Dolby AC-3 audio, as well as PCM audio signals. The system includes a memory controller. The system includes a system bridge controller to interface a CPU with devices internal to the system as well as peripheral devices including PCI devices and I/O devices such as RAM,

ROM and flash memory devices. The system is capable of displaying video and graphics in both the high definition (HD) mode and the standard definition (SD) mode. The system may output an HDTV video while converting the HDTV video and providing as another output having an SDTV format or another HDTV format.

French Abstract

L'invention porte sur systeme video, audio et graphique utilisant des processeurs multitransporteurs permettant: la reception de signaux MPEG en bande et hors bande, le filtrage de PID et de sections, et le decryptage et le demultiplexage des signaux DVB et DES. Le systeme convertit les PES en signaux numeriques audio, traite la video MPEG et les messages, et peut decoder concurremment plusieurs types de MPEG. Les fenetres graphiques sont melangees en parallele et melangees a des video utilisant le melange alpha. Pendant le traitement des graphiques on utilise une SRAM a une porte equivalent a une SRAM a deux portes. Les normes video englobent a la fois la television analogique (par exemple NTSC/PAL/SECAM/S-video) et la television numerique (par exemple MPEG-2 en format HD ou DN). Le systeme peut fonctionner en mode de memoire reduite ou les images video sont reduite de moitie dans le sens horizontal uniquement durant le decodage. Le systeme peut recevoir et traiter des signaux audio numeriques tels que le MPEG audio Layer 1 et Layer 2, le Dolby audio AC-3, et les signaux audio MIC. Le systeme comporte un controleur de memoire. Le systeme comporte un controleur de passerelles de systeme servant d'interface entre l'unite centrale et des dispositifs interieurs au systeme et des peripheriques ou des PCI, et des dispositifs d'E/S tels que des RAM, des ROM, et des memoires flash. Le systeme peut presenter des programmes video et des graphiques soit en mode haute definition (HD) soit en mode definition normale (DN). Le systeme peut tout en produisant une video en HD en la convertissant, produire une video en DN ou une autre video dans un autre format HD.

Legal Status (Type, Date, Text)

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International Patent Class: G09G-001/16

Fulltext Availability:

Detailed Description

Detailed Description

... time throughput requirements, including

35 slice and higher layers of MPEG video decoding, error handling in digital **video** streams, and 3D and 2D graphics bit map

2

acceleration in the graphics accelerator for current and...accommodate low pass filtering for higher quality downscaling.

In the preferred embodiment, the video pipeline uses a

separate window controller and DMA. In an alternate embodiment, these elements may be shared. The FIFOs are logically **separate** but may be implemented in a common SRAM.

The video compositor block 108 blends the output of...and one analog video signal. The ITU-R 656 decoder 160 processes the decoded MPEG video to **extract** timing and data information.

In one embodiment, an on-chip video decoder (VDEC) 50 converts the analog...are 720 x 16 two port SRAM. For vertical filtering, the three line buffers 178 may provide **video** signals to three of the four taps of the four-tap vertical filter while the video input...pipeline, the DMA module preferably has three channels where the graphics pipeline and the video pipeline use **separate** DMA modules. These may include window descriptor read, graphics data

read and CLUT read. Each channel has...windows
onto the screen repeats.

Referring to FIG. 7, window descriptors are preferably
sorted by the window **controller** and used to transfer graphics
data to the display engine. Each of window descriptors,
including the window...that corresponds to the window
descriptor with the largest window layer parameter at the top.

The window **controller** preferably transfers the graphics
data for the bottom-most graphics window to be processed first.

33
The...

...packet and written to the graphics FIFO. The DMA
engine preferably sends a request to the memory **controller** to
read the corresponding graphics data for this window and send the
graphics data to the graphics...empty-line header is preferably placed
into
the FIFO so that the display engine may release the **display** line
for display..

Packetized data structures have been used primarily in the
communication world where large amount...includes the
graphics FIFO 132 which receives the header packets and the
graphics data from the memory **controller** over the memory
interface. The graphics FIFO preferably provides received raw
graphics data to the graphics converter...

...converter
processes all the window layers of each scan line in half the
time of an interlaced **display** line, due to the need to have lines
from both fields available in the SRAM for use...

12/5,K/22 (Item 7 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00806392

**TECHNOLOGY SHARING DURING ASSET MANAGEMENT AND ASSET TRACKING IN A
NETWORK-BASED SUPPLY CHAIN ENVIRONMENT AND METHOD THEREOF
PARTAGE TECHNOLOGIQUE LORS DE LA GESTION ET DU SUIVI DU PARC INFORMATIQUE
DANS UN ENVIRONNEMENT DU TYPE CHAINE D'APPROVISIONNEMENT RESEAUTE, ET
PROCEDE ASSOCIE**

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DK DM DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL

TJ TM TR TT TZ UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

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Claims
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English Abstract

French Abstract

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abstract; title not checked by the International
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Detailed Description

Detailed Description

... and/or usage requests, Finally, in step 2006, a network process to
which to send the generated data is identified.

Figure 21 shows a block diagram of the Problem Handling Process 1502. The
Problem Handling...

12/5,K/23 (Item 8 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00806383

COLLABORATIVE CAPACITY PLANNING AND REVERSE INVENTORY MANAGEMENT DURING
DEMAND AND SUPPLY PLANNING IN A NETWORK-BASED SUPPLY CHAIN ENVIRONMENT
AND METHOD THEREOF
PLANIFICATION EN COLLABORATION DES CAPACITES ET GESTION ANTICIPEE DES
STOCKS LORS DE LA PLANIFICATION DE L'OFFRE ET DE LA DEMANDE DANS UN
ENVIRONNEMENT DE CHAINE D'APPROVISIONNEMENT FONDEE SUR LE RESEAU ET
PROCEDE ASSOCIE

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200139029 A2 20010531 (WO 0139029)

Application: WO 2000US32309 20001122 (PCT/WO US0032309)

Priority Application: US 99444655 19991122; US 99444886 19991122

Designated States: AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE
DK DM DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL
TJ TM TR TT TZ UA UG UZ VN YU ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-015/00**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 15619

English Abstract

A computer program product is provided which includes in combination a media player control (12) and a web browser control (14) stored on a digital storage medium. The product further includes a display controller which defines a browsing zone and a media player zone on a display screen of a computer. The media player control (12) controls the display of media player data in the media player zone and the browser control (14) controls the display of browser data in the browsing zone. Media player data is selectively retrieved from a remote content provider and/or from a storage device in the computer. The invention extends further to a web browser product and to a client computer product including the computer program product. The product includes a ticker control (20) for displaying ticks independently of the browsing zone.

French Abstract

L'invention concerne un progiciel, qui comprend une commande de diffuseur de medias (12) et une commande de navigateur web (14) combinees stockees dans un support de stockage numerique. Le progiciel comprend egalement un controleur d'ecran de visualisation qui definit une zone d'exploration et une zone de diffusion de medias sur un ecran de visualisation d'un ordinateur. La commande de diffuseur de medias (12) commande la diffusion des donnees du diffuseur de medias dans la zone de diffusion de medias, et la commande du navigateur (14) commande l'affichage des donnees d'exploration dans la zone d'exploration. Des donnees du diffuseur de medias sont selectivement extraites d'un fournisseur de contenu distant et/ou d'une memoire de l'ordinateur. L'invention concerne en outre un logiciel de navigation web et un ordinateur client contenant le progiciel de l'invention. Le progiciel comprend une commande de teleimprimeur (20) servant a presenter des amorces independamment de la zone d'exploration.

Legal Status (Type, Date, Text)

Publication 20010322 A1 With international search report.

Publication 20010322 A1 Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.

Claim Mod 20011108 Later publication of amended claims under Article 19 received: 20010409

Republication 20011108 A1 With international search report.

Republication 20011108 A1 With amended claims and statement.

Examination 20011206 Request for preliminary examination prior to end of 19th month from priority date

Main International Patent Class: **G06F-015/00**

Fulltext Availability:

Claims

Claim

... data communication system
and to a web browser product. It also relates to method of controlling the **display** of **data** on a display screen of a computer and to client computer.

BACKGROUND OF THE INVENTION

Browsers for...

...display of media player data in the media player zone and the browser control typically controls the **display** of browser **data** in the

browsing zone. The data may be information, text, video or the like. The computer program...product includes a ticker control, the display controller defining a ticker zone on the display screen which **displays** ticker **data** in the ticker zone under control of the ticker control. Typically, the product includes a ticker data...displayed in the media player zone and route the data to an appropriate display controller which then **displays** the **data** in the media player zone. Accordingly, the browser may include display controllers or interfaces for controlling the...

...player zone may be multi-functional displaying various formats. The PC may thus include a sound card, **video card**, associated codecs or the like. The product may include a satellite communication interface for receiving audio/visual...

...player control. Still further in accordance with the invention, there is provided a method of controlling the **display** of **data** on a display screen of a computer, the method including defining on the display screen a screen a browser control and **displaying** the **data** in the browsing zone at least upon demand by the user; and retrieving data from a media player control and **displaying** the **data** in the media player zone. The method may include retrieving the data for display in the media...

...The method may include defining on the display screen a ticker zone and retrieving ticker data and **displaying** the ticker **data** in the ticker zone. The ticker data may be retrieved from a storage device in the computer...

...displayed in the media player zone and routing the data to an appropriate display controller which then **displays** the **data** in the media player zone. The audio/visual data may be received from a satellite feed and...

...zone and a ticker zone on a computer screen and includes a ticker control which controls the **display** of ticker **data** in zone ticker zone, the ticker data being displayed independently of web pages displayed in the browsing...

...with the invention may thus include a ticker facility included within the browser and not as a **separate** unit, e.g. as part of a web page. The ticker data may include at least one...across a bottom region of the display screen.

The invention extends to a method of controlling the **display** of **data** on a display screen of a computer, the method including defining a browsing zone and a ticker zone on the display screen and **displaying** ticker **data** in the ticker zone independently of web pages displayed in the browsing zone. Content or data may...

...with the invention, there is provided a client computer which includes a display controller for controlling the **display** of **data** on a display screen of the computer, the controller defining on the display screen a screen layout...

...includes a browsing zone and a media player zone;
a browser control operable to retrieve data and **display** the data in
the browsing zone at least upon demand by the user; and
media player control operable to retrieve data and **display** the data
in the media player zone.
The browser control and media player control are typically
integrated into a...

...may include a system control for controlling the
media player control and the browser control selectively to **display**
data
on a monitor.
The system control preferably includes a skins manager for
managing the provision of skins...

12/5,K/25 (Item 10 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00784184 **Image available**

**A SYSTEM, METHOD FOR FIXED FORMAT STREAM COMMUNICATION IN A COMMUNICATION
SERVICES PATTERNS ENVIRONMENT**
**SYSTEME, PROCEDE ET ARTICLE POUR FLUX DE FORMAT FIXE DANS UN ENVIRONNEMENT
A CONFIGURATIONS DE SERVICES DE COMMUNICATION**

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200117194 A2-A3 20010308 (WO 0117194)

Application: WO 2000US24114 20000831 (PCT/WO US0024114)

Priority Application: US 99386430 19990831

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DE DK DZ EE ES FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL

TJ TM TR TT UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-029/06

International Patent Class: **G06F-017/22** ; H04L-029/12

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 149954

English Abstract

A system, method, and article of manufacture provide a fixed format
stream-based communication system. A sending fixed format contract on
interface code is defined for a sending system. A receiving fixed format
contract on interface code is also defined for a receiving system. A
message to be sent from the sending system to the receiving system is
translated based on the sending fixed format contract. The message is
then sent from the sending system and subsequently received by the
receiving system. The message received by the receiving system is then
translated based on the receiving fixed format contract.

French Abstract

L'invention concerne un systeme, un procede et un article pour systeme de communication a flux de format fixe. Un contrat de format fixe de transmission sur code d'interface est defini pour un systeme de transmission. Un contrat de format fixe de reception sur code d'interface est egalement defini pour un systeme de reception. Un message destine a etre envoye du systeme de transmission au systeme de reception est converti sur la base du contrat de format fixe de transmission. Le message est ensuite transmis depuis le systeme de transmission, puis il est recu par le systeme de reception et converti sur la base du contrat de format fixe.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be republished upon receipt of that report.

Examination 20010816 Request for preliminary examination prior to end of 19th month from priority date

Search Rpt 20020103 Late publication of international search report

Republication 20020103 A3 With international search report.

International Patent Class: **G06F-017/22** ...

Fulltext Availability:

Claims

Claim

... connection between the sender and the receiver that lasts for the duration of the "call" includes voice, **video**, fax, etc. includes data in a circuit switched architecture (e.g., dial-up connections)

Packetized

transferred through...groups, is equally critical to testing as to design. The component aggregation must then support an effective **partitioning** of the application architecture and team organization.

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Testing requires aflexible organization

On large projects, the set...

...to support flexible integration testing teams that form to ensure a particular business ftinction works correctly across **partitions**.

Testing effort shifts earlier in development

The system test phase should gofaster

The implications of greater modularity...Object development realizes the benefits of flexibility and reusability through a greater level of decomposition than was **present** in traditional systems. While smaller objects have the advantage of making it easier to have pre-defined...must then be very participatory and flexible to adapt to the dynamic requirements. One large engagement defined **separate**, overlapping ownership responsibilityfor:

Windows

Domain object model sub-systems, or components; the model comprised about 350

model objects which were **partitioned** into about 12 major areas

346

Business processes that were particularly complex, highly reusable, and cut across many

windows; for example, writing off a bill

Common architecture framework components

Separate concept of ownershipfrom developerfor increasingproductivity One solution to the above problem is the clear distinction between component...days appeared to be a step backward or treading water.

349

However, keeping the application clean paid **dividends** in addressing and fixing problems more efficiently. Generally speaking, the longer the problem went unfixed, the more...new object, look in the pool to see if there is one that can be recycled. Keep **separate** pools for each class of objects. Allocating objects is a lot more expensive than one tends to ...possible. Typically when one does object-to relational mappings, one tends to make every unique object type a **separate** table. This is best

from a design perspective. But in cases where you know you have a...

...do aggregate loads in code, rather than doing a "point-of-use"

370

instantiation for each one **separately**. Of course, these optimizations can have a more substantial impact on your object model. Consider making "lightIt is desirable to **separate** concerns between architecture/framework and implementation details. One way to do this is to exploit the power...

...batch job. In an additional aspect, the abstract data and the batch specific data may be stored **separately**. In a fourth aspect, the logic of the batch job sub-classes may be executed by a...

...when considering component-based systems though. In a component-based system, the application architecture is usually very **separated** from the business application classes. In many cases, the business classes and components are built without regard...when the problem domain contains many independent work items. In this case, the work can simply be **divided** among the available processors, providing nearly linear scaling. Happily, this is exactly the situation encountered in many...to structure the components performing processing steps within a batch system so that the system is cleanly **partitioned** while maintaining performance and scalability goals? Often batch processing systems perform a series of processing or transformation...

...be developed by several

379

developers, requirements are likely to change and it is difficult to cleanly **partition** the modules resulting in a highly coupled system. Compounding the difficulty in implementing the system is the...

...to manage complexity through encapsulation, a component-based batch system can more easily be defined with clean **partitioning** than when using a procedural paradigm. Defined with foresight, this **partitioning** enables the system to scale to meet difficult performance requirements. Therefore, encapsulate each processing step within a...

...multiplexing/demultiplexing techniques, there may be several instances of a particular type of filter running in parallel. **Partitioning**. As a result of encapsulating each processing step within a filter component it becomes easier to manage...technical details. Therefore, the application architecture should control access to a business object's data. This will **separate** out reusable, technical, architecture details. Business objects should use an Attribute Dictionary to provide an architectural hook...method to represent constants in an object-based system is required. Therefore, represent named constants in a **separate** class, grouping categories of constant values together within one name space. Constants tend to naturally fall into...elements.nextElement();
System.out.println(pt.toString());

I

I

public String toString()
return typed;

This type **partition** is used by PhoneNumber. See main() for uncommenting a line that demonstrates the type safety protection through...two or three-tiered client-server application, the services are maintained away from the users (Client) on **separate** Server machines. Whenever a user needs to use a service, the user must send a request across...finds the data in a database and forwards it back to the client. The Client can then **display** the **data** in a User Interface for a user. The scenario was broken into two message trace diagrams. The...

00761429

METHODS, CONCEPTS AND TECHNOLOGY FOR A VIRTUAL SHOPPING SYSTEM CAPABLE OF ASSESSING NEEDS OF A CUSTOMER AND RECOMMENDING A PRODUCT OR SERVICE BASED ON SUCH ASSESSED NEEDS

PROCEDES, CONCEPTS ET TECHNOLOGIE POUR SYSTEME D'ACHAT VIRTUEL CAPABLE D'EVALUER LES BESOINS D'UN CLIENT ET DE RECOMMANDER UN PRODUIT OU UN SERVICE SUR LA BASE DE CES BESOINS

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200073955 A2 20001207 (WO 0073955)

Application: WO 2000US14357 20000524 (PCT/WO US0014357)

Priority Application: US 99321495 19990527

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DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC

LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI

SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-017/60**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 148469

English Abstract

French Abstract

La presente invention concerne un systeme permettant de realiser des transactions commerciales virtuelles apres identification des besoins de l'utilisateur. Tout d'abord, le systeme evalue les besoins d'un utilisateur. Il genere ensuite, sur la base des besoins de l'utilisateur, une solution, qui est affichee. Un paiement est alors accepte en echange de la solution. Il convient de noter que dans le cadre du present descriptif de l'invention, ladite solution est, mais pas exclusivement, un produit ou un service.

Legal Status (Type, Date, Text)

Publication 20001207 A2 Without international search report and to be republished upon receipt of that report.

Examination 20010301 Request for preliminary examination prior to end of 19th month from priority date

Declaration 20010802 Late publication under Article 17.2a

Republication 20010802 A2 With declaration under Article 17(2)(a); without abstract; title not checked by the International Searching Authority.

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

Detailed Description

... the tool to be used for construction is not known at design time then specific tools for **presentation design** are needed.

e) Is the design complex?

J) Does the design have to be presented to multiple...provides the interface between the manager(s) of the system and management data generated by the system. **Data** can be manipulated for various forms of output. By integrating the operational architecture it is possible to reduce the number of front-end interfaces required. Commonly, the **presentation** component uses a GUI front-end interface. This component is also responsible for real-time and historical...

12/5,K/27 (Item 12 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00761422

BUSINESS ALLIANCE IDENTIFICATION

**SYSTEME, PROCEDE ET ARTICLE DE PRODUCTION POUR L'IDENTIFICATION D'ALLIANCES
COMMERCIALES DANS UN CADRE D'ARCHITECTURE RESEAU**

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Legal Representative:

BRUESS Steven C (agent), Merchant, Gould, Smith, Edell, Welter & Schmidt,
P.A., P.O. Box 2903, Minneapolis, MN 55402-0903, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073928 A2-A3 20001207 (WO 0073928)

Application: WO 2000US14375 20000524 (PCT/WO US0014375)

Priority Application: US 99320816 19990527

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DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC

LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI

SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-017/60**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 149371

English Abstract

A system, method and article of manufacture are provided for identifying alliances among a plurality of business entities in components of a network framework. First, alliances are identified among a plurality of business entities in terms of components of a current network framework. Next, a pictorial representation is displayed of the current network framework and the components. The alliances are then conveyed by indicia coding the components of the current network framework in which the alliances exist.

French Abstract

La presente invention concerne un systeme, un procede et un article de production permettant d'identifier les alliances au sein d'un groupe de plusieurs entites commerciales en terme de composants d'un cadre de reseau. Tout d'abord, les alliances sont identifiees parmi un groupe de plusieurs entites commerciales en terme de composants d'un cadre de

reseau en cours. Ensuite, une representation graphique du reseau en cours et des composants est affichee. Les alliances sont alors acheminees en codant les composants du cadre de reseau en cours dans lequel les alliances existent avec des marques.

Legal Status (Type, Date, Text)

Publication 20001207 A2 Without international search report and to be republished upon receipt of that report.
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Search Rpt 20010525 Late publication of international search report.
Republication 20010525 A3 With international search report.

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

Detailed Description

... and

2

omissions among components of a web based architecture in accordance with one embodiment of the **present** invention;
Figure 1B-1 is a flowchart providing more detail of the method for identifying redundancies and...services offered by each vendor in relation to the entire network framework may be made. Figure 1M **shows** one way the present method may be implemented.

1 2

As stated above, various vendors may be...implemented from the logical data requirements. The tools also represent data elements, indexing, and foreign keys.

Many **data design** tools integrate data modeling, database design, and database construction. An integrated tool will typically generate the first...In these cases a dedicated presentation design tool can be used to provide maintainable documentation of the **presentation design** which can be used to clarify and communicate issues.

Product Considerations

a) How much does the tool...automatically (through execution of online 15 activity in the system), manually (through test data manipulation tools), **extracted** from production databases, and so on. Once the baseline databases are selected and created, the repeatable test...tools support and automate the conduct of system tests. Test Execution support includes the tools required to.

Extract input data and expected results from the repository
5 0 Load this data into the appropriate Test...

12/5,K/28 (Item 13 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00504224 **Image available**

INFORMATION HANDLING SYSTEM WITH SUSPEND/RESUME OPERATION

SYSTEME DE GESTION D'INFORMATIONS AVEC FONCTION DE SUSPENSION/REPRISE

Patent Applicant/Assignee:

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IBM UNITED KINGDOM LIMITED,
KOHNO Hiroshi,
SHIMOTOHNO Susumu,

Inventor(s):

KOHNO Hiroshi,

SHIMOTOHNO Susumu,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9935576 A1 19990715

Application: WO 99GB53 19990107 (PCT/WO GB9900053)

Priority Application: JP 981253 19980107

Designated States: CZ HU IL KR PL RU US AT BE CH CY DE DK ES FI FR GB GR IE
IT LU MC NL PT SE

Main International Patent Class: **G06F-011/14**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 12778

English Abstract

Provided is an information handling system and a method of controlling the same which allows the state of the system to be saved without destructing other user data on an external storage device. When a predetermined event occurs, the system saves hibernation information in a hibernation information storing area on a hard disk. A hibernation managing information storing area is provided in the outermost cylinder of the hard disk. A boot sector already exists in the outermost cylinder. Therefore, the hibernation managing information is written in the outermost cylinder after the boot sector has been saved to the hibernation information storing area. After such series of processes have been completed, the system shifts to a hibernation mode. On the other hand, when power feeding to the system is resumed, the hibernation information is read out to check whether or not the system was in the hibernation mode, etc. The hibernation information saved in the hibernation information storing area is restored to the original place and writes the master boot record which was saved to the hibernation information storing area back to the outermost cylinder.

French Abstract

Cette invention se rapporte a un systeme de gestion d'informations et a un procede de commande de ce systeme, qui permettent de sauvegarder l'etat du systeme sans detruire les autres donnees d'utilisateur stockees sur un dispositif de memorisation externe. Lorsqu'un evenement predetermine se produit, le systeme sauvegarde les informations d'hibernation dans une zone de stockage des informations d'hibernation se trouvant sur un disque dur. Une zone de stockage des informations de gestion d'hibernation est prevue dans le cylindre exterieur du disque dur. Un secteur d'initialisation se trouve deja dans ce cylindre exterieur. Par consequent, les informations de gestion d'hibernation sont inscrites dans ce cylindre exterieur, apres que le secteur d'initialisation a ete sauvegarde dans la zone de stockage des informations d'hibernation. Une fois terminee cette serie d'operations, le systeme passe en mode d'hibernation. Par ailleurs, lors du retablissement de l'alimentation du systeme, les informations d'hibernation sont extraites pour verifier si le systeme se trouve en mode d'hibernation, notamment. Les informations d'hibernation sauvegardees dans la zone de stockage des informations d'hibernation sont a nouveau stockees a leur emplacement d'origine et le fichier d'initialisation maitre, qui a ete sauvegarde dans la zone de stockage des informations d'hibernation, est inscrit dans ledit cylindre exterieur.

Main International Patent Class: **G06F-011/14**

Fulltext Availability:

Claims

Claim

... value

and a timer count value of controller chips such as CPU 11, PIC, DMA controller and **video controller** 20. The hardware context information is important data which defines the current status of the system 100...the OS or the application again to resume the

task from the point of interruption.

Fig.4 shows a data structure which is saved and restored in the disk in shifting to the hibernation mode. This will...

...area provided on the hard

disk while A is saved in the hibernation managing information storing area separately.

The hibernation information storing area is a "hibernation file" which is managed by the OS file system, for example. The hibernation file is allocated in a user partition on the hard disk as a file in a same level as a user file. A hibernation...

12/5,K/29 (Item 14 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00456597

**DATA PROCESSING SYSTEM AND METHOD FOR DETERMINING AND ANALYZING
CORRESPONDENCE INFORMATION FOR A STEREO IMAGE
SYSTEME ET PROCEDE DE TRAITEMENT DES DONNEES**

Patent Applicant/Assignee:

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Inventor(s):

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BAKER Henry Harlyn,

VON HERZEN Brian,

ALKIRE Robert Dale,

Patent and Priority Information (Country, Number, Date):

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Priority Application: US 97839767 19970415

Designated States: AL AM AT AT AU AZ BA BB BG BR BY CA CH CN CU CZ CZ DE DE

DK DK EE EE ES FI FI GB GE GH GM GW HU ID IL IS JP KE KG KP KR KZ LC LK

LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SK SL

TJ TM TR TT UA UG UZ VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ

MD RU TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ

CF CG CI CM GA GN ML MR NE SN TD TG

Main International Patent Class: G06K-009/00

International Patent Class: G06K-009/64; H04N-013/00; H04N-013/02;

G06T-007/60; G06F-017/15

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 80157

English Abstract

A powerful, scalable, and reconfigurable image processing system and method for processing stereo image data is described. A general purpose, reconfigurable engine with toroidal topology, distributed memory, and wide bandwidth I/O is described for solving real applications at real-time speeds. The reconfigurable image processing system can be optimized to efficiently perform specialized computations, such as real-time video and audio processing. This reconfigurable image processing system provides high performance via high computational density, high memory bandwidth, and high I/O bandwidth. Generally, the reconfigurable image processing system and its control structure include a homogeneous array of 16 field programmable gate arrays (FPGA) and 16 static random access memories (SRAM) arranged in a partial torus configuration (Fig. 46). The reconfigurable image processing system also includes a PCI bus interface chip, a clock control chip, and a datapath chip. It can be implemented in a single board. It receives data from its external environment, computes correspondence, and uses the results of the correspondence computations for various post-processing, industrial applications. The reconfigurable image processing system determines correspondence by using non-parametric local transforms followed by

correlation. These non-parametric local transforms include the census, and rank transforms. Other embodiments involve a combination of correspondence, rectification, a left-right consistency check, and the application of an interest operator.

French Abstract

L'invention concerne un systeme de traitement des images puissant, a echelle variable et de type reconfigurable, et un procede de traitement des donnees. La machine reconfigurable, de type polyvalent, a topologie toroidale, a memoire repartie et a grande largeur de bande en entrees/sorties, permet le traitement des applications reelles aux vitesses du temps reel. Le systeme reconfigurable de traitement des images peut etre optimise efficacement pour les besoins de calculs specialises (par exemple, traitement video et audio en temps reel). Ce systeme a un haut rendement grace a sa densite de calcul ainsi que sa largeur de bande elevee en memoire et en entrees/sorties. Generalement, le systeme et sa structure de commande ont un ensemble homogene de 16 reseaux de portes programmables par l'utilisateur (ou circuits FPGA) et de 16 memoires RAM statiques (SRAM), en configuration toroidale partielle. En outre, le systeme a une puce d'interface de bus d'interconnexion de peripheriques (PCI), une puce de commande d'horloge et une puce de trajet de donnees. La mise en oeuvre est possible sur carte unique. Le systeme recoit les donnees de l'environnement externe, calcule les correspondances et utilise les resultats des calculs de correspondance pour differentes applications industrielles de post-traitement. Enfin, le systeme determine les correspondances en utilisant des transformees locales non parametriques, cette operation etant suivie par une correlation. Ces transformees comprennent les transformees de denombrement et de rang. D'autres variantes font intervenir en combinaison la correspondance, la rectification, le controle d'homogeneite de gauche a droite et l'application d'un operateur de bonification dans l'interet de l'utilisateur.

...International Patent Class: **G06F-017/15**

Fulltext Availability:

Detailed Description

Detailed Description

... configuration.

FIG. 47 shows the data flow in the array of the image processing system.

FIG. 48 shows a high level block diagram of one embodiment of the hardware implementation of the census vector generator...Each of these aspects is discussed in greater detail in the following sections.

One aspect of the **present** invention is the software/algorithm implementation, generally called the correspondence algorithms. Generally, one embodiment of the correspondence...use the results in a useful format.

In another embodiment of the software/algorithm aspect of the **present** invention, the census and correlation steps are performed in parallel and pipelined fashion. The systolic nature of...detection and recognition, image comprehension, compression and video editing or compositing.

Although the various aspects of the **present** invention may be used for a variety of applications, one illustrative embodiment will be used to illustrate...system. In this way, the computer can be optimized to perform specialized computations, for example real-time **video** or audio processing, more

17

efficiently. Another benefit of a reconfigurable image processing system is its...depending on the context.

B. PCI-COMPLIANT SYSTEM

FIG. I shows a particular industrial application of the **present** invention in which two sensors or cameras capture data with respect to an

object and supply the...bridge 101. Other devices that may be coupled to the PCI bus 182 include audio peripherals 120, **video** peripherals 131, video memory 132 coupled to the video peripherals 131 via bus 188, SCSI adapter 140...PCI and can grab single frames. These frame grabbers can receive input from various sources including camcorders, **video** recorders, VCRs, videodisc, security cameras, any standard NTSC or PAL compatible sources, any device that outputs an NTSC signal on an RCA type jack, or any nonstandard **video** signals.

In the described embodiment, the frame grabber produces an array of pixels, or digital picture elements...

...2,690 MB/sec of memory access. The pairs of input data can be from two spatially **separated** cameras or sensors or a single camera or sensor which receives data in a time division manner...

12/5,K/30 (Item 15 from file: 349)
DIALOG(R) File 349:PCT FULLTEXT
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00418748 **Image available**

SYSTEMS AND METHODS FOR SECURE TRANSACTION MANAGEMENT AND ELECTRONIC RIGHTS PROTECTION

SYSTEMES ET PROCEDES DE GESTION DE TRANSACTIONS SECURISEES ET DE PROTECTION DE DROITS ELECTRONIQUES

Patent Applicant/Assignee:

INTERTRUST TECHNOLOGIES CORP,

Inventor(s):

GINTER Karl L,
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SIBERT W Olin,
SPAHN Francis J,
VAN WIE David M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9809209 A1 19980305

Application: WO 97US15243 19970829 (PCT/WO US9715243)

Priority Application: US 96706206 19960830

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH HU IL IS JP KE KG KP KZ LC LK LR LS LT LU LV MD MG MK MN

MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW

GH KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI

FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG

Main International Patent Class: **G06F-001/00**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 195626

English Abstract

The present invention provides systems and methods for electronic commerce including secure transaction management and electronic rights protection. Electronic appliances such as computers employed in accordance with the present invention help to ensure that information is accessed and used only in authorized ways, and maintain the integrity, availability, and/or confidentiality of the information. Secure subsystems used with such electronic appliances provide a distributed virtual distribution environment (VDE) that may enforce a secure chain of handling and control, for example, to control and/or meter or otherwise monitor use of electronically stored or disseminated information. Such a virtual distribution environment may be used to protect rights of various participants in electronic commerce and other electronic or electronic-facilitated transactions. Secure distributed and other operating system environments and architectures, employing, for example, secure semiconductor processing arrangements that may establish secure, protected environments at each node. These techniques may be used to

support an end-to-end electronic information distribution capability that may be used, for example, utilizing the "electronic highway".

French Abstract

La presente invention concerne des systemes et des procedes de commerce electronique comprenant une gestion de transactions securisees et la protection de droits electroniques. Des appareils electroniques tels que des ordinateurs utilises conformement a la presente invention contribuent a assurer que l'accès aux informations et l'utilisation des informations ne se font que par des voies autorisees et ils maintiennent l'integrite, la disponibilite et/ou la confidentialite des informations. Des sous-systemes securises utilises avec ces appareils electroniques constituent un environnement de distribution virtuel (VDE) reparti pouvant faire valoir une chaine securisee de traitement et de commande, par exemple, pour commander et/ou mesurer ou encore controler l'utilisation d'informations memorisees ou disseminees electroniquement. Cet environnement de distribution virtuel peut etre utilise pour proteger les droits de divers participants dans le commerce electronique et dans d'autres transactions electroniques ou dans lesquelles intervient l'electronique. Des environnements et des architectures de systemes repartis securises et autres systemes d'exploitation emploient, par exemple, des arrangements de traitement a semi-conducteurs securises pouvant etabli des environnements proteges securises a chaque noeud. On peut utiliser ces techniques pour apporter un soutien a une capacite de distribution d'informations electroniques de bout-en-bout pouvant etre utilisees, par exemple, en empruntant l'"autoroute electronique".

Main International Patent Class: **G06F-001/00**

Fulltext Availability:

Detailed Description

Detailed Description

... SPU task

and memory management, supports internal SPU hardware interrupts, provides certain "low level services," manages "DTD" data structures, and manages the SPU bus interface unit 530.

Kernel/dispatcher 552 also includes a load module...

12/5,K/31 (Item 16 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00344642

SYSTEMS AND METHODS FOR SECURE TRANSACTION MANAGEMENT AND ELECTRONIC RIGHTS PROTECTION

SYSTEMES ET PROCEDES DE GESTION SECURISEE DE TRANSACTIONS ET DE PROTECTION ELECTRONIQUE DES DROITS

Patent Applicant/Assignee:

ELECTRONIC PUBLISHING RESOURCES INC,

Inventor(s):

GINTER Karl L,
SHEAR Victor H,
SPAHN Francis J,
VAN WIE David M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9627155 A2 19960906

Application: WO 96US2303 19960213 (PCT/WO US9602303)

Priority Application: US 95388107 19950213

Designated States: AL AM AT AU AZ BB BG BR BY CA CH CN CZ DE DK EE ES FI GB

GE HU IS JP KE KG KP KR KZ LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL

PT RO RU SD SE SG SI SK TJ TM TR TT UA UG UZ VN KE LS MW SD SZ UG AZ BY

KG KZ RU TJ TM AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF BJ CF

CG CI CM GA GN ML MR NE SN TD TG

Main International Patent Class: **G06F-001/00**

International Patent Class: **G06F-17:60**

Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 207972

English Abstract

The present invention provides systems and methods for electronic commerce including secure transaction management and electronic rights protection. Electronic appliances such as computers employed in accordance with the present invention help to ensure that information is accessed and used only in authorized ways, and maintain the integrity, availability, and/or confidentiality of the information. Secure subsystems used with such electronic appliances provide a distributed virtual distribution environment (VDE) that may enforce a secure chain of handling and control, for example, to control and/or meter or otherwise monitor use of electronically stored or disseminated information. Such a virtual distribution environment may be used to protect rights of various participants in electronic commerce and other electronic or electronic-facilitated transactions. Secure distributed and other operating system environments and architectures, employing, for example, secure semiconductor processing arrangements that may establish secure, protected environments at each node. These techniques may be used to support an end-to-end electronic information distribution capability that may be used, for example, utilizing the "electronic highway".

French Abstract

Systemes et procedes destines au domaine du commerce electronique, et notamment a la gestion securisee des transactions et a la protection electronique des droits. Les appareils electroniques tels que les ordinateurs utilisent conformement a la presente invention permettent d'assurer que les informations ne sont consultees et exploitees que de maniere autorisee, et ils conservent l'integrite, la disponibilite et/ou le caractere confidentiel des informations. Les sous-systemes securises utilises en association avec de tels appareils electroniques constituent un environnement de distribution virtuel distribue (VDE) apte a imposer une chaine securisee de traitement et de commande, par exemple pour la commande et/ou la mesure ou encore le controle de l'utilisation d'informations stockees ou diffusees electroniquement. Cet environnement de distribution virtuel peut servir a proteger les droits de differents individus impliquees dans le commerce electronique et dans d'autres transactions electroniques ou assistees par des moyens electroniques. On a egalement prevu des environnements et architectures de systeme d'exploitation distribues, securises et autres mettant en oeuvre, par exemple, des ensembles de traitement securise a semi-conducteurs pouvant etablir des environnements securises et proteges au niveau de chaque noeud. Ces techniques peuvent servir de soutien pour une fonction electronique de distribution d'informations de bout en bout, cette fonction etant utilisable, par exemple, dans le domaine de l'"autoroute electronique".

Main International Patent Class: G06F-001/00
International Patent Class: G06F-17:60

12/5,K/32 (Item 17 from file: 349)
DIALOG(R) File 349:PCT FULLTEXT
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00312067

VARIABLE PIXEL DEPTH AND FORMAT FOR VIDEO WINDOWS
FORMAT ET PROFONDEUR DE PIXEL VARIABLES POUR FENETRES VIDEO

Patent Applicant/Assignee:

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Inventor(s):

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BRIL Vlad,

EGLIT Alexander,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9530220 A1 19951109

Application: WO 95US5259 19950428 (PCT/WO US9505259)

Priority Application: US 94235764 19940429

Designated States: JP KR AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE

Main International Patent Class: **G09G-005/14**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 11266

English Abstract

A computer video controller, particularly a VGA or SVGA video controller for use with graphical user interface (GUI) software such as WINDOWS or OS/2 is provided with two video data pipelines for simultaneously displaying full motion video within a window in a video display. A first data pipeline displays background video at a first pixel depth. A second data pipeline is provided to display a motion video window at a second, usually higher, pixel depth. The location of the motion video window is measured horizontally in number of memory fetch cycles needed to retrieve the horizontal scan line of pixel data abutting the motion video window. The width of the motion video window is measured in the number of memory fetches required to retrieve one scan line of the motion video window. By providing two parallel data pipelines having equal delays, the motion video window can be generated by selectively retrieving background pixel data or motion video window pixel data and transferring the data to the appropriate pipeline. In an alternative embodiment, data tags may be used to distinguish between background and motion video window pixel data. The controller may also support various compression formats for motion video.

French Abstract

Un regisseur video informatique, en particulier un regisseur video VGA ou SVGA destine a etre utilise avec un logiciel a interface utilisateur graphique (GUI) tel que WINDOW ou OS/2 est dote de deux pipelines de donnees video pour l'affichage simultane d'une video animee dans une fenetre d'un affichage video. Un premier pipeline de donnees affiche une video d'arriere plan a une premiere profondeur de pixel. Un second pipeline de donnees est prevu pour afficher une fenetre de video animee a une seconde profondeur de pixel, generalement superieure. L'emplacement de la fenetre de video animee est mesure horizontalement en nombre de cycles d'extraction de memoire necessaire pour extraire la ligne de balayage horizontale des donnees de pixel contigues a la fenetre de video animee. La largeur de la fenetre de video animee est mesuree en nombre d'extractions de memoire necessaires pour extraire une ligne de balayage de la fenetre de video animee. Grace a deux pipelines de donnees paralleles a retards egaux, on peut generer une fenetre de video animee en extrayant selectivement des donnees de pixel d'arriere plan ou des donnees de pixel de la fenetre de video animee, et en envoyant les donnees au pipeline approprie. Dans un autre mode de realisation, des etiquettes de donnees peuvent etre utilisees pour distinguer les donnees de pixel d'arriere plan et de fenetre de video animee. Le regisseur peut egalement prendre en charge differents formats de compression pour la video animee.

Main International Patent Class: **G09G-005/14**

Fulltext Availability:

Detailed Description

Detailed Description

... DAC 536 using, for

example, eight bit pixel data in a 4:2:2 RGB format.

The **video controller** of Figure 5A is also provided with 30 a MVW FIFO 551. MVW FIFO 551 may comprise a FIFO 32 bits 18

wide having a depth of 20. When a MVW is to be **displayed**,

the MVW data , along with the background data, stored in appropriate locations in memory array 501 by the host processor (not shown). **Separate** memory locations may be 5 provided for MVW data, or the same memory locations may be provided...

12/5,K/33 (Item 18 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00265982 **Image available**

**METHOD FOR UPDATING THE COLOR LOOK UP TABLES OF VIDEO DISPLAY DEVICES TO
DISPLAY DIGITAL VIDEO SIGNALS
PROCEDE DE MISE A JOUR DE TABLES DE CONSULTATION DE COULEUR DES DISPOSITIFS
D'AFFICHAGE VIDEO POUR AFFICHER DES SIGNAUX VIDEO NUMERIQUES**

Patent Applicant/Assignee:

VIACOM INTERNATIONAL INC,

Inventor(s):

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BEZARK Matt,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9414151 A1 19940623

Application: WO 93US12213 19931215 (PCT/WO US9312213)

Priority Application: US 92706 19921215

Designated States: AU BB BG BR BY CA CZ FI HU JP KR KZ LK LV MG MN MW NO NZ

PL RO RU SD SK UA UZ AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE BF

BJ CF CG CI CM GA GN ML MR NE SN TD TG

Main International Patent Class: **G09G-001/28**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 2588

English Abstract

A method for updating the color look up tables (42) of video display devices during the active scan of the raster display without disrupting the normal viewing of the video display is disclosed. In operation, the present invention accesses color index information from only a first selected partition (50) of the color look up table (42) while updating color index information in another partition (54) of the color look up table (42). Then, color index information is accessed only from the updated partition of the color look up table while updating the color index information in the first selected partition (50). In this manner, the entirety of the color look up table (42) is updated while ensuring data integrity and minimizing the disruption of the normal viewing of the video display.

French Abstract

L'invention concerne un procede de mise a jour de tables (42) de consultation de couleur de dispositifs d'affichage video pendant le balayage actif de l'affichage de la trame sans interruption de la visualisation normale de l'affichage video. L'invention permet d'avoir acces a des informations d'indice de couleur a partir d'une seule premiere partition selectionnee (50) de la table de consultation de couleur (42) tout en effectuant une mise a jour des informations d'indice de couleur dans une autre partition (54) de la table de consultation de couleur (42). Ensuite, l'invention permet d'avoir acces aux informations d'indice de couleur uniquement a partir de la partition mise a jour de la table de consultation de couleur pendant que s'effectue une mise a jour des informations d'indice de couleur dans la premiere partition selectionnee (50). De cette maniere, la totalite de la table de consultation de couleur (42) est mise a jour tout en assurant l'integrite des donnees et en reduisant au minimum l'interruption de la visualisation normale de l'affichage video.

Detailed Description

... video

data in the random access memory (RAM) 38* The microprocessor 14 also uncompresses, if necessary. To **display** the video **data** ,, the microprocessor 14 sends the video data to the **video** graphics **card** 22. The video data contains index information into a color look up table 42 of the **video** graphics **card** 22, As pixels are displayed, color information in the color look up table 42 is accessed, and the **video** graphics **card** -112 sends the color information to the display monitor 26 to be displayed,

Referring to FIG. 2. there is shown a diagram of the **partitioned** color look up table 42 of FIG. 1. In a preferred embodiment of the present invention, the...

Set	Items	Description
S1	7335	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	991	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	219	VIDEO(2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	1277	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	10	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATI- ON OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N) S2
S6	1442	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT()TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	6047	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBL- EM? OR ICON? OR GUI OR GUIs OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) ()DOWN()MENU? OR SELECTOR?
S8	3323	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERE- NT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	0	S1 AND S2 AND S3

?ds; show files

Set	Items	Description
S1	7335	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	991	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	219	VIDEO(2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	1277	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	10	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATI- ON OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N) S2
S6	1442	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT()TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	6047	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBL- EM? OR ICON? OR GUI OR GUIs OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) ()DOWN()MENU? OR SELECTOR?
S8	3323	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERE- NT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	0	S1 AND S2 AND S3

File 256:SoftBase:Reviews,Companies&Prods. 82-2003/Apr
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Set	Items	Description
S1	1945763	EXTRACT? OR DIVIDE? OR SEPARATE? OR PARTITION? OR SPLIT?
S2	85058	(DISPLAY? OR SHOW? OR PRESENT? OR VIEW? OR VISAL?) (2W) (DATA OR ELEMENT OR ENTITY)
S3	3027	VIDEO (2W) (CARD? OR BOARD? OR CONTROLLER? OR ADAPTER?)
S4	43165	(FIRST OR 1ST OR PRIME OR PRIMARY OR INITIAL OR LEADING OR CARDINAL OR ORIGINAL) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S5	839	(LAYOUT OR ORDER OR ARRANGEMENT OR ORGANIZATION OR FORMATION OR STRUCTURE OF CONFIGURATION OR DESIGN) (2N) S2
S6	68413	(CONSTRUCT? OR BUILD? OR PRODUCE? OR ASSEMBLE? OR FABRICAT? OR MAKE OR MAKING OR PUT()TOGETHER OR CREATE) (3N) (INTERFACE? OR CONNECT? OR PATH? OR ROUTE? ? OR TRANSMISSION OR BOUNDAR?)
S7	114501	(USER OR MEDIA) () (INTERFACE? OR BUTTON? OR SYMBOL? OR EMBLEM? OR ICON? OR GUI OR GUIS OR GRAPHIC? OR DIAL?) OR (PULL OR DROP) ()DOWN()MENU? OR SELECTOR?
S8	70475	(SECOND OR 2ND OR ADDITIONAL OR TWO OR SEPARATE OR DIFFERENT OR ANOTHER OR TARGET OR NEXT OR SUCCEEDING OR SUCCESSIVE OF FOLLOWING OR SUBSEQUENT) (2W) (APPLICATION? OR DATA OR PROGRAM? OR DATABASE? OR DATA()BASE? OR SOFTWARE OR FILE?)
S9	26	S1 AND S2 AND S3
S10	20	S9 AND IC=(G06F? OR G09G?)
S11	9	S9 AND MC=(T01-C04A OR T01-C04D OR T04-H01 OR T04-H03C2 OR 203-A08A9)
S12	21	S10 OR S11

File 347:JAPIO Oct 1976-2003/Jan(Updated 030506)

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File 350:Derwent WPIX 1963-2003/UD,UM &UP=200329

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12/5/1 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO
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06656423 **Image available**
DISPLAY DEVICE, VIDEO CONTROLLER UNIT, AND IMAGE DISPLAY METHOD

PUB. NO.: 2000-242246 [JP 2000242246 A]
PUBLISHED: September 08, 2000 (20000908)
INVENTOR(s): MOTAI SEIJI
KONO SHINICHI
CHIBA KAZUO
APPLICANT(s): INTERNATL BUSINESS MACH CORP (IBM)
APPL. NO.: 11-040041 [JP 9940041]
FILED: February 18, 1999 (19990218)
INTL CLASS: G09G-005/00 ; G07G-001/01; G09G-003/20 ; G09G-003/36 ;
H04N-005/66

ABSTRACT

PROBLEM TO BE SOLVED: To reduce circuits corresponding to respective panels and to reduce the number of required parts by allocating upper/lower display areas of a DSTN panel to a panel of another module and controlling two displays with a device driver for circuit/OS controlling one sheet of panel.

SOLUTION: A video controller 20 outputs DSTN controlling signals of control signals and display data signals (chrominance signals) 31, 33 to control one sheet of panel. For driving half-sized panels 41, 42, when these control signals and a chrominance data signal 31 for an upper panel or the chrominance data signal 33 for a lower part panel exist, the panels are displayed. Then, the signal lines of the chrominance data signal 31 for the upper part panel and the chrominance data signal 33 for the lower part panel are connected respectively to the half-sized panels 41, 42. Further, the control signal originally being a piece is divided into two signals 37, 39 on the way to be connected to respective panels, and two sheets of panels 41, 42 are driven.

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12/5/2 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014616626 **Image available**
WPI Acc No: 2002-437330/200247
XRPX Acc No: N02-344290

**Portable computer display system for varying sizes of viewing data ,
has video controller operable to receive a video signal to determine
panels used in display**

Patent Assignee: HEWLETT-PACKARD CO (HEWP)
Inventor: NAUKA K
Number of Countries: 029 Number of Patents: 004
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1202162	A1	20020502	EP 2001308817	A	20011017	200247 B
CN 1350227	A	20020522	CN 2001125789	A	20010824	200258
JP 2002196741	A	20020712	JP 2001306761	A	20011002	200261
KR 2002032320	A	20020503	KR 200165214	A	20011023	200270

Priority Applications (No Type Date): US 2000694473 A 20001024
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 1202162	A1	E	16	G06F-003/14	
Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT LI LT LU LV MC MK NL PT RO SE SI TR					
CN 1350227	A			G06F-003/147	
JP 2002196741	A		8	G09G-005/00	

Abstract (Basic): EP 1202162 A1

NOVELTY - A **video controller** (60) receives a video signal to determine the number of panels (110) used in a display (100) and **partitions** the signal among the panels. The vertical and horizontal surfaces (22,24) has electrical contact connecting the display panel for sending signal to the display panel.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is included for flat panel display.

USE - Portable computer display system for varying sizes of **viewing data**.

ADVANTAGE - The display may created using off-the shelf components which helps in easily achieving mechanical support elements and logic circuits. The software solutions needed to manipulate the image are either commercially available, or may be adapted from existing system. Less cost display.

DESCRIPTION OF DRAWING(S) - The figure shows a perspective view of variable size multiplane display system for portable computer.

Horizontal and vertical surface (22,24)

Video controller (60)

Display (100)

Multiple panels (110)

pp; 16 DwgNo 1/13

Title Terms: PORTABLE; COMPUTER; DISPLAY; SYSTEM; VARY; SIZE; VIEW; DATA;

VIDEO; CONTROL; OPERATE; RECEIVE; VIDEO; SIGNAL; DETERMINE; PANEL;

DISPLAY

Derwent Class: P85; T01; T04

International Patent Class (Main): G06F-003/14 ; G06F-003/147 ;

G09G-005/00

International Patent Class (Additional): G06F-003/153

File Segment: EPI; EngPI

12/5/3 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014311774 **Image available**

WPI Acc No: 2002-132476/200218

XRPX Acc No: N02-099946

Electronic device to release and manage independently regions of the display on a microcomputer, uses supplementary video controller that mixes the computer display with externally provided display data

Patent Assignee: DEBIONNE M (DEBI-I)

Inventor: DEBIONNE M

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
FR 2808604	A1	20011109	FR 20005661	A	20000503	200218 B

Priority Applications (No Type Date): FR 20005661 A 20000503

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
FR 2808604	A1		20	G06F-003/14	

Abstract (Basic): FR 2808604 A1

NOVELTY - The display control device adjusts display parameters seen by the computer operating system so the computer uses only part of the display, either by adjusting screen resolution or by compressing data sent by the **video controller** to the display. The control device has a processor and its own **video controller**, and adjusts video data from the computer to allow space on the screen for other uses.

USE - Display of advertising on part of a computer screen.

ADVANTAGE - Provides display that is not controlled fully by computer associated with the display, allowing external source to

control part of the display, even when the computer is not activated, and preventing the computer user masking that part of the display.

DESCRIPTION OF DRAWING(S) - The drawing shows a schematic of the **partitioned** display. The drawing contains non-English language text.
pp; 20 DwgNo 1/3

Title Terms: ELECTRONIC; DEVICE; RELEASE; MANAGE; INDEPENDENT; REGION;
DISPLAY; MICROCOMPUTER; SUPPLEMENTARY; VIDEO; CONTROL; MIX; COMPUTER;
DISPLAY; EXTERNAL; DISPLAY; DATA

Derwent Class: P85; T01; T04

International Patent Class (Main): G06F-003/14

International Patent Class (Additional): G09G-005/14

File Segment: EPI; EngPI

12/5/4 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013838731 **Image available**

WPI Acc No: 2001-322943/200134

XRPX Acc No: N01-232510

Video **display** controller for personal computer, distributes data relevant to arbitrary enlargement factors uniformly other than integral multiplication data

Patent Assignee: HITACHI GAZO JOHO SYSTEM KK (HITA-N); HITACHI LTD (HITA)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2001083957	A	20010330	JP 99262726	A	19990916	200134 B

Priority Applications (No Type Date): JP 99262726 A 19990916

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
JP 2001083957	A		39	G09G-005/36	

Abstract (Basic): JP 2001083957 A

NOVELTY - An enlargement unit generates enlarged video **display data** from the input video **display data** depending on designated enlargement factor. The enlargement data corresponding to arbitrary enlargement factors are dispersed uniformly by control unit in display screen other than data with integral multiplication value.

USE - For liquid-crystal display device of personal computer.

ADVANTAGE - Enlarged display corresponding to arbitrary enlargement factors is performed at high-speed with high-resolution, since **separate** frame memories are used for odd and even lines.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of display system. (Drawing includes non-English language text).

pp; 39 DwgNo 1/23

Title Terms: VIDEO; DISPLAY; CONTROL; PERSON; COMPUTER; DISTRIBUTE; DATA;
RELEVANT; ARBITRARY; ENLARGE; FACTOR; UNIFORM; INTEGRAL; MULTIPLICATION;
DATA

Derwent Class: P85; T04

International Patent Class (Main): G09G-005/36

File Segment: EPI; EngPI

12/5/5 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013483273 **Image available**

WPI Acc No: 2000-655216/200063

Related WPI Acc No: 2001-136456

XRPX Acc No: N00-485633

Video controller **subsystem**, coupled to separate high speed memory and low speed memory having separate data paths

Patent Assignee: MICRON ELECTRONICS INC (MICR-N)

Inventor: KLEIN D A

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6104373	A	20000815	US 96712893	A	19960912	200063 B
			US 97918495	A	19970822	

Priority Applications (No Type Date): US 96712893 A 19960912; US 97918495 A 19970822

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6104373	A	7	G09G-005/00	Cont of application US 96712893

Abstract (Basic): US 6104373 A

NOVELTY - High order address bit is used to indicate **display** of **data** from high-speed memory (102) or from low-speed memory (103). Low speed memory is system memory and high speed memory is embedded in **video controller**. **Video controller** (101) is connected to memories by different data paths (104,105) and address busses (106,107).

USE - For **display** of **data** on cathode ray tube display or flat panel display.

ADVANTAGE - VGA compatible memory data bus is distinct from system memory data bus so no reduction in system performance when executing VGA compatible software, supports high resolution video modes needing large amount of memory

DESCRIPTION OF DRAWING(S) - Simplified view of **video controller** subsystem.

Video controller (101)

High speed memory (102)

Low speed memory (103)

Data paths (104,105)

Address busses (106,107)

pp; 7 DwgNo 1/3

Title Terms: VIDEO; CONTROL; SUBSYSTEM; COUPLE; **SEPARATE** ; HIGH; SPEED;

MEMORY; LOW; SPEED; MEMORY; **SEPARATE** ; DATA; PATH

Derwent Class: P85; T01; T04

International Patent Class (Main): G09G-005/00

File Segment: EPI; EngPI

12/5/6 (Item 5 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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013421731 **Image available**

WPI Acc No: 2000-593670/200056

Related WPI Acc No: 1999-034300

XRPX Acc No: N00-439622

Video controller integrated circuit for computer display, has multiplexer that directs CRT and LCD pixel data received from lookup table, based on value of corresponding data tag

Patent Assignee: CIRRUS LOGIC INC (CIRR-N)

Inventor: BINDLISH R; BRIL V; EGLIT A J; FUIKS K; HAN R S; KOTHA S

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6118413	A	20000912	US 95483584	A	19950607	200056 B
			US 98136791	A	19980819	

Priority Applications (No Type Date): US 95483584 A 19950607; US 98136791 A 19980819

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6118413	A	14	G09G-005/00	Div ex application US 95483584 Div ex patent US 5841418

Abstract (Basic): US 6118413 A

NOVELTY - A lookup table receives CRT and LCD pixel data and corresponding data tags from CRT/LCD video data path. A multiplexer receives the CRT and LCD pixel data output from lookup table and directs the data based on the value of corresponding data tag.

DETAILED DESCRIPTION - A CRT/LCD **display data** pipeline is coupled to display memory for processing CRT/LCD **display data**. A CRT/LCD FIFO (621) receives and stores the CRT and LCD data from memory along with a data tag. The CRT/LCD video data path decompresses compressed portion of CRT/LCD data and outputs respective pixel data along with corresponding data tag.

USE - For multimedia methodology and in graphical user interface.

ADVANTAGE - Enables producing different images on different displays using a single **video controller**. Produces independent images having differing pixel resolutions and/or refresh rates on different video displays. Enables utilizing available bandwidth from wide DRAMs by providing two **separate** data paths in signal **video controller** for two video displays.

DESCRIPTION OF DRAWING(S) - The figure shows the block diagram of **video controller**.

CRT/LCD FIFO (621)

pp; 14 DwgNo 6/6

Title Terms: VIDEO; CONTROL; INTEGRATE; CIRCUIT; COMPUTER; DISPLAY;

MULTIPLEX; DIRECT; CRT; LCD; PIXEL; DATA; RECEIVE; TABLE; BASED; VALUE; CORRESPOND; DATA; TAG

Derwent Class: P85; T01; T04

International Patent Class (Main): G09G-005/00

File Segment: EPI; EngPI

12/5/7 (Item 6 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012897265 **Image available**

WPI Acc No: 2000-069100/200006

XRPX Acc No: N02-291074

Video data displaying method for testing video board , involves dividing total number of colors expressed by each color into preset gradations and selecting intermediate color to detect intermediate value of each color

Patent Assignee: SAMSUNG ELECTRONICS CO LTD (SMSU)

Inventor: JEON Y; JEON Y I

Number of Countries: 003 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
KR 98082202	A	19981205	KR 9716976	A	19970502	200006 B
US 6340988	B1	20020122	US 9871539	A	19980504	200241
TW 370761	A	19990921	TW 98106249	A	19980423	200036
KR 222985	B1	19991001	KR 9716976	A	19970502	200108

Priority Applications (No Type Date): KR 9716976 A 19970502

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
KR 98082202	A			H05K-013/08	
US 6340988	B1	14		H04N-017/00	
TW 370761	A			H04N-017/00	
KR 222985	B1			H05K-013/08	

Abstract (Basic): US 6340988 B1

NOVELTY - Total number of colors expressed by red, green and blue colors are **divided** into 256 gradations and intermediate colors with respect to colors which are **divided** into 256 gradations are selected, so as to detect intermediate value of each color. The detected intermediate value is then displayed.

DETAILED DESCRIPTION - An INDEPENDENT CLAIM is also included for apparatus to **display video data** for testing a **video board** of a

computer.

USE - For testing **video board** of computer.

ADVANTAGE - Allows a target image data displayed on a screen to have a predetermined regularity. Simplifies the test process and easily determines whether a product is a normal one or not and controls an error limitation.

DESCRIPTION OF DRAWING(S) - The figure shows a block diagram of an apparatus for testing a **video board**.

pp; 14 DwgNo 1/7

Title Terms: VIDEO; DATA; DISPLAY; METHOD; TEST; VIDEO; BOARD; **DIVIDE** ;

TOTAL; NUMBER; COLOUR; EXPRESS; COLOUR; PRESET; GRADATION; SELECT;

INTERMEDIATE; COLOUR; DETECT; INTERMEDIATE; VALUE; COLOUR

Derwent Class: S01; T01; T04; W02; W03; W04

International Patent Class (Main): H04N-017/00; H05K-013/08

International Patent Class (Additional): H04N-003/27

File Segment: EPI

12/5/8 (Item 7 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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012637062 **Image available**

WPI Acc No: 1999-443166/199937

XRPX Acc No: N99-330441

Display memory management system for computer system

Patent Assignee: APPLE COMPUTER INC (APPY)

Inventor: BAILEY R L; HOWARD B D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5929868	A	19990727	US 96722543	A	19960927	199937 B

Priority Applications (No Type Date): US 96722543 A 19960927

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5929868	A	13	G09G-005/00	

Abstract (Basic): US 5929868 A

NOVELTY - Logical frame buffers having different bits per pixel transmits in repeating pattern, the stored image information in sequence of T-bit data words. A memory (340) stores image information of logical frame buffers and forwards it to display controller (330) which transfers received information to display unit (335).

DETAILED DESCRIPTION - The image information is transmitted from logical buffer to memory and to **display** controller via **data** bus (310,325) An INDEPENDENT CLAIM is also included for expansion card for display control.

USE - For computer system.

ADVANTAGE - Image data from two logical frame buffers are distributed in physical memory such that stream of data from buffers arrive correctly, when accessed sequentially by **video display controller**. Addressing of memory system on data read and write are manipulated such that two frame buffers are made to appear individually **separate** and distinct to software.

DESCRIPTION OF DRAWING(S) - The figure shows the functional diagram of partial computer system.

Data bus (310,325)

Display controller (330)

Display unit (335)

Memory (340)

pp; 13 DwgNo 3/7

Title Terms: DISPLAY; MEMORY; MANAGEMENT; SYSTEM; COMPUTER; SYSTEM

Derwent Class: P85; T01

International Patent Class (Main): **G09G-005/00**

File Segment: EPI; EngPI

12/5/9 (Item 8 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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012605398 **Image available**
WPI Acc No: 1999-411502/199935
XRPX Acc No: N99-307863

Digital video data transmission controller of computer graphic subsystem - has display timing generator at reception side, which generates various control signals for regulating operation of display device, based on required expanded video data acquired from buffer

Patent Assignee: NEC CORP (NIDE)
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 11164304	A	19990618	JP 97341969	A	19971128	199935 B
JP 3013826	B2	20000228	JP 97341969	A	19971128	200015

Priority Applications (No Type Date): JP 97341969 A 19971128

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 11164304	A	5	H04N-007/24	
JP 3013826	B2	4	H04N-007/24	Previous Publ. patent JP 11164304

Abstract (Basic): JP 11164304 A

NOVELTY - A digital data expansion circuit (4) expands the compressed video data received through a cable (3). The expanded video data are temporarily stored in a buffer (5). Required data are **extracted** from the buffer, based on which a display timing generator (6) generates various control signals for controlling the operation of display device at the receiving side. DETAILED DESCRIPTION - A data compression circuit (1) at the transmission side, performs compression of video data and the corresponding **display timing data**. The compressed data are temporarily stored in a buffer (2) and then transmitted through a cable (3).

USE - For controlling transmission of digital video data in computer graphic subsystem.

ADVANTAGE - Avoids need for changing design of subsystem, even if specification of display device is varied. Since video data are compressed and forwarded through a cable, simultaneous transmission of other data is enabled. DESCRIPTION OF DRAWING(S) - The figure shows block diagram of digital **video data transmission controller**. (1) Data compression circuit; (2) Buffer; (3) Cable; (4) Digital data expansion circuit; (5) Reception side buffer; (6) Display timing generator.

Dwg.1/1

Title Terms: DIGITAL; VIDEO; DATA; TRANSMISSION; CONTROL; COMPUTER; GRAPHIC ; SUBSYSTEM; DISPLAY; TIME; GENERATOR; RECEPTION; SIDE; GENERATE; VARIOUS ; CONTROL; SIGNAL; REGULATE; OPERATE; DISPLAY; DEVICE; BASED; REQUIRE; EXPAND; VIDEO; DATA; ACQUIRE; BUFFER

Derwent Class: P85; W02; W04

International Patent Class (Main): H04N-007/24

International Patent Class (Additional): **G09G-005/00** ; H04N-005/073; H04N-005/92

File Segment: EPI; EngPI

12/5/10 (Item 9 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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012474456 **Image available**
WPI Acc No: 1999-280564/199927
XRPX Acc No: N99-210438

Remote control method for color monitor

Patent Assignee: DAEWOO ELECTRONICS CO LTD (DAEW-N)

Inventor: KIM Y M

Number of Countries: 002 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2331902	A	19990602	GB 9818631	A	19980826	199927 B
KR 99043422	A	19990615	KR 9764423	A	19971129	200036
KR 257547	B1	20000601	KR 9764423	A	19971129	200130

Priority Applications (No Type Date): KR 9764423 A 19971129

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2331902	A		16	G09G-001/16	
KR 99043422	A			G06F-003/153	
KR 257547	B1			G06F-003/153	

Abstract (Basic): GB 2331902 A

NOVELTY - Control data is transmitted through video signal lines from the host computer. A serial data packet indicating the user's command and data to control the display, are inserted into blanking periods of the R,G,B video signals.

DETAILED DESCRIPTION - An N-bit serial data packet is prepared in response to the user command and/or data to control display characteristics of the monitor. Each bit of the serial data packet is inserted into the R,G,B video signals by varying the synchronizing signals' amplitude in the blanking periods according to the logic level of each bit of the serial data packet. The signals are transmitted with horizontal and vertical synchronization signals to the monitor via a video cable with **video adapter** on the monitor.

Each bit of the serial data packet is **extracted** from the R,G,B video signals and display characteristics are controlled according to the serial data packet

USE - For control of brightness, contrast, picture position, size and manage power supply of monitor in general purpose computer system.

ADVANTAGE - No need of additional communication channels between host computer and monitor.

DESCRIPTION OF DRAWING(S) - Diagram **showing control data** inserted in R, G and B video signals.

pp; 16 DwgNo 2/2

Title Terms: REMOTE; CONTROL; METHOD; MONITOR

Derwent Class: P85; T01; T04

International Patent Class (Main): **G06F-003/153 ; G09G-001/16**

File Segment: EPI; EngPI

12/5/11 (Item 10 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012228193 **Image available**

WPI Acc No: 1999-034300/199903

Related WPI Acc No: 2000-593670

XRPX Acc No: N99-025644

Dual display video controller IC for GUI - has LCD and CRT display data clocks with frequency characteristics corresponding to respective refresh rate and pixel resolution of LCD and CRT display, mutually different from each other

Patent Assignee: CIRRUSS LOGIC INC (CIRR-N)

Inventor: BINDLISH R; BRIL V; EGLIT A J; FUIKS K; HAN R S; KOTHA S

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5841418	A	19981124	US 95483584	A	19950607	199903 B

Priority Applications (No Type Date): US 95483584 A 19950607

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5841418	A		15	G09G-005/00	

Abstract (Basic): US 5841418 A

The IC is connected with a single display memory (220) containing CRT and LCD **display data**, through a **display memory data bus** (230). A CRT **display data** clock has a frequency characteristic corresponding to refresh rate and pixel resolution of a CRT display. An LCD **display data** clock has a frequency characteristics corresponding to refresh rate and pixel resolution of LCD display, which is independent of CRT display. A CRT **display data** pipeline is coupled to the **display memory data bus** and CRT **display data** clock, for processing the CRT **display data** synchronising with CRT display clock. The CRT **display data** pipeline has a CRT FIFO (221) which receives and temporarily stores CRT **display data** from display memory, during CRT data read cycle.

The stored CRT **display data** is output to a CRT video data path (222), where the compressed portions of **display data** are decompressed and a CRT pixel data is output. A look up table receives the CRT pixel data and outputs it along with its corresponding address. A D/A converter converts the CRT pixel data and outputs an analog CRT display signal. An LCD **display data** pipeline is coupled to **display memory data bus** and LCD **display data** clock, for processing LCD **display data**. The LCD **display data** pipeline has an LCD FIFO (231) which temporarily stores LCD **display data** from display memory during LCD data read cycle. The LCD **display data** is decompressed along an LCD video data path (232) thereby obtaining an LCD pixel data. The LCD pixel data is output from a look-up table, along with its corresponding address.

ADVANTAGE - Controls more than one video display with single **video controller**. Produces different images on different displays using single **video controller**. Provides an easy to use software model for writing video information for different images into video memory. Utilizes available band width from wide DRAMS by providing two **separate** data paths in a single **video controller** for two video displays.

Dwg.2/6

Title Terms: DUAL; DISPLAY; VIDEO; CONTROL; IC; LCD; CRT; DISPLAY; DATA; CLOCK; FREQUENCY; CHARACTERISTIC; CORRESPOND; RESPECTIVE; REFRESH; RATE; PIXEL; RESOLUTION; LCD; CRT; DISPLAY; MUTUAL
Derwent Class: P85; T01; T04; W03
International Patent Class (Main): G09G-005/00
File Segment: EPI; EngPI

12/5/12 (Item 11 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011133829 **Image available**

WPI Acc No: 1997-111753/199711

XRPX Acc No: N97-092488

Electronic register appts for restaurant - has kitchen video controller that enables display data to be sequentially indicated on displays in indicated picture formats designated by setting data

Patent Assignee: MATSUSHITA ELECTRIC IND CO LTD (MATU); MATSUSHITA DENKI SANGYO KK (MATU)

Inventor: FUYAMA S

Number of Countries: 004 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
GB 2303477	A	19970219	GB 9612360	A	19960613	199711 B
JP 9035147	A	19970207	JP 95182406	A	19950719	199716
CA 2179004	A	19970120	CA 2179004	A	19960612	199721
GB 2303477	B	19970709	GB 9612360	A	19960613	199730
US 6049780	A	20000411	US 96662031	A	19960612	200025
CA 2179004	C	20010220	CA 2179004	A	19960612	200113
JP 3384517	B2	20030310	JP 95182406	A	19950719	200321

Priority Applications (No Type Date): JP 95182406 A 19950119

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
GB 2303477	A		299	G06F-017/60	
JP 9035147	A		71	G07G-001/14	
CA 2179004	A			G06F-003/02	
GB 2303477	B			G06F-017/60	
US 6049780	A			G06F-015/24	
CA 2179004	C	E		G06F-003/02	
JP 3384517	B2		66	G07G-001/12	Previous Publ. patent JP 9035147

Abstract (Basic): GB 2303477 A

The appts includes a register terminal (11) and a kitchen **video controller** (12) with display (13,15) are connected to enable data communication. Setting data representative of display control functions and indicated picture formats is transmitted from the register terminal to the kitchen controller to set display control functions.

The register terminal searches a customers order which is registered in the register terminals and transmits **display data** representative of the registered product items to the kitchen **video controller**. The kitchen **video controller** enables the **display data** to be sequentially indicated on the displays in indicated picture formats designated by the setting data.

ADVANTAGE - Provides improved electronic register that provides management report that customer service time.

Dwg.1/156

Title Terms: ELECTRONIC; REGISTER; APPARATUS; RESTAURANT; KITCHEN; VIDEO; CONTROL; ENABLE; DISPLAY; DATA; SEQUENCE; INDICATE; DISPLAY; INDICATE; PICTURE; FORMAT; DESIGNATED; SET; DATA

Derwent Class: T01; T05

International Patent Class (Main): G06F-003/02 ; G06F-015/24 ; G06F-017/60 ; G07G-001/12; G07G-001/14

International Patent Class (Additional): G06F-153-00 ; G07C-011/00; H04N-007/14; H04N-007/18; H04Q-001/00

File Segment: EPI

12/5/13 (Item 12 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011036615 **Image available**

WPI Acc No: 1997-014539/199702

XRPX Acc No: N97-012568

Process control system esp. for workstations which carry out operations in batch sequences - has preprocessor that automatically retrieves data from records within databank and generates and stores flag file which is then fed to command signal generator which is connected to automatic workstation

Patent Assignee: WOODCHESTER SYSTEMS LTD (WOOD-N)

Inventor: FLEMING R

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
IE 69570	B	19961002	IE 9432	A	19940117	199702 B

Priority Applications (No Type Date): IE 9432 A 19940117

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
IE 69570	B		20	G06F-009/44	

Abstract (Basic): IE 69570 B

The process control system comprises at least one automatic workstation (25) which is driven by control signals generated from a sequence of data records. A **video controller** is connected to a video screen for outputting data according to **viewing templates** for **data** records. Data files are held in a databank and each file

comprises several records. Records of different files are linked in hardware by interconnected data and address buses. The records are linked in software by a microprocessor which identifies relational fields within the records.

The primary allocation processor receives filtered data records and generates an **extraction** file for a second workstation (14). The records which are not written to the **extraction** file are transmitted to a secondary allocation processor which creates a secondary **extraction** file. The secondary **extraction** file contains records which are grouped into a current and future dataset and is connected to the **video controller** and the retrieval processor.

Records within the databank are updated by using an update circuit which receives interactive inputs from the workstations.

USE/ADVANTAGE - E.g. automatic moulding of casings for electronic devices. Reduces risk of data corruption. Ensures that failure of one workstation does not cause disruption of other workstations, by isolating command instructions for different workstations. Fast response when command instructions are requested for activation of particular workstation. Efficiently carries out data processing for generation of command instructions.

Dwg.1/4

Title Terms: PROCESS; CONTROL; SYSTEM; CARRY; OPERATE; BATCH; SEQUENCE; AUTOMATIC; RETRIEVAL; DATA; RECORD; GENERATE; STORAGE; FLAG; FILE; FEED; COMMAND; SIGNAL; GENERATOR; CONNECT; AUTOMATIC

Derwent Class: T01

International Patent Class (Main): G06F-009/44

File Segment: EPI

12/5/14 (Item 13 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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011000127 **Image available**

WPI Acc No: 1996-497076/199649

Related WPI Acc No: 1995-223176

XRPX Acc No: N96-419233

Circuitry for cursor generation in colour graphics display - incorporated into graphics sub-system having frame memory and video DAC with colour palette RAM to generate display data

Patent Assignee: COMPAQ COMPUTER CORP (COPQ)

Inventor: ALBERS T M; PRESTON S B; WOOD P B

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5570107	A	19961029	US 91696355	A	19910506	199649 B
			US 9326207	A	19930302	
			US 95388331	A	19950214	

Priority Applications (No Type Date): US 91696355 A 19910506; US 9326207 A 19930302; US 95388331 A 19950214

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5570107	A		12	G09G-001/28	Cont of application US 91696355 Cont of application US 9326207 Cont of patent US 5389947

Abstract (Basic): US 5570107 A

The circuitry has a high speed oscillator to generate a pixel clock signal (PCLK) at the frequency at which pixels are to be displayed. The video DAC (30) has a frequency **divider** (38) providing an output clock signal of a period which is a multiple of the pixel clock signal. The multiple corresponds to the level of multiplexing of pixel data to be provided by the video DAC. The **video controller** receives the output clock signal (OUTCLK). It generates clock signals to control the serial port of the frame memory and to control the latching of the first stage in the video DAC.

The first stage latch (32) in the video DAC latch in the multiple pixel data from the frame memory. The multiplexer in the video DAC **presents** the **data** to the colour palette RAM (48) or around the colour palette RAM in true-colour non-multiplexed mode, according to the pixel clock signal. The cursor highlighted pixels are displayed by inverting the output of the colour palette RAM at cursor locations.

USE/ADVANTAGE - For high resolution display. Provides cursor image with contrast with surroundings, used in conjunction with colour palette RAM.

Dwg.1/3

Title Terms: CIRCUIT; CURSOR; GENERATE; COLOUR; GRAPHIC; DISPLAY;
INCORPORATE; GRAPHIC; SUB; SYSTEM; FRAME; MEMORY; VIDEO; DAC; COLOUR;
PALLET; RAM; GENERATE; DISPLAY; DATA
Derwent Class: P85; T01
International Patent Class (Main): G09G-001/28
File Segment: EPI; EngPI

12/5/15 (Item 14 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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010608422 **Image available**

WPI Acc No: 1996-105375/199611

Related WPI Acc No: 1994-074554; 1998-446389

XRPX Acc No: N96-088325

Graphics subsystem including video digital-to-analog converter - has high speed oscillator generating pixel clock signal at frequency at which pixels are to be displayed, with frequency divider outputting clock signal which is multiple of pixel clock signal

Patent Assignee: COMPAQ COMPUTER CORP (COPQ)

Inventor: BOUNDS B F; WOOD P B

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5488393	A	19960130	US 91695963	A	19910506	199611 B
			US 9381794	A	19930623	

Priority Applications (No Type Date): US 91695963 A 19910506; US 9381794 A 19930623

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5488393	A		15	G09G-005/06	Cont of application US 91695963 Cont of patent US 5291187

Abstract (Basic): US 5488393 A

The computer system for processing data comprising video data, has a processor, and a **video controller**. A first bus is coupled between the processor and the **video controller**, and a memory stores video data. A second bus is coupled between the **video controller** and the memory, and the **video controller** is operable to provide the video data to the memory. A video display driver circuit for driving a display device, has a first clock terminal for receiving a pixel clock signal. A second clock terminal receives a latch clock signal and an output clock terminal. A frequency **divider** circuit receives the pixel clock signal from the first clock terminal, and presents at the output clock terminal an output clock signal having a period which is a multiple of the period of the pixel clock signal. A number of data terminals receives the pixel data from the memory.

A latch, coupled to the data terminals and to the second clock terminal, stores pixel data received at the data terminals in response to the latch clock signal. A multiplexer has a data input coupled to the latch for receiving the pixel data, a clock input for receiving the pixel clock signal, and an output. The multiplexer applies a selected portion of the pixel data to its output in response to the pixel clock signal. Output circuitry coupled to the output of the multiplexer, **presents** the pixel **data** to the display device.

ADVANTAGE - Pixel clock rate is not dependent on propagation delay of output clock signal through **video controller**, and has higher speed system operation.

Dwg.3/7

Title Terms: GRAPHIC; SUBSYSTEM; VIDEO; DIGITAL-ANALOGUE; CONVERTER; HIGH; SPEED; OSCILLATOR; GENERATE; PIXEL; CLOCK; SIGNAL; FREQUENCY; PIXEL; DISPLAY; FREQUENCY; **DIVIDE**; OUTPUT; CLOCK; SIGNAL; MULTIPLE; PIXEL; CLOCK; SIGNAL

Derwent Class: P85; T04; U21

International Patent Class (Main): **G09G-005/06**

File Segment: EPI; EngPI

12/5/16 (Item 15 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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009794701 **Image available**

WPI Acc No: 1994-074554/199409

Related WPI Acc No: 1996-105375; 1998-446389

XRPX Acc No: N94-058233

High-speed visual display system - has first stage latch in video D-A converter which latches in multiple pixel data from frame memory, and multiplexer which presents data to colour palette RAM

Patent Assignee: COMPAQ COMPUTER CORP (COPQ)

Inventor: BOUNDS B F; WOOD P B

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5291187	A	19940301	US 91695963	A	19910506	199409 B

Priority Applications (No Type Date): US 91695963 A 19910506

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5291187	A		15	G09G-001/02	

Abstract (Basic): US 5291187 A

A graphics subsystem, includes a video digital-to-analog converter (VDAC). A high speed oscillator generates a pixel clock signal at the frequency at which pixels are to be displayed. Included in the video DAC is a frequency **divider** which presents an output clock signal having a period which is a multiple of the pixel clock signal. The multiple corresponds to the level of multiplexing of pixel data to be provided by the video DAC; this multiple can equal unity.

The **video controller** in the system receives the output clock signal, and generates clock signals to control the serial port of the frame memory, and also to control the latching of the first stage in the video DAC. The first stage latch in the video DAC latches in the multiple pixel data from the frame memory, and the multiplexer in the video DAC **presents** the **data** to the colour palette RAM, or around the colour palette RAM in true-colour non-multiplexed mode, according to the pixel clock signal. As a result, the pixel clock rate is not dependent by the propagation delay of the output clock signal through the **video controller**, and higher speed system operation is achieved.

ADVANTAGE - Provides system capable of handling high data rates compatible with high resolution display monitors. Allows for extremely wide pixel data words, necessary for true colour display.

Dwg.3/6

Title Terms: HIGH; SPEED; VISUAL; DISPLAY; SYSTEM; FIRST; STAGE; LATCH; VIDEO; DIGITAL-ANALOGUE; CONVERTER; LATCH; MULTIPLE; PIXEL; DATA; FRAME; MEMORY; MULTIPLEX; PRESENT; DATA; COLOUR; PALLET; RAM

Derwent Class: P85; T04; U21

International Patent Class (Main): **G09G-001/02**

File Segment: EPI; EngPI

12/5/17 (Item 16 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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009205950 **Image available**
WPI Acc No: 1992-333371/199241
Related WPI Acc No: 1993-361218; 1996-400993
XRPX Acc No: N92-254470

**LCD addressing system esp. for LCD displays - limits peak voltage levels
across pixels to improve bright and dark states and to increase video
update rate**

Patent Assignee: IN FOCUS SYSTEMS INC (INFO-N); FOCUS SYSTEMS KK (FOCU-N)
Inventor: CLIFTON B R; SCHEFFER T J
Number of Countries: 022 Number of Patents: 016
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 507061	A2	19921007	EP 92102353	A	19920212	199241 B
AU 9210758	A	19921008	AU 9210758	A	19920205	199248
CA 2060735	A	19921002	CA 2060735	A	19920206	199251
TW 209914	A	19930721	TW 92101944	A	19920314	199344
EP 507061	A3	19930602	EP 92102353	A	19920212	199404
AU 646140	B	19940210	AU 9210758	A	19920205	199411
US 5420604	A	19950530	US 91678736	A	19910401	199527
			US 9358316	A	19930503	
US 5485173	A	19960116	US 91678736	A	19910401	199609
US 5546102	A	19960813	US 91678736	A	19910401	199638
			US 95484433	A	19950607	
US 5585816	A	19961217	US 91678736	A	19910401	199705
			US 95468549	A	19950606	
EP 507061	B1	19970827	EP 92102353	A	19920212	199739
DE 69221759	E	19971002	DE 621759	A	19920212	199745
			EP 92102353	A	19920212	
US 5852429	A	19981222	US 91678736	A	19910401	199907
			US 95468549	A	19950606	
			US 96684433	A	19960719	
KR 9603440	B1	19960313	KR 925477	A	19920401	199911
CA 2060735	C	19990413	CA 2060735	A	19920206	199933
JP 2001092428	A	20010406	JP 9279847	A	19920401	200126
			JP 2000241126	A	19920401	

Priority Applications (No Type Date): US 91678736 A 19910401; US 9358316 A 19930503; US 95484433 A 19950607; US 95468549 A 19950606; US 96684433 A 19960719

Cited Patents: No-SR.Pub; CH 645473; GB 2002562; GB 2204174

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 507061	A2	E	50	G09G-003/36	
					Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LI LU MC NL PT SE
AU 9210758	A			G09G-003/18	
CA 2060735	A			G09G-003/36	
TW 209914	A			H01L-033/00	
EP 507061	A3			G09G-003/36	
AU 646140	B			G09G-003/18	Previous Publ. patent AU 9210758
US 5420604	A		42	G09G-003/36	Cont of application US 91678736
US 5485173	A		40	G09G-003/36	
US 5546102	A		46	G09G-003/36	Div ex application US 91678736
US 5585816	A		39	G09G-003/36	Div ex application US 91678736
EP 507061	B1	E	49	G09G-003/36	
					Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LI LU MC NL PT SE
DE 69221759	E			G09G-003/36	Based on patent EP 507061
US 5852429	A			G09G-003/36	Div ex application US 91678736 Div ex application US 95468549 Div ex patent US 5485173 Div ex patent US 5585816
KR 9603440	B1			G09G-003/36	

Abstract (Basic): EP 507061 A

The apparatus has a set of LCD electrodes which are continuously driven with signals, each comprising a train of pulses. The pulses are periodic in time, have a common period T, are independent of the information to be displayed and are preferably orthonormal. A number of column signals are generated from the collective information state of the pixels defined by the overlap with a second electrode pattern.

Hardware implementation comprises an external **video** source, a **controller** that receives the formats video data and timing information, a storage device, a row signal generator, a column signal generator and at least one LCD panel.

ADVANTAGE - Provides bright, high contrast, high information content and video rate display that is also free of alignment instabilities.

Dwg.12/25

Title Terms: LCD; ADDRESS; SYSTEM; LCD; DISPLAY; LIMIT; PEAK; VOLTAGE;

LEVEL; PIXEL; IMPROVE; BRIGHT; DARK; STATE; INCREASE; VIDEO; UPDATE; RATE

Derwent Class: P81; P85; T04

International Patent Class (Main): G09G-003/18 ; G09G-003/36 ;

H01L-033/00

International Patent Class (Additional): G02B-006/42; G02F-001/133;

G09G-003/20

File Segment: EPI; EngPI

12/5/18 (Item 17 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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008859304 **Image available**

WPI Acc No: 1991-363328/199150

XRPX Acc No: N91-278314

Multi-media interface for computer system - includes two displays and data processing units, and operates in parallel with another application software module such as an expert system

Patent Assignee: ELECTRIC POWER RES INST INC (ELPO)

Inventor: BEDROS R; BLOOM C P; BUTLER A W; CAMPOS F M; ISLE B A; QUENTIN G H; SPOOR D; SWEET S R; WUNDERLIN D J

Number of Countries: 014 Number of Patents: 003

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 460867	A	19911211	EP 91304877	A	19910530	199150 B
US 5208745	A	19930504	US 88223499	A	19880725	199319
			US 90532596	A	19900604	
EP 460867	A3	19920610	EP 91304877	A	19910530	199332

Priority Applications (No Type Date): US 90532596 A 19900604; US 88223499 A 19880725

Cited Patents: NoSR.Pub; 3.Jnl.Ref; US 4931950; WO 8905023; WO 9001195

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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EP 460867	A				
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Designated States (Regional): AT BE CH DE ES FR GB GR IT LI LU NL SE

US 5208745	A	26	G06F-015/46	CIP of application US 88223499
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CIP of patent US 4931950

Abstract (Basic): EP 460867 A

The system includes two data processing units that are coupled to two displays. A memory is connected to one data processing unit for storing application software. The application software displays messages on its display and transmits video display instructions. The second data processing unit is connected to another display and receives transmitted video display instructions from the other data processing unit.

A random access video memory is connected to the second data processing unit to store multiple digital video images. A digital **video controller** displays on the second display selected sequences of digitised video images. These are stored in the video memory with the video display instructions that are transmitted by the application software.

USE - Portable multimedia export system delivery vehicle. (34pp
Dwg.No.2/10

Title Terms: MULTI; MEDIUM; INTERFACE; COMPUTER; SYSTEM; TWO; DISPLAY; DATA
; PROCESS; UNIT; OPERATE; PARALLEL; APPLY; SOFTWARE; MODULE; EXPERT;
SYSTEM

Derwent Class: T01

International Patent Class (Main): G06F-015/46

International Patent Class (Additional): G06F-011/30 ; G06F-015/40

File Segment: EPI

12/5/19 (Item 18 from file: 350)

DIALOG(R) File 350:Derwent WPIX

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008548330 **Image available**

WPI Acc No: 1991-052381/199108

XRPX Acc No: N91-040610

Computer for reproducing video data on monitor - has programmable video enabling use of many different types of monitor

Patent Assignee: APPLE COMPUTER INC (APPY)

Inventor: BEILEY R L; HOWARD B D; BAILEY R L

Number of Countries: 004 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 4025295	A	19910214	DE 4025295	A	19900809	199108 B
GB 2235358	A	19910227	GB 9017510	A	19900809	199109
US 5257350	A	19931026	US 89392111	A	19890810	199344
			US 91807611	A	19911213	
GB 2235358	B	19940511	GB 9017510	A	19900809	199416
JP 3096849	B2	20001010	JP 90210539	A	19900810	200052

Priority Applications (No Type Date): US 89392111 A 19890810; US 91807611 A 19911213

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5257350	A		14	G06F-015/62	Cont of application US 89392111
GB 2235358	B		2	G06F-003/153	
JP 3096849	B2		15	G09G-005/00	Previous Publ. patent JP 3148697

Abstract (Basic): DE 4025295 A

A computer with a CPU for running a program for reproducing data on a monitor has a direct access memory (11) for storing the video data. A programmable video circuit (14) provides video time generation signals to the monitor (27) and transfers video data from the memory to the monitor to generate the display.

The monitor sends a signal (35) to the video circuit to make the latter compatible with its requirements.

USE/ADVANTAGE - Esp. microprocessor system for visually reproducing video signals, enables many types of monitor to be used without reconfiguring computer's internal video circuit. (15pp
Dwg.No.2/6)

Title Terms: COMPUTER; REPRODUCE; VIDEO; DATA; MONITOR; PROGRAM; VIDEO;
ENABLE; TYPE; MONITOR

Derwent Class: P85; T01; W03

International Patent Class (Main): G06F-003/153 ; G06F-015/62 ;
G09G-005/00

International Patent Class (Additional): G06F-003/15 ; G06F-015/78 ;
G09G-005/12 ; G09G-005/18

File Segment: EPI; EngPI

12/5/20 (Item 19 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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004635523

WPI Acc No: 1986-138866/198622

Related WPI Acc No: 1992-133835; 1992-142778

XRPX Acc No: N86-102617

**Video system and controller for multi-plane colour memory - has
microprocessor video system controlling data transfer between memory,
display and processor**

Patent Assignee: TEXAS INSTR INC (TEXI)

Inventor: ALBACHTEN R J; BOND J C; GUTTAG K M; MORAVEC J V; NOVAK M;

PINKHAM R; THADEN R C; VANAKEN J; WATTS M W

Number of Countries: 005 Number of Patents: 011

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 182454	A	19860528	EP 85305225	A	19850723	198622 B
US 4654804	A	19870331	US 84633389	A	19840723	198715
US 4656596	A	19870407	US 84633386	A	19840723	198716
US 4656597	A	19870407	US 84633385	A	19840723	198716
US 4660155	A	19870421	US 84633388	A	19840723	198718
US 4660156	A	19870421	US 87633383	A	19870723	198718
US 4665495	A	19870512				198721
US 4691289	A	19870901	US 84633367	A	19840723	198737
EP 182454	B1	19940202	EP 85305225	A	19850723	199405
DE 3587744	G	19940317	DE 3587744	A	19850723	199412
			EP 85305225	A	19850723	
JP 10091136	A	19980410	JP 85160411	A	19850722	199825
			JP 97153659	A	19850722	

Priority Applications (No Type Date): US 84633389 A 19840723; US 84633367 A
19840723; US 84633383 A 19840723; US 84633384 A 19840723; US 84633385 A
19840723; US 84633386 A 19840723; US 84633387 A 19840723; US 84633388 A
19840723; US 87633383 A 19870723

Cited Patents: A3...8812; No-SR.Pub; US 4104624; US 4125873; US 4286320; US
4390780; US 4424572; US 4566005

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

EP 182454 A E 173

Designated States (Regional): DE FR GB

EP 182454 B1 E 142 G06F-003/14

Designated States (Regional): DE FR GB

DE 3587744 G G06F-003/14 Based on patent EP 182454

JP 10091136 A 109 G09G-005/00 Div ex application JP 85160411

Abstract (Basic): EP 182454 A

The **video system controller** (3) acts to allow contention free access of the microprocessor to the system dynamic RAM(19) and to the display memory (5). In addn. the controller generates automatically the refresh cycles required for maintaining stored data. Periodically the controller operates to load new video data to the display memory shift registers. It also provides the video syne signals and blanking signals to the video monitor. A CRT monitor (11) **displays data** passed to it from the microprocessor over the data bus (17) under the supervision of the system controller.

The controller embodies a respective row, column address latch and X,Y address logic and acts to multiplex access requests to the video memory using a printing circuit.

ADVANTAGE - Standard dynamic RAMs can be used to implement variable resolution modes without slowing down operation of central processor.

(173pp Dwg.No 1/40

Title Terms: VIDEO; SYSTEM; CONTROL; MULTI; PLANE; COLOUR; MEMORY;

MICROPROCESSOR; VIDEO; SYSTEM; CONTROL; DATA; TRANSFER; MEMORY; DISPLAY;
PROCESSOR

Derwent Class: P85; T01; T04

International Patent Class (Main): G06F-003/14 ; G09G-003/00
International Patent Class (Additional): G06F-012/00 ; G06F-015/66 ;
G06T-001/60; G09G-001/00 ; G09G-005/18
File Segment: EPI; EngPI

12/5/21 (Item 20 from file: 350)
DIALOG(R) File 350:Derwent WPIX
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004505639

WPI Acc No: 1986-008983/198602

XRPX Acc No: N86-006458

**Video display controller - uses weighting of each bit representing
primary colour data to obtain increased number of black and white
gradations**

Patent Assignee: ASCII CORP (ASCII-N); ASC II CROP (ASCII-N); ASUKI KK
(ASUK-N); NIPPON GAKKI SEIZO KK (NIHG)

Inventor: KAZUHIKO N; RYOZO Y; SHIGEMITSU Y; TAKATOSHI O; TAKTOSHI I

Number of Countries: 006 Number of Patents: 005

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 166966	A	19860108	EP 85106611	A	19850529	198602 B
JP 60254190	A	19851214	JP 84111613	A	19840531	198607
US 4737772	A	19880412	US 85739036	A	19850529	198817
EP 166966	B	19920304				199210
DE 3585463	G	19920409				199216

Priority Applications (No Type Date): JP 84111613 A 19840531

Cited Patents: 1.Jnl.Ref; A3...8924; EP 71745; No-SR.Pub; US 3939487

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 166966	A	E	47		

Designated States (Regional): DE FR GB NL

EP 166966 B

Designated States (Regional): DE FR GB NL

Abstract (Basic): EP 166966 A

The **video display controller** includes: a processor; a video memory; a clock generator synchronised to the display timing; a memory reading system timed by the clock signal; phase generating circuits operating in synchronism with the clock; and an encoder multiplying the image read from memory by coefficients accessed by the phase signals to output data representative of a colour chrominance.

A colour palette circuit converts the chrominance into primary colour data to form an RGB signal. Increased gradation is obtained by selectively **extracting** the bits from each colour register and multiplying by coefficients selected by means of the phase signals. To prepare a composite video signal the colour components are multiplied by predetermined coefficients before combination into an analog signal.

ADVANTAGE - Increased number of gradations of a black and white image without increasing the number of bits used to represent each primary colour

Title Terms: VIDEO; DISPLAY; CONTROL; WEIGHT; BIT; REPRESENT; PRIMARY; COLOUR; DATA; OBTAIN; INCREASE; NUMBER; BLACK; WHITE; GRADATION

Derwent Class: P85; T01; T04

International Patent Class (Additional): G01G-001/28; G09G-001/16 ;
H04N-009/45

File Segment: EPI; EngPI